



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS ON JP1 AND JP2 PIN 3 AND 4; JP3 AND JP4 PIN 1 AND 2.

* CIN1A IS AN OPTIONAL CAPACITOR. IT WAS INSERTED ON THE DC704A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE USE OF LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, CIN1A IS NOT NEEDED.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: TOM G.

DESIGNER:



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TITLE: SCHEMATIC

DUAL OUTPUT LOW NOISE LDO REGULATORS

SIZE
A

DWG NO.

DC704A * LT3023EDD

REV
A

DATE: Monday, January 05, 2004

SHEET 1 OF 1