


NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE 0402.
2. INSTALL SHUNT ON JP1 PIN 1 AND 2.

<p align="center">CUSTOMER NOTICE</p> <p>LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.</p> <p>THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.</p>		CONTRACT NO.	 <p>1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only</p>
		APPROVALS DRAWN: KIM T.	
<p>APPROVED:</p> <p>ENGINEER: CHARLIE Z.</p> <p>DESIGNER:</p>		TITLE:	<p align="center">2-PHASE DUAL STEP-DOWN CONVERTER FOR DDR/QDR MEMORY TERMINATION</p>
<p>DATE: Friday, October 08, 2004</p>		SIZE A	
<p>DATE: Friday, October 08, 2004</p>		DWG NO. DC829A - LTC3776EUF	SHEET 1 OF 1