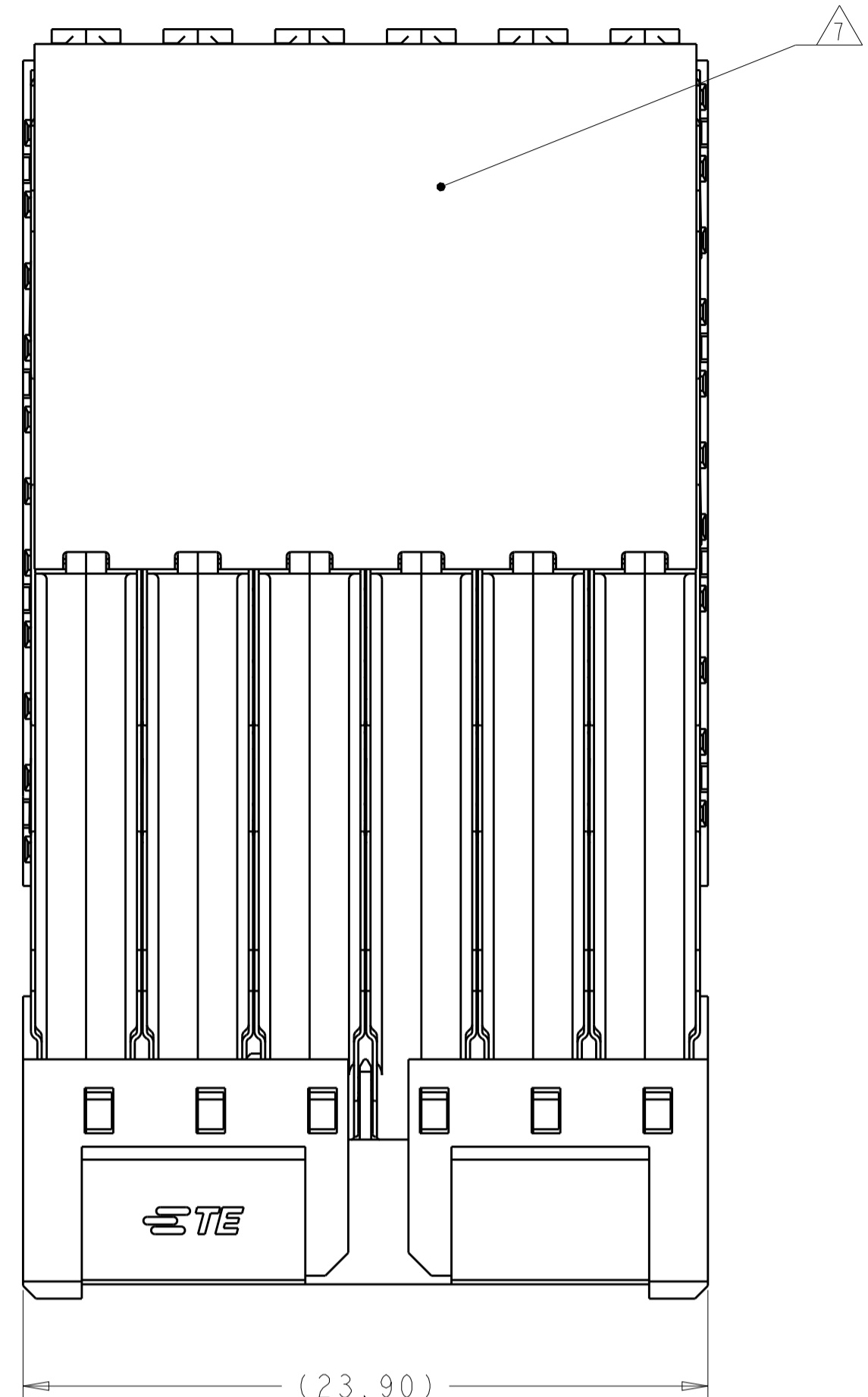
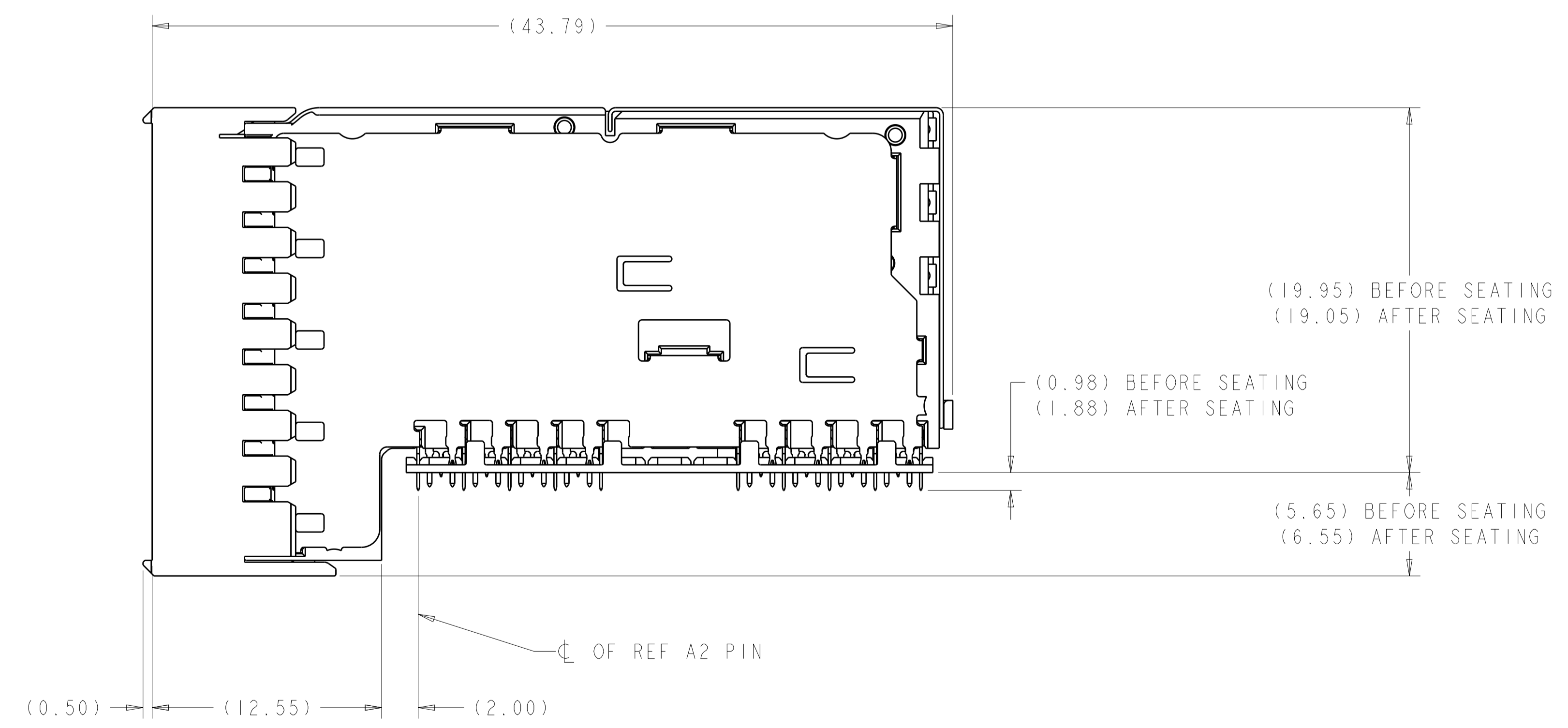
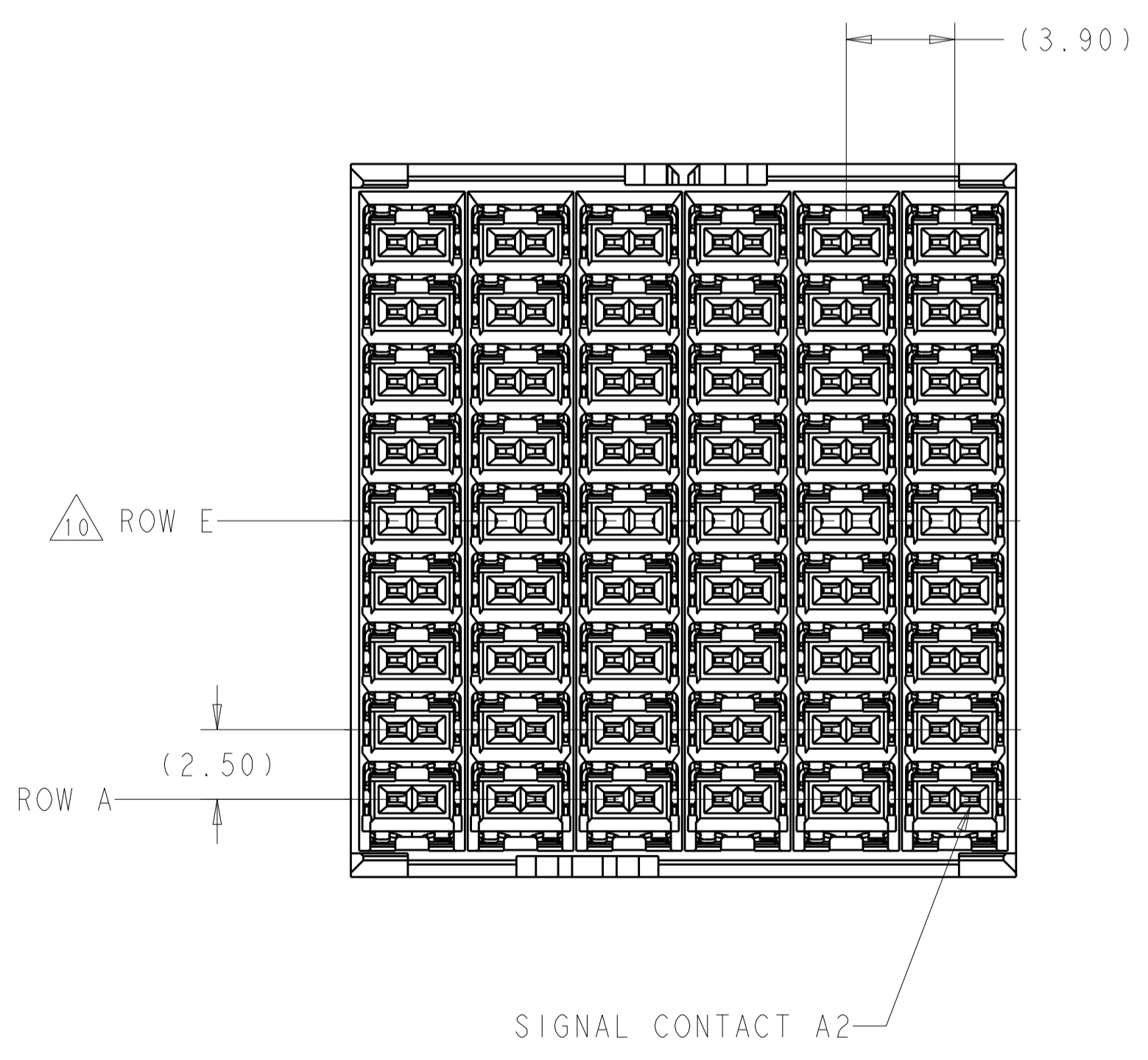


LOC	DIST	REV	DATE	BY	CHK	APPV
-	-	12	08OCT2013	LAW	EW	
		13	11NOV13	EW	EW	
		14	14JAN2014	JKS	EW	
		15	22OCT2014	NE	SF	



- 1 MATERIAL:
 CONTACTS AND SHIELDS - COPPER ALLOY
 ORGANIZER - STAINLESS STEEL
 HOUSING, GROUND SHELLS, AND PIN SPACER - THERMOPLASTIC, UL 94V-0, BLACK
- 2 FINISH:
 - CONFORMS TO THE REQUIREMENTS OF TE PRODUCT SPECIFICATION 108-32021
 BASED ON TELCORDIA GR-1217-CORE FOR SYSTEM QUALITY LEVEL III,
 APPLICATIONS IN CONTROLLED ENVIRONMENTS (CENTRAL OFFICE).
 - COMPLIANT PIN AREA: 0.5µm MIN MATTE TIN.
- 3 GROUND PLATED THRU HOLE REQUIREMENTS:
 FINISHED HOLE = \varnothing 0.34±0.05
 HOLE SIZE PRIOR TO PLATING = \varnothing 0.420±0.025
 COPPER THICKNESS = 0.025 MIN
 IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 4 SIGNAL PLATED THRU HOLE REQUIREMENTS:
 FINISHED HOLE = \varnothing 0.24±0.04
 HOLE SIZE PRIOR TO PLATING = \varnothing 0.320±0.025
 COPPER THICKNESS = 0.025 MIN
 IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 5 SI VIA PLATED THRU HOLE REQUIREMENTS:
 FINISHED HOLE = \varnothing 0.22 REF
 HOLE SIZE PRIOR TO PLATING = \varnothing 0.300±0.025
 COPPER THICKNESS = 0.025 MIN
 IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 6 SI VIAS & SIGNAL HOLES:
 PTH DIMENSIONS APPLY TO THE TOP 1.00mm OF THE PCB THICKNESS FROM THE
 CONNECTOR MOUNTING SIDE.
 GROUND HOLES:
 PTH DIMENSIONS APPLY TO THE TOP 1.25mm OF THE PCB THICKNESS FROM THE
 CONNECTOR MOUNTING SIDE.
- 7 CONNECTOR MARKED WITH PART NUMBER, DATE CODE, AND "100 OHM" IN
 APPROXIMATE AREA SHOWN.
- 8 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER
- 9 APPLIES TO FINISHED HOLE DIAMETER.
- 10 NO SIGNAL CONTACTS IN ROW E.

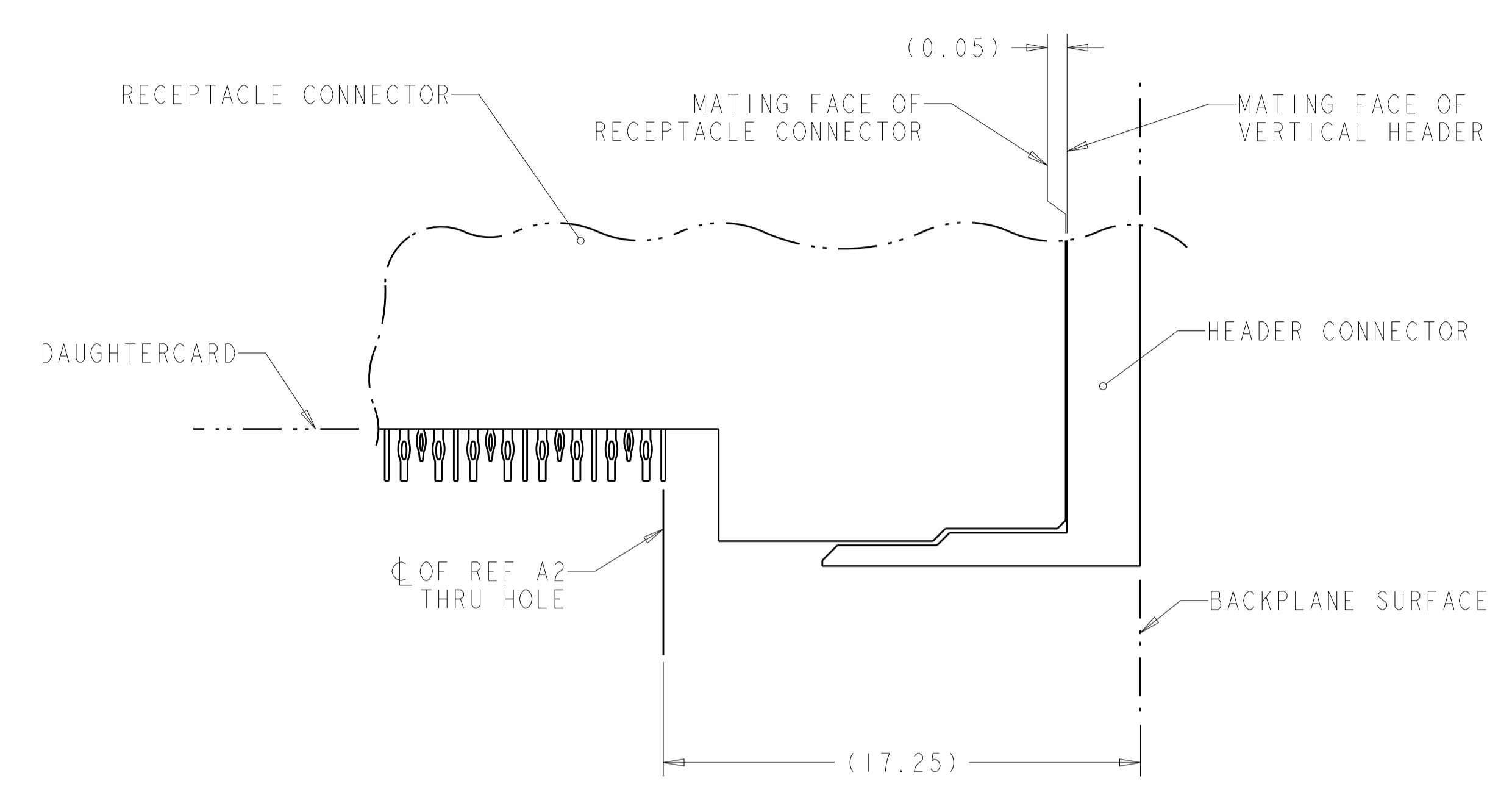
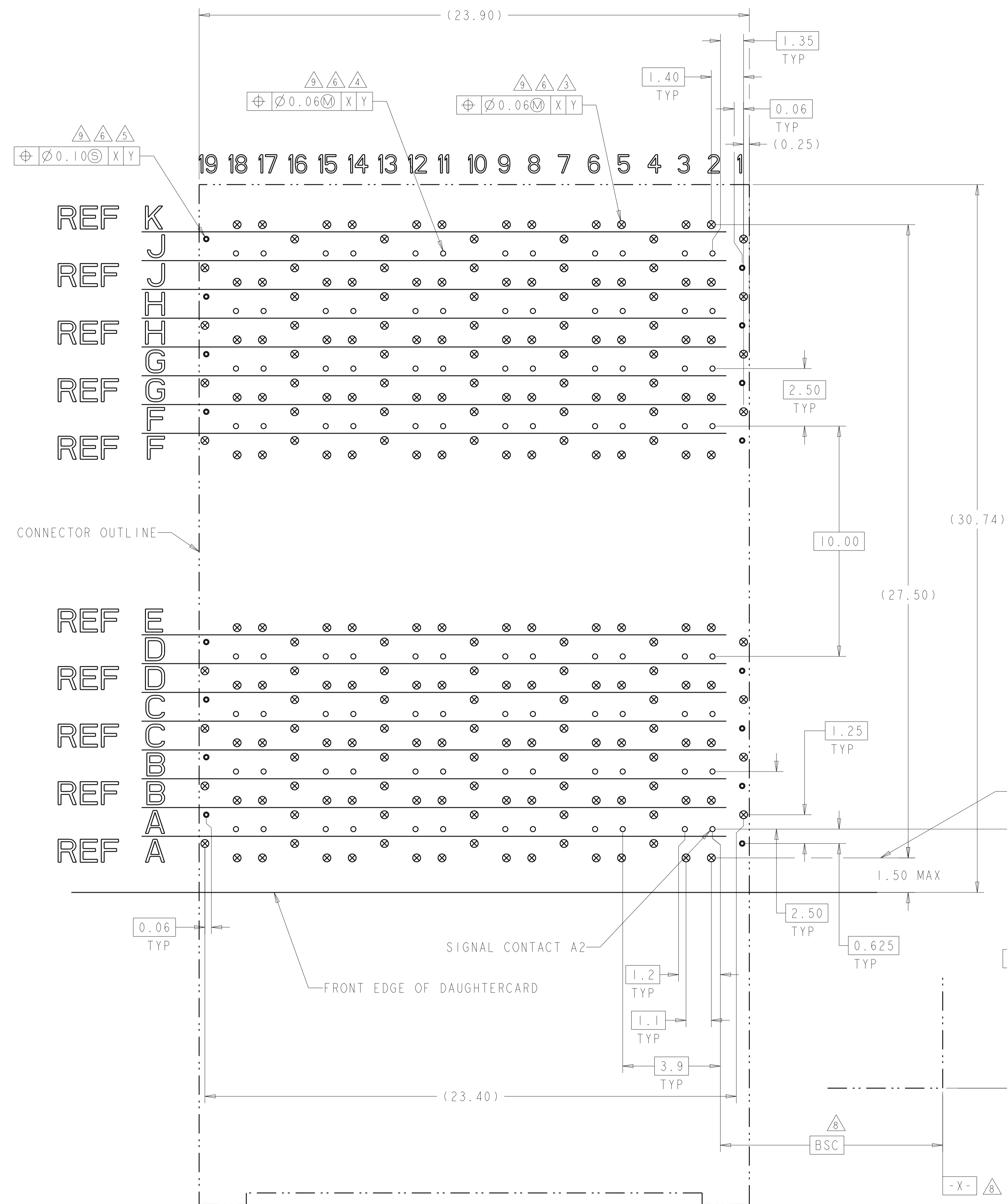


2187194-1
 PART NO

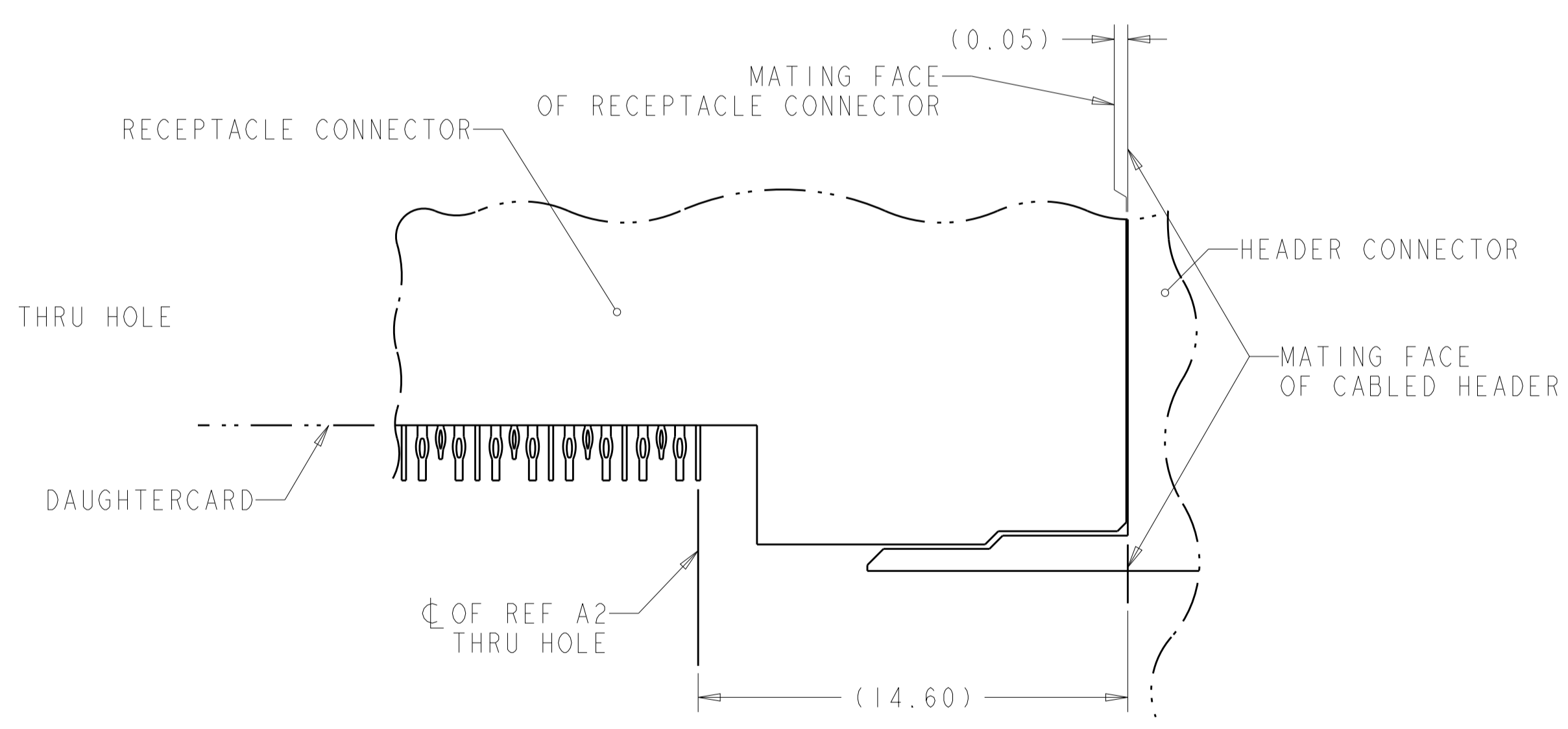
**THIS PRODUCT HAS NOT
 COMPLETED VALIDATION AND
 QUALIFICATION TESTING**

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: S. WELDON 04NOV2011	
DIMENSIONS: mm		CHK: W. DAVIS 04NOV2011	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APPV: S. HELCKINGER 08OCT2013	PRELIMINARY P/A RECEPTACLE ASSEMBLY, 100 OHM 9 PAIR, 6 COLUMN, ROW E SKIPPED, PAIR-IN-ROW, STRADA WHISPER
0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ±		PRODUCT SPEC: 108-32021	
MATERIAL: 1		APPLICATION SPEC: 114-32029	RESTRICTED TO
FINISH: 2		WEIGHT: -	SIZE: A1
		CUSTOMER DRAWING	SCALE: 5:1 SHEET 1 OF 2 REV 15

LOC		DIST		REVISIONS			
				DESCRIPTION	DATE	OWN	APVD
				SEE SHEET 1			



MIDPLANE APPLICATION
MATED ASSEMBLY (PARTIAL VIEW)
SCALE 6:1



CABLED HEADER APPLICATION
MATED ASSEMBLY (PARTIAL VIEW)
SCALE 6:1

○ = SIGNAL
⊗ = GROUND
⊙ = SIGNAL INTEGRITY VIA TO GROUND

RECOMMENDED PCB LAYOUT
SINGLE CONNECTOR (NON-STACKING)
COMPONENT SIDE SHOWN
SCALE 10:1

**THIS PRODUCT HAS NOT
COMPLETED VALIDATION AND
QUALIFICATION TESTING**

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: S. WELDON 04NOV2011	TE Connectivity
DIMENSIONS: mm		CHK: W. DAVIS 04NOV2011	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: S. FLICKINGER 08OCT2013	NAME: R/A RECEPTACLE ASSEMBLY, 100 OHM 9 PAIR, 6 COLUMN, ROW E SKIPPED, PAIR-IN-ROW, STRADA WHISPER
0 PLC ±		PRODUCT SPEC: 108-32021	
1 PLC ±		APPLICATION SPEC: 114-32029	
2 PLC ±		WEIGHT: -	
MATERIAL: -		FINISH: -	SIZE: A1
CUSTOMER DRAWING		SCALE: 1:1	SHEET: 2 OF 2