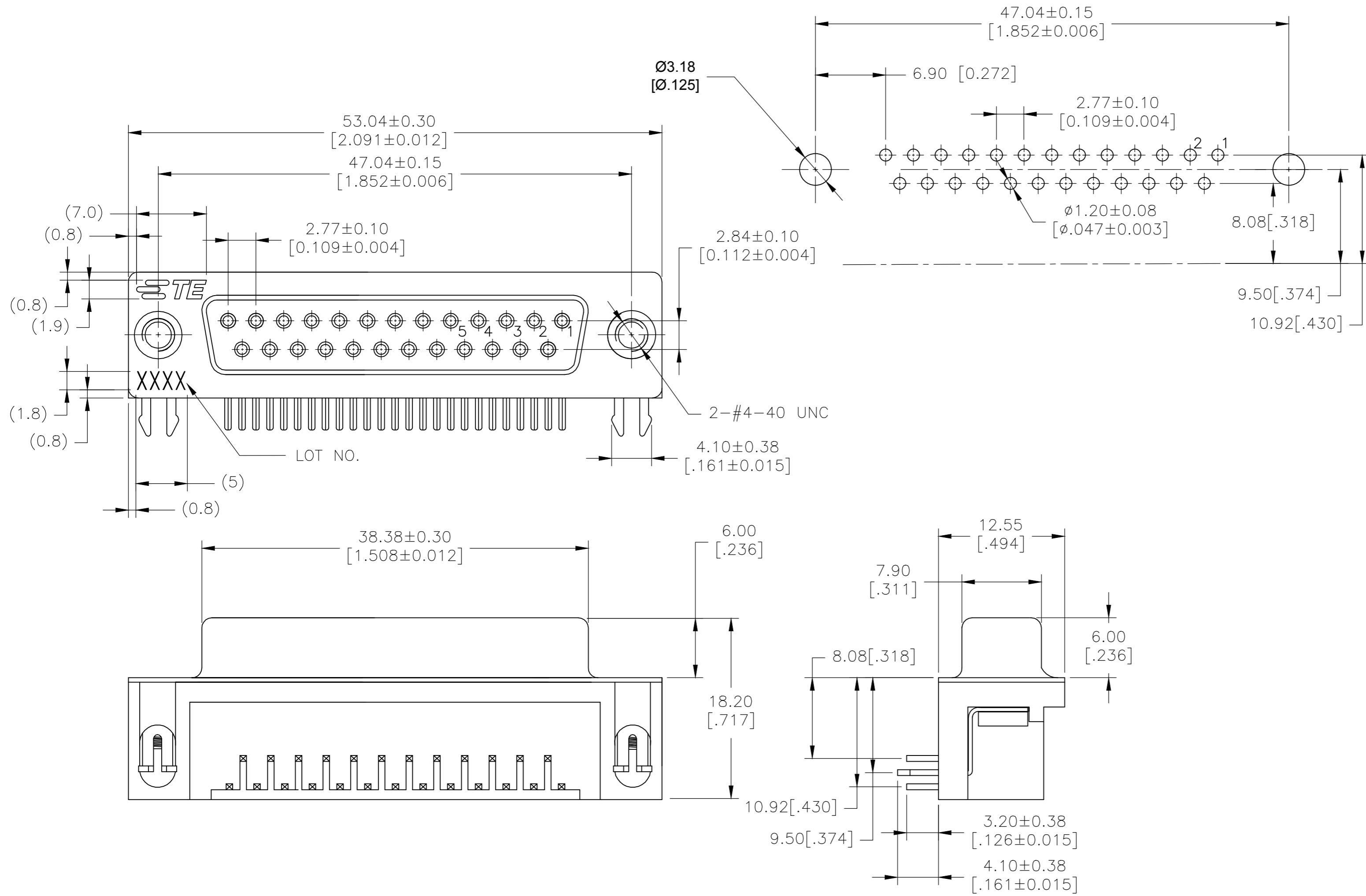


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REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
B		REVISED PER ECO-17-016472	04DEC2017	AP	GD

RECOMMENDED PCB LAYOUT
 TOLERANCE IS ±0.05



NOTES:
 CURRENT RATING:3.0A AMP
 CONTACT RESISTANCE:25MILLIOHM MAX
 WITHSTAND VOLTAGE:500V AC/MINUTE
 INSULATION RESISTANCE:1000MEGOHM MIN
 OPERATION TEMPERATURE:-55°C TO +105°C

HOUSING:PBT+GF(UL94V-0) BLACK
 CONTACT:PHOSPHOR BRONZE
 " GOLD PLATING ON CONTACT AREA
 100u" MATTE TIN PLATING ON SOLDER TAIL
 50u" NICKEL UNDERPLATED OVERALL
 HOLDDOWN:BRASS,100u" TIN OVER 50u" NICKEL
 SHELL:SPCC,NICKEL PLATING OVERALL
 RIVETLOCK:BRASS,NICKEL PLATING OVERALL

LOT NUMBER "XXXX"
 STANDS FOR CALENDAR YEAR
 STANDS FOR WEEK NUMBER

THREADED	Au Flash	2311762-1
TYPE	Contact Area Plating	P/N

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN AJITH PAI 21FEB2017	TE Connectivity	
DIMENSIONS: mm [INCHES]		CHK GORENC, DANIEL L. 21FEB2017	NAME AMPLIMITE RECEPTACLE, HD20, RIGHT ANGLE, 25P THREADED	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD GORENC, DANIEL L. 21FEB2017	DRAWING NO C=2311762	
0 PLC ± -		PRODUCT SPEC	RESTRICTED TO	
1 PLC ± 0.40		APPLICATION SPEC	-	
2 PLC ± 0.25		WEIGHT	A2 00779	
3 PLC ± 0.15		CUSTOMER DRAWING	SCALE 1:1 SHEET 1 OF 1 REV B	
4 PLC ± -				
ANGLES ± 3°				
FINISH				