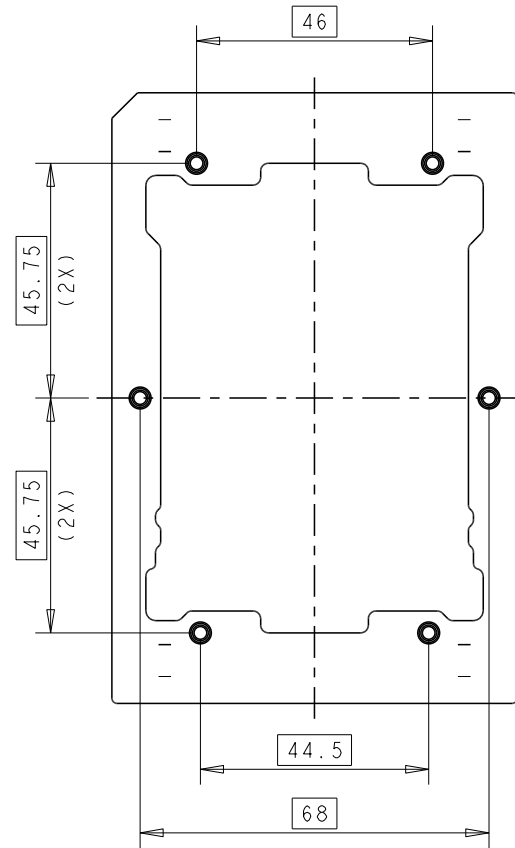
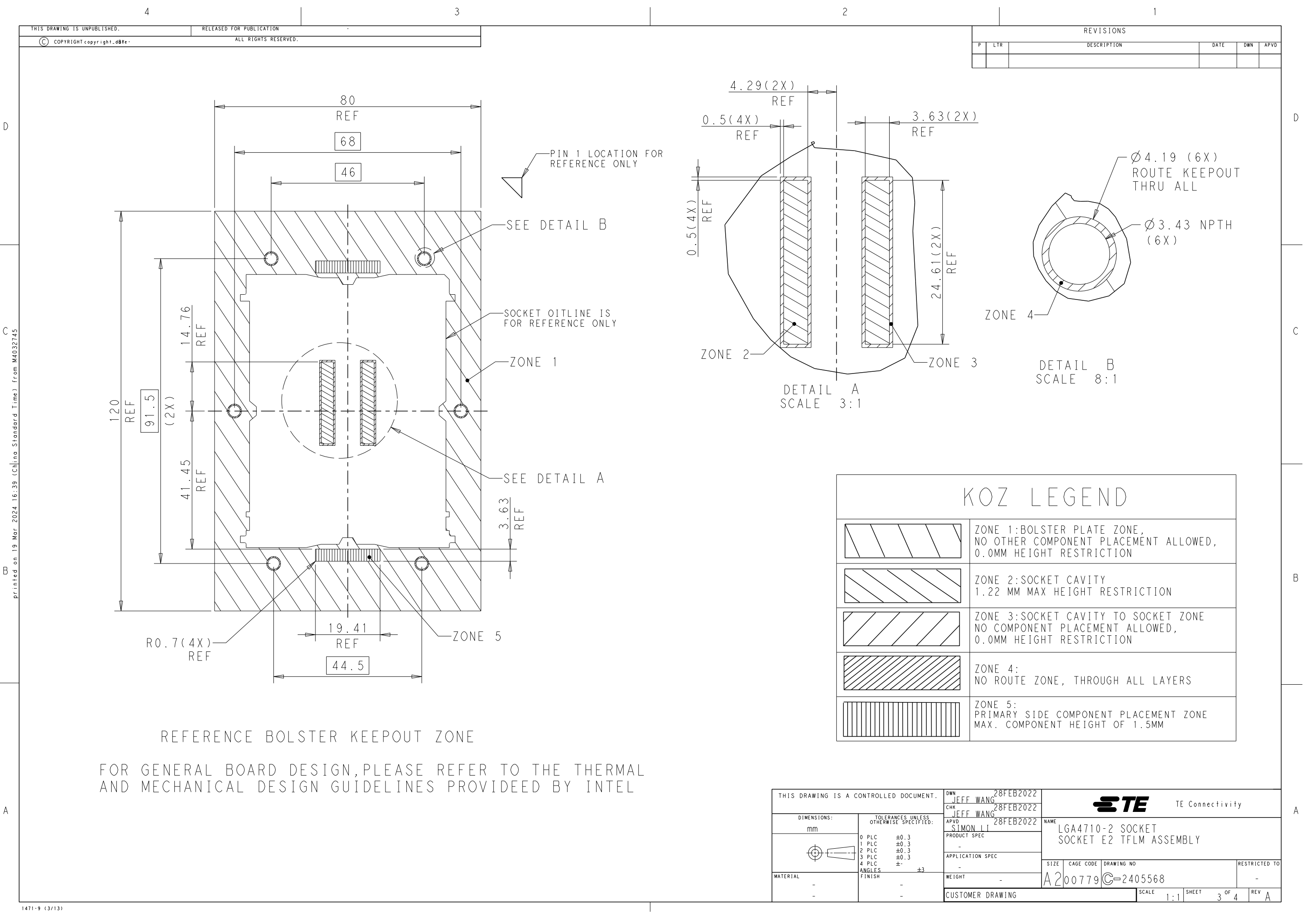


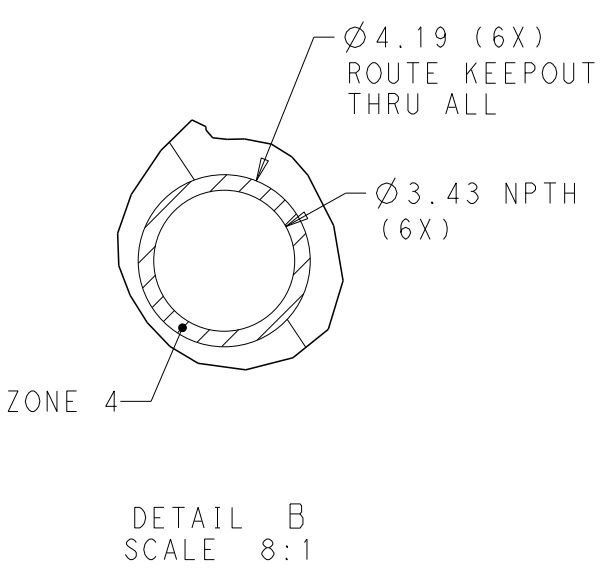
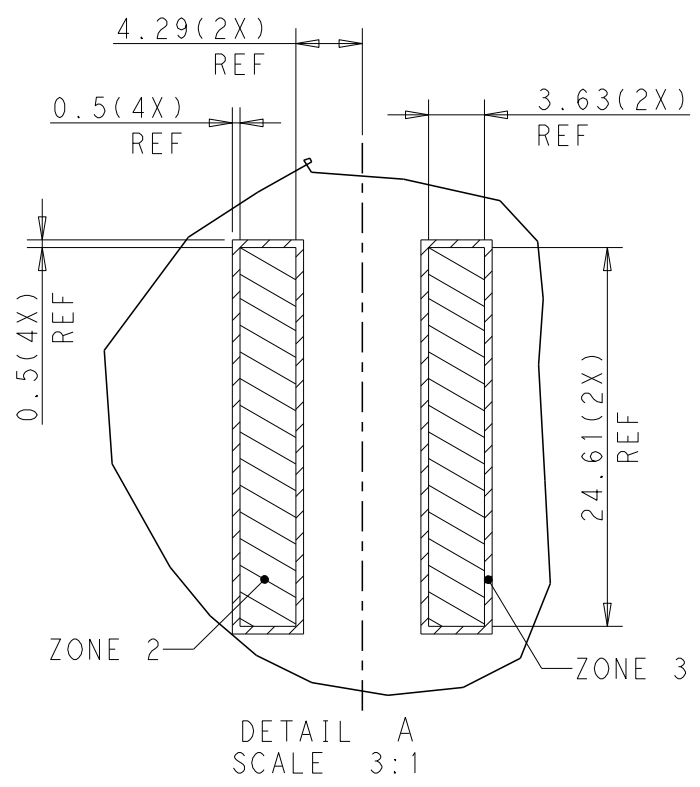
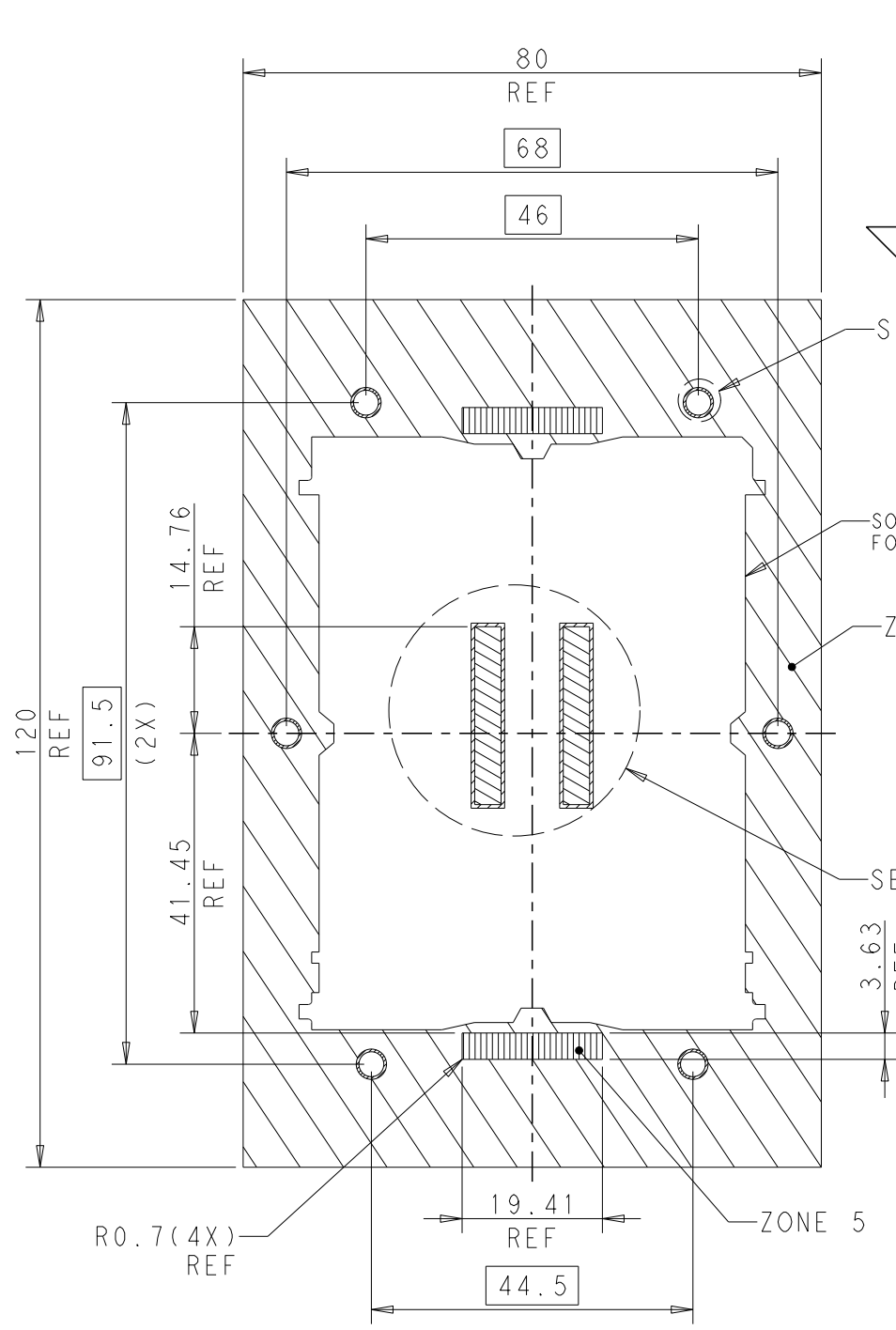
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REVISIONS				
P	LTR	DESCRIPTION	DATE	APVD



KOZ LEGEND	
	ZONE 1: BOLSTER PLATE ZONE, NO OTHER COMPONENT PLACEMENT ALLOWED, 0.0MM HEIGHT RESTRICTION
	ZONE 2: SOCKET CAVITY 1.22 MM MAX HEIGHT RESTRICTION
	ZONE 3: SOCKET CAVITY TO SOCKET ZONE NO COMPONENT PLACEMENT ALLOWED, 0.0MM HEIGHT RESTRICTION
	ZONE 4: NO ROUTE ZONE, THROUGH ALL LAYERS
	ZONE 5: PRIMARY SIDE COMPONENT PLACEMENT ZONE MAX. COMPONENT HEIGHT OF 1.5MM

REFERENCE BOLSTER KEEPOUT ZONE
FOR GENERAL BOARD DESIGN, PLEASE REFER TO THE THERMAL AND MECHANICAL DESIGN GUIDELINES PROVIDED BY INTEL

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN JEFF WANG 28FEB2022	TE Connectivity	
DIMENSIONS: mm		CHK JEFF WANG 28FEB2022		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD SIMON LI 28FEB2022	NAME LGA4710-2 SOCKET SOCKET E2 TFLM ASSEMBLY	
0 PLC ±0.3		PRODUCT SPEC -	SIZE CAGE CODE DRAWING NO RESTRICTED TO	
1 PLC ±0.3		APPLICATION SPEC -	A200779 C-2405568	
2 PLC ±0.3		WEIGHT -	SCALE 1:1 SHEET 3 OF 4 REV A	
3 PLC ±0.3		CUSTOMER DRAWING		
4 PLC ±0.3				
ANGLES ±°				
FINISH -				
MATERIAL -				

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