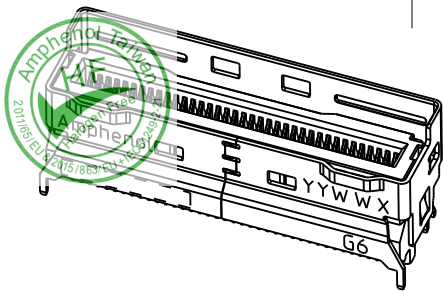
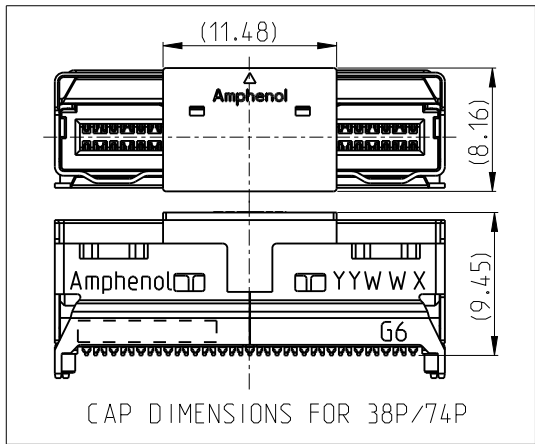
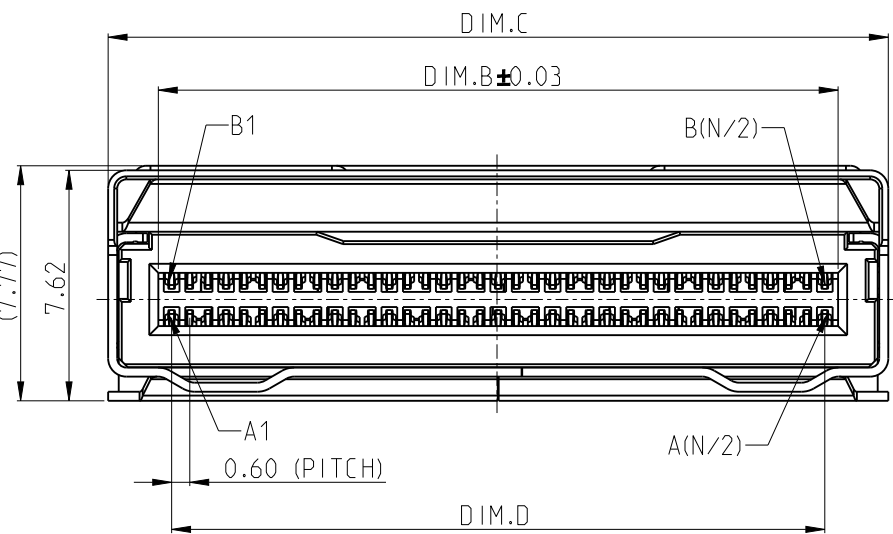


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REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
AX4	NE-24178	ADDED GH10V11XXXXHR FRONT VIEW	2024-08-28	JOAN LU
AX5	NE-24203	UPDATED THE DISPLAY METHOD OF PIN OUT	2024-09-27	JOAN LU
AX6	NE-24270	1.DC CHANGED FROM 4 DIGIT DC TO 5 DIGIT DC 2.ADDED ALL HIGH SPEED CHANNELS OPTIONS INTO PNS 3.ADDED PACKING CODE 5	2024-12-10	JOAN LU



MINI COOL EDGE IO
INTEGRATED CONNECTOR

VERTICAL TYPE

CONNECTOR SPEED
1: PCIE GEN 6

CHANNEL/PIN QTY
1: 38P(4X+SB)
2: 74P(8X+SB)
4: 124P(16X+SB)
6: 148P(16X+SB)
A: 38P(6X)
B: 74P(12X)
C: 124P(20X)
D: 148P(24X)

CONTACT FINISH
2: 15u" GOLD
3: 30u" GOLD

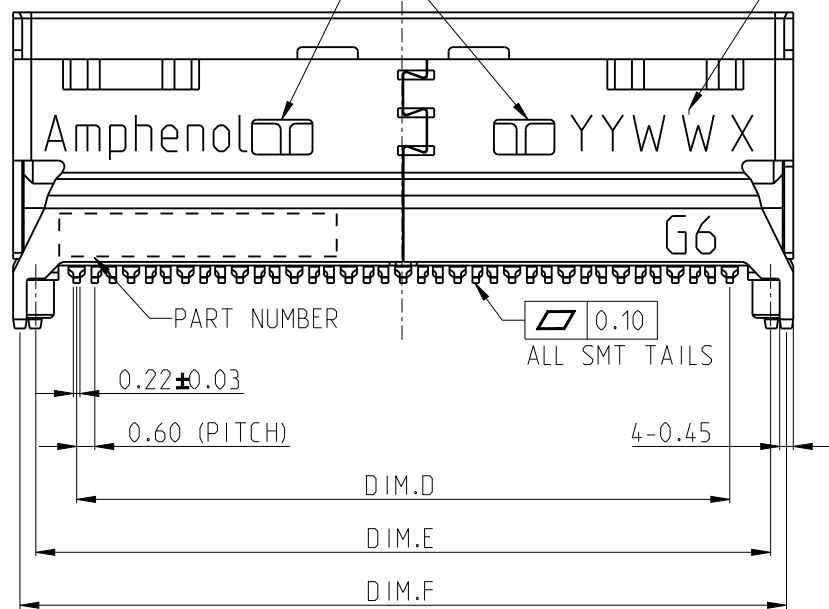
COMPLIANT CODE
HR: HALOGEN-FREE & RoHS

VARIATION CODE
BLANK: STANDARD
W: HOUSING COLOR : NATURAL

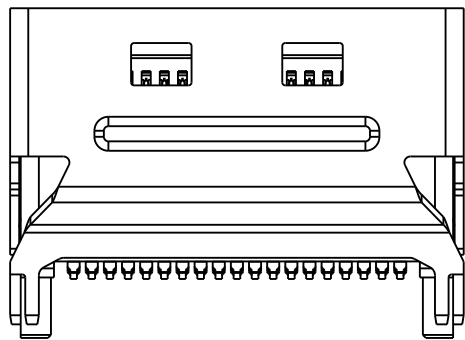
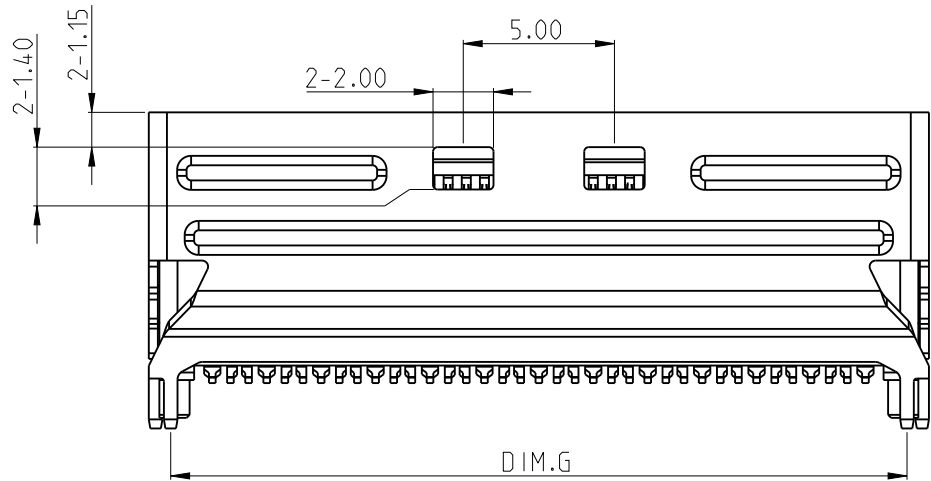
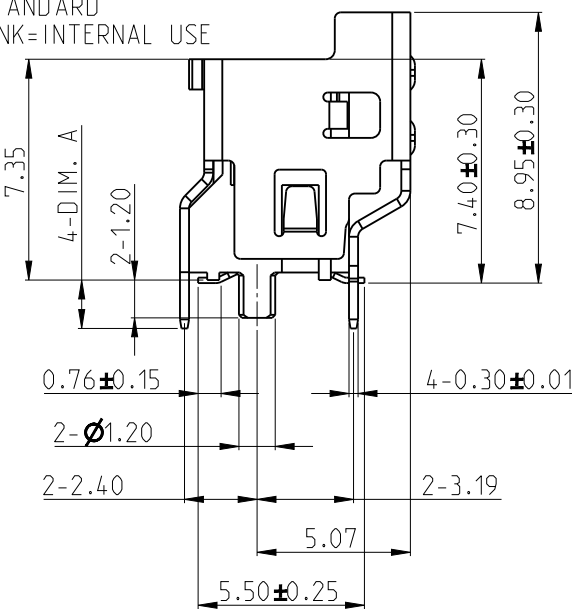
PACKING CODE
2: TAPE AND REEL (WITH CAP)
3: TAPE AND REEL (W/O CAP)
5. TAPE AND REEL WITH CUSTOMIZED

SHELL PIN LENGTH OPTION (DIM.A)
1: 1.50mm
2: 2.20mm
3: 3.00mm
4: 4.20mm
5: 1.20mm

38P WITHOUT SHELL HOLES
74P WITH SHELL HOLES



DATE CODE
YY:YEAR
W W:WORK WEEK
X:BLANK=STANDARD
NON-BLANK=INTERNAL USE



NOTES:
1. MATERIALS:
HOUSING : HIGH TEMPERATURE THERMOPLASTIC UL94V-0,
COLOR: GH10V1XXXXHR: BLACK
GH10V1AXXXXXHR: NATURAL
WAFER :HIGH TEMPERATURE THERMO PLASTIC
UL94V-0,COLOR: BLACK
SHELL/GND BAR : STAINLESS
CONTACT: COPPER ALLOY

2. FINISH
TERMINAL : CONTACT AREA : SELECTED GOLD PLATING.
SOLDER AREA : MATTE TIN PLATING 100u"
UNDER PLATING: NICKEL PLATING 50u"MIN OVER ALL
SHELL : UNDER PLATING: ELECTROLESS NICKEL PLATING 50u"MIN OVER ALL
3.THIS PRODUCT DOESN'T CONTAIN ENVIROMENTAL HAZARDOUS MATERIALS PER
DERCTIVE 2011/65/EU & 2015/863/EU FOR RoHS AND PER IEC 61249-2-21 FOR
HALOGEN FREE.
4.PRODUCT SPEC REFER TO PS-07853.
5.GND PINS ARE CONNECTED.

PART NUMBER	POS. (N)	DIM. B	DIM. C	DIM. D	DIM. E	DIM. F	DIM.G
GH10V11XXXXHR	38	11.68	15.00	10.80	13.50	14.55	13.55
GH10V1AXXXXXHR	74	22.48	25.80	21.60	24.30	25.35	24.35

TOLERANCE	DRAWN	APPROVALS	DATE	TITLE		UNIT		SIZE	PART No.	
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10	DESIGNED	JOAN LU	2024-12-09	MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE		mm		A3	GH10V1XXXXXHR	
ANGULAR ±2°	CHECKED	Sondra Sang	2024-12-11			SCALE		1 OF 11	GH10V1XXXXXHR	
UNLESS OTHERWISE SPECIFIED	APPROVED	Hank Hsu	2024-12-11			NA		1 OF 11	GH10V1XXXXXHR	
	DWG TYPE	CUST DWG	PROJECT CODE	TXXX					DWG No.	REV.
										AX6

Amphenol®

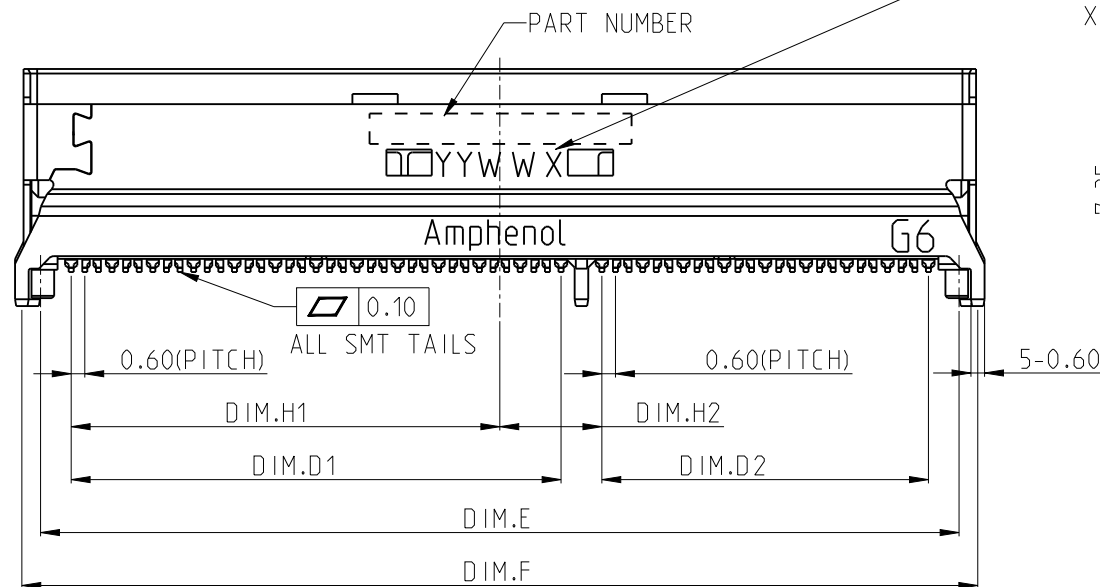
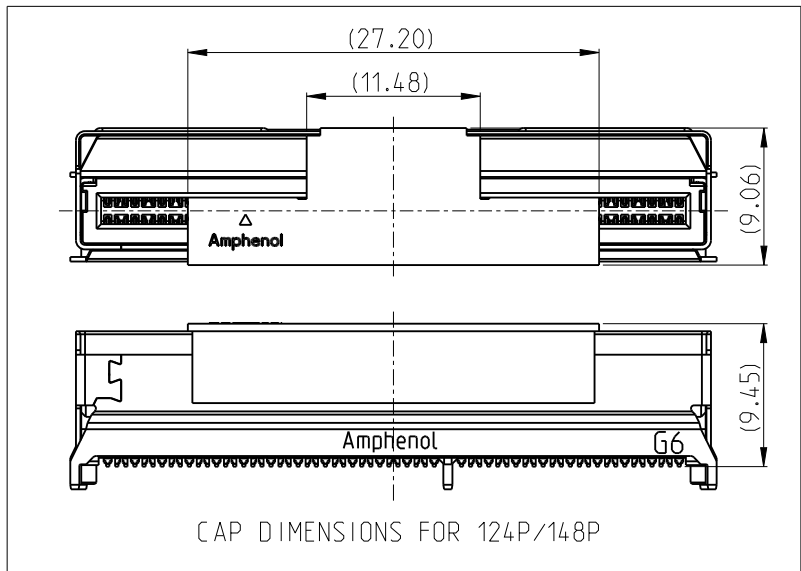
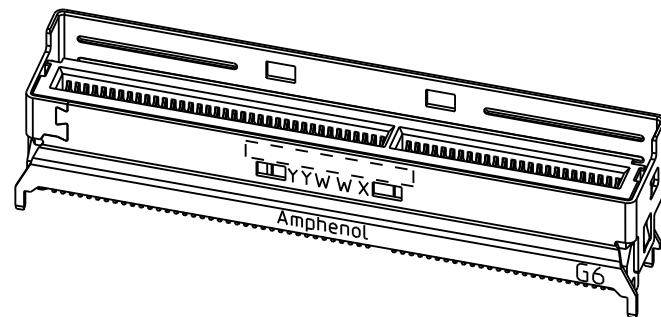
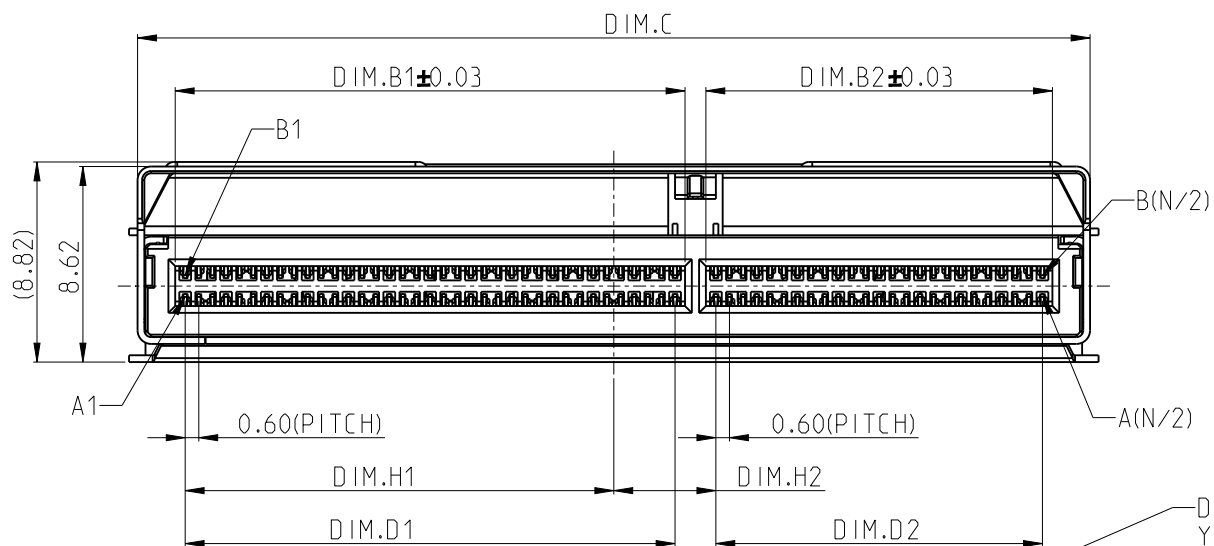
Amphenol Corporation
Amphenol Taiwan Corporation

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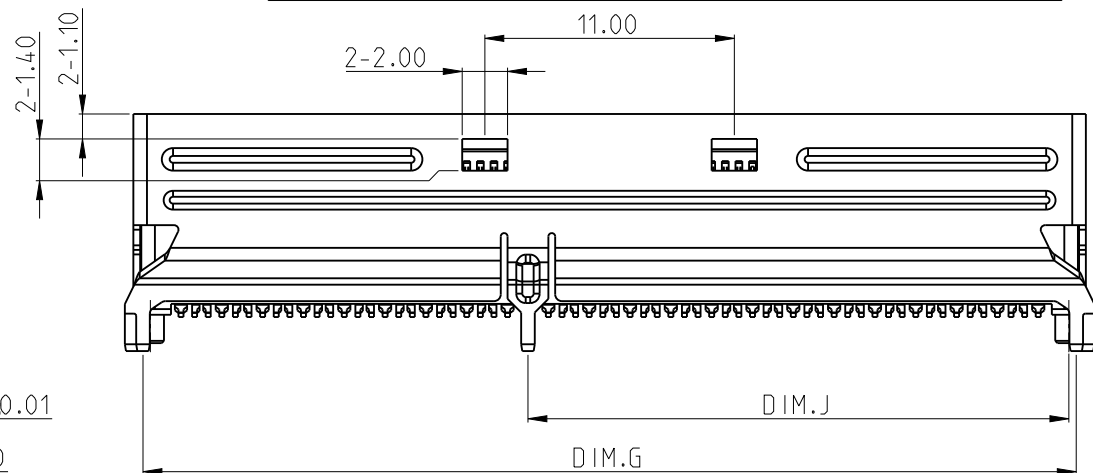
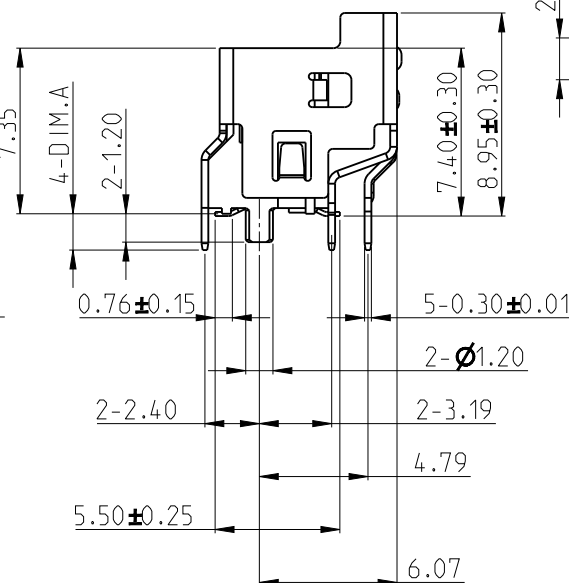
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REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
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AX6	NE-24270	1.DC CHANGED FROM 4 DIGIT DC TO 5 DIGIT DC 2.ADDED ALL HIGH SPEED CHANNELS OPTIONS INTO PNS 3.ADDED PACKING CODE 5	2024-12-10	JOAN LU



DATE CODE
YY:YEAR
WW:WORK WEEK
X:BLANK=STANDARD
NON-BLANK=INTERNAL USE



PART NUMBER	POS. (N)	DIM. B1	DIM. B2	DIM. C	DIM. D1	DIM. D2	DIM. E	DIM. F	DIM. G	DIM. H1	DIM. H2	DIM. J
GH10V14XXXXHR	124	22.48	15.28	42.00	21.60	14.40	40.50	42.15	41.15	18.90	4.50	23.85
GH10V1CXXXXHR												
GH10V16XXXXHR	148	22.48	22.48	49.20	21.60	21.60	47.70	49.35	48.35	22.50	0.90	23.85
GH10V1DXXXXHR												

TOLERANCE	DRAWN	APPROVALS	DATE	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE		Amphenol® Amphenol Corporation Amphenol Taiwan Corporation			
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10	DESIGNED	JOAN LU	2024-12-09						
ANGULAR ±2°	CHECKED	Sondra Sang	2024-12-11			PART No. GH10V1XXXXXHR			
UNLESS OTHERWISE SPECIFIED	APPROVED	Hank Hsu	2024-12-11			DWG No. GH10V1XXXXXHR			
	DWG TYPE	CUST DWG	PROJECT CODE	TXXX	UNIT	mm	SIZE	A3	REV. AX6
					SCALE	NA	SHEET	2 OF 11	

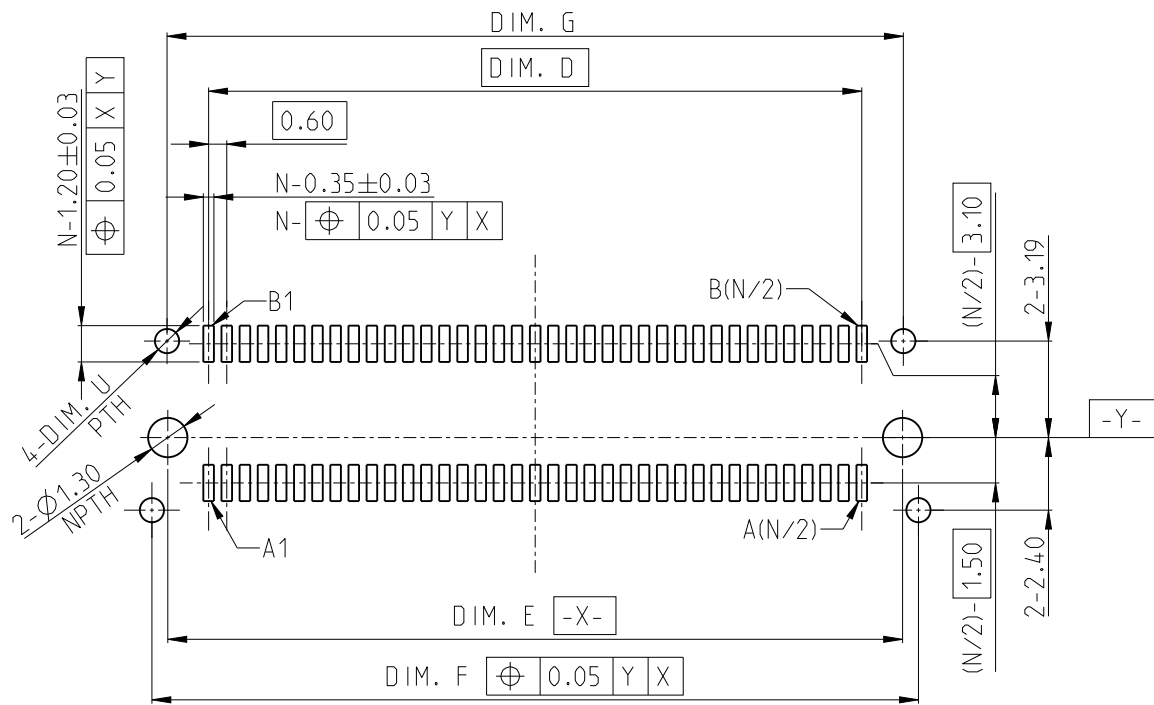
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RECOMMENDED PCB LAYOUT
(GENERAL TOLERANCES :+/-0.05)

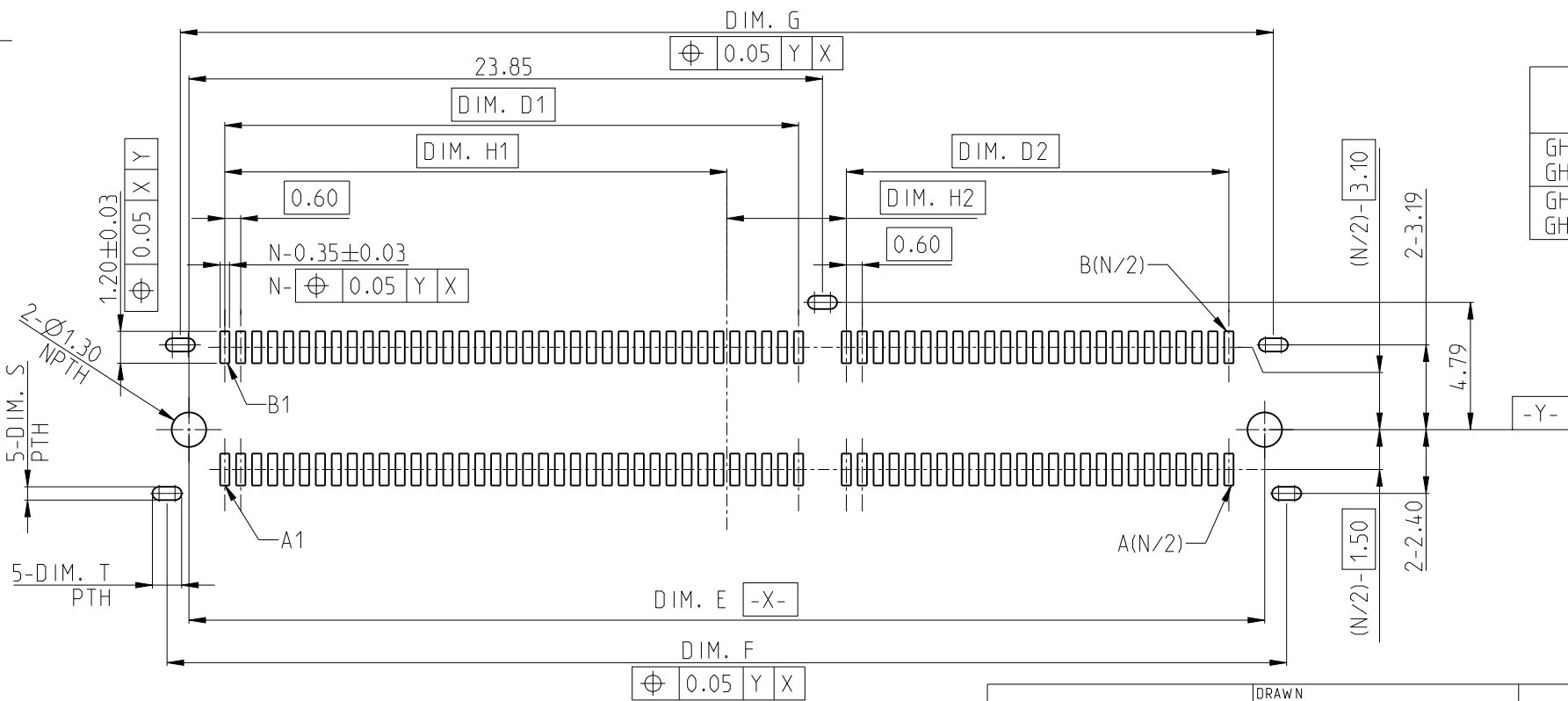
REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
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PART NUMBER	POS. (N)	DIM. D	DIM. E	DIM. F	DIM. G
GH10V11XXXXHR GH10V1AXXXXHR	38	10.80	13.50	14.55	13.55
GH10V12XXXXHR GH10V1BXXXXHR	74	21.60	24.30	25.35	24.35

PART NUMBER (APPLY TO 38P/74P)	DIM. U
GH10V1XX1XXHR	0.8
GH10V1XX2XXHR	
GH10V1XX3XXHR	
GH10V1XX4XXHR	0.95
GH10V1XX5XXHR	0.8

PART NUMBER	POS. (N)	DIM. D1	DIM. D2	DIM. E	DIM. F	DIM. G	DIM. H1	DIM. H2
GH10V14XXXXHR GH10V1CXXXXHR	124	21.60	14.40	40.50	42.15	41.15	18.90	4.50
GH10V16XXXXHR GH10V1DXXXXHR	148	21.60	21.60	47.70	49.35	48.35	22.50	0.9



PART NUMBER	POS. (N)	DIM. S	DIM. T
GH10V14X1XXHR GH10V1C X1XXHR	124	0.5	1.1
GH10V14X2XXHR GH10V1C X2XXHR			
GH10V14X3XXHR GH10V1C X3XXHR			
GH10V14X4XXHR GH10V1C X4XXHR		0.65	1.3
GH10V14X5XXHR GH10V1C X5XXHR		0.5	1.1
GH10V16XXXXHR GH10V1DXXXXHR	148	0.65	1.3

TOLERANCE	DRAWN	APPROVALS	DATE	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE		Amphenol® Amphenol Corporation Amphenol Taiwan Corporation		
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10	DESIGNED	JOAN LU	2024-12-09					
ANGULAR ±2°	CHECKED	Sondra Sang	2024-12-11		UNIT mm	SIZE A3	PART No. GH10V1XXXXXHR	
UNLESS OTHERWISE SPECIFIED	DWG TYPE	CUST DWG	PROJECT CODE TXXX				DWG No. GH10V1XXXXXHR	REV. AX6

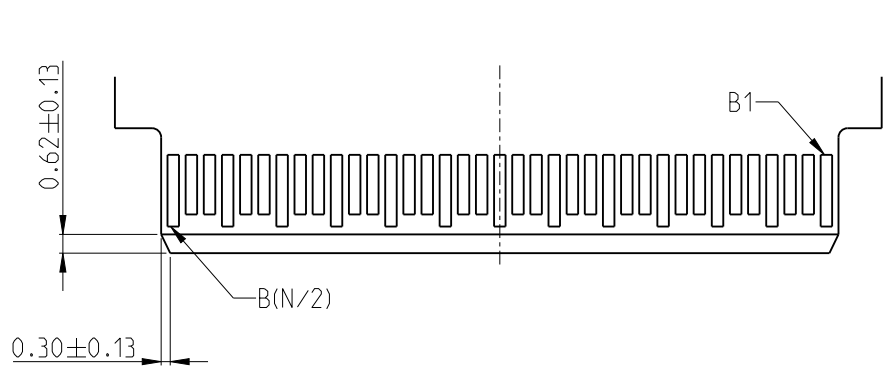
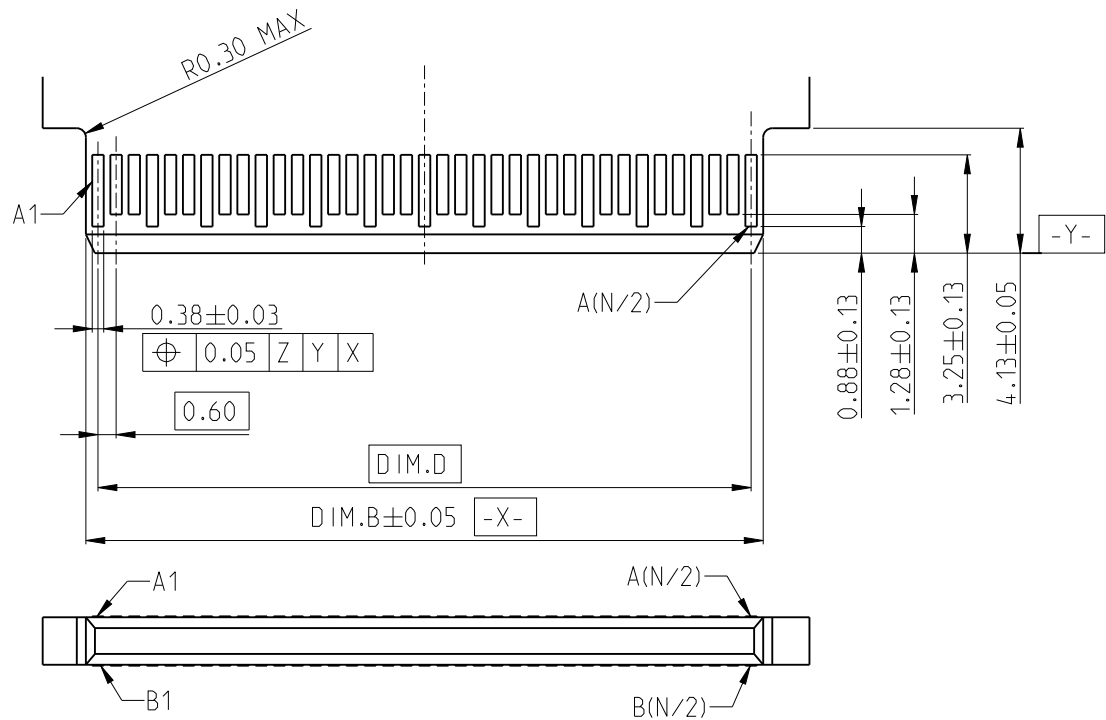
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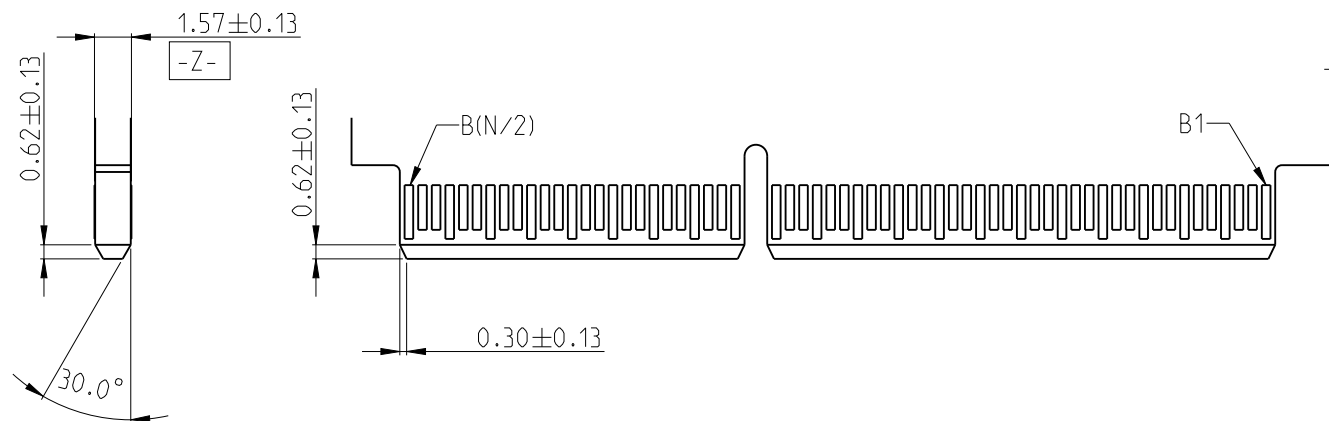
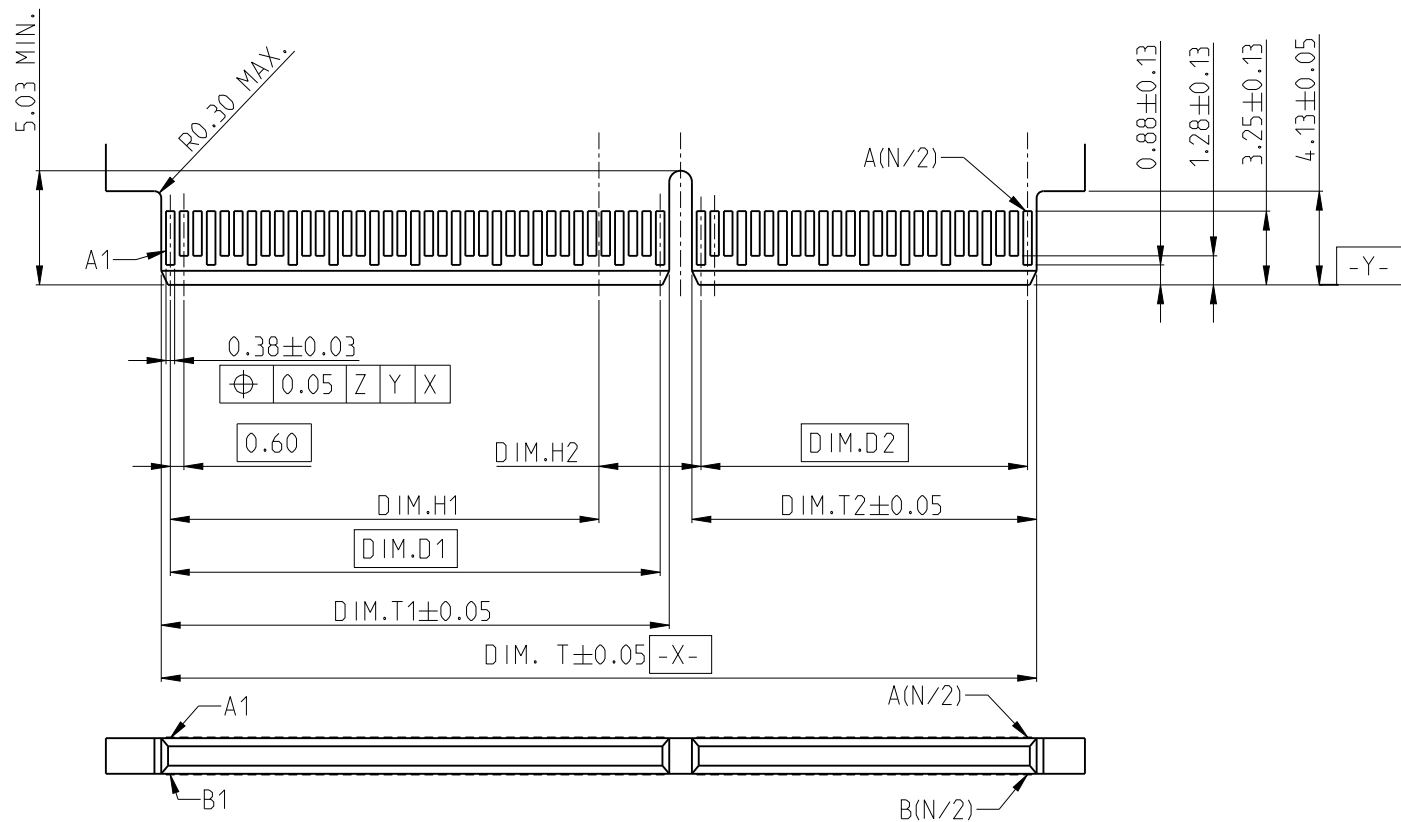


RECOMMENDED AIC MATING CARD DIMENSIONS

REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
AX4	NE-24178	ADDED GH10V11XXXXXHR FRONT VIEW	2024-08-28	JOAN LU
AX5	NE-24203	UPDATED THE DISPLAY METHOD OF PIN OUT	2024-09-27	JOAN LU
AX6	NE-24270	1.DC CHANGED FROM 4 DIGIT DC TO 5 DIGIT DC 2.ADDED ALL HIGH SPEED CHANNELS OPTIONS INTO PNS 3.ADDED PACKING CODE 5	2024-12-10	JOAN LU



PART NUMBER	POS. (N)	DIM. B	DIM. D
GH10V11XXXXXHR GH10V1AXXXXXHR	38	11.6	10.80
GH10V12XXXXXHR GH10V1BXXXXXHR	74	22.4	21.6



PART NUMBER	POS. (N)	DIM. D1	DIM. D2	DIM. H1	DIM. H2	DIM. T	DIM. T1	DIM. T2
GH10V14XXXXXHR GH10V1CXXXXXHR	124	21.60	14.40	18.90	4.50	38.6	22.40	15.2
GH10V16XXXXXHR GH10V1DXXXXXHR	148	21.60	21.60	22.50	0.90	45.80	22.40	22.40

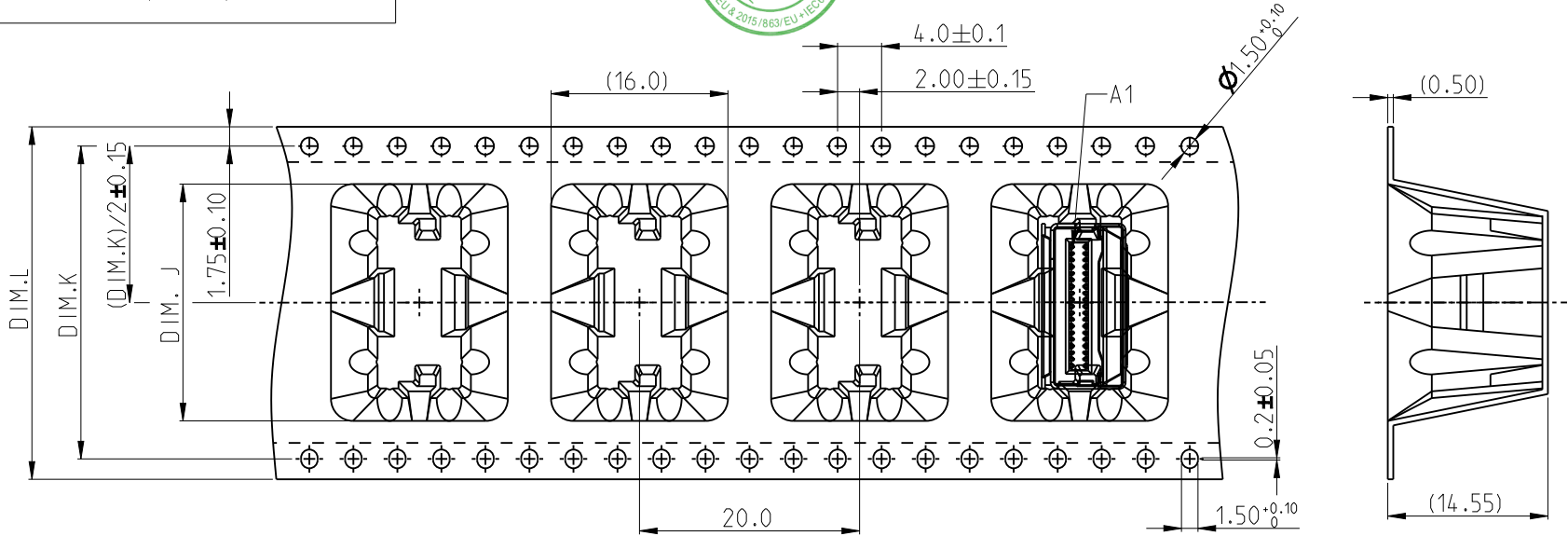
TOLERANCE	DRAWN	APPROVALS	DATE	TITLE		Amphenol®		
X. ±0.30	DESIGNED	JOAN LU	2024-12-09	MINI COOL EDGE IO		Amphenol Corporation		
X.X ±0.20	CHECKED	Sondra Sang	2024-12-11	INTEGRATED CONNECTOR		Amphenol Taiwan Corporation		
X.XX ±0.10	APPROVED	Hank Hsu	2024-12-11	0.6 PITCH, VERTICAL		PART No. GH10V1XXXXXHR		
ANGULAR ±2°	DWG TYPE	CUST DWG	PROJECT CODE	TXXX		DWG No. GH10V1XXXXXHR		
UNLESS OTHERWISE SPECIFIED						REV. AX6		

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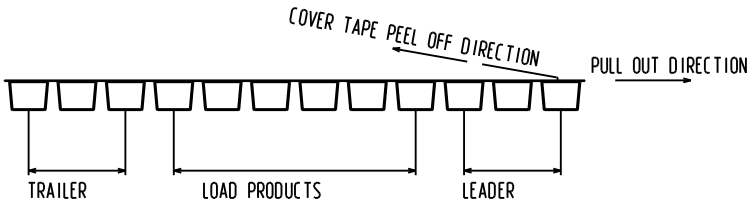
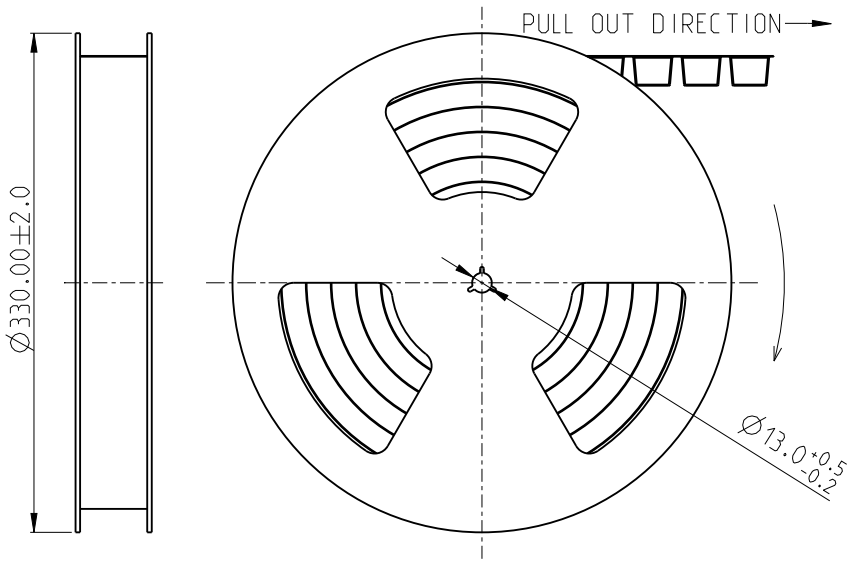
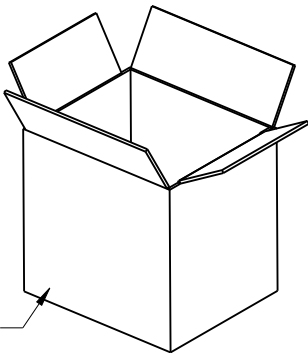
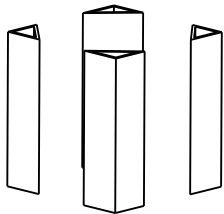
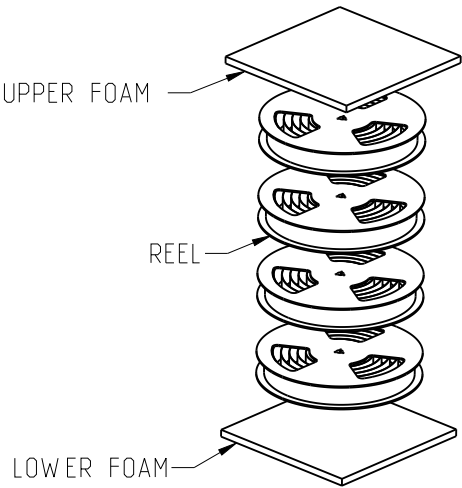
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REVISIONS				
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PART NUMBER	POS. (N)	DIM. J	DIM. K	DIM. L	PCS /REEL	REEL /CARTON	PCS /CARTON	UPPER FOAM	LOWER FOAM
GH10V11XXXXHR GH10V1AXXXXHR	38	(21.5)	28.4±0.1	32.0±0.3	200	7	1400	NO	YES
GH10V12XXXXHR GH10V1BXXXXHR	74	(34.0)	52.4±0.1	56.0±0.3	200	4	800	YES	YES
GH10V14XXXXHR GH10V1CXXXXHR	124	(39.5)	68.40±0.1	72.0±0.3	200	3	600	YES	YES
GH10V16XXXXHR GH10V1DXXXXHR	148	(57.9)	68.40±0.1	72.0±0.3	200	3	600	YES	YES



NOTE 2:

1. CARTON SIZE
340L X 340W X265H
2. REFER TO AMTA LABELING STANDARD.
3. CARTON CLOSURES MUST BE SEALED WITH TAPE.

NOTE 1 :

- 1.MATERIAL:
CARRIER TAPE: POLYSTYRENE, BLACK
COVER TAPE: TRANSPARENT POLYSTYRENE
REEL: POLYSTYRENE, BLACK
- 2.ALL TAPE AND REEL SPECIFICATION FOLLOW EIA-481 STANDARD.
- 3.THIS PRODUCT DOESN'T CONTAIN ENVIROMENTAL HAZARDOUS MATERIALS PER DERCTIVE 2011/65/EU & 2015/863/EU FOR RoHS AND PER IEC 61249-2-21 FOR HALOGEN FREE.

TOLERANCE		DRAWN APPROVALS		DATE	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE			Amphenol® Amphenol Corporation Amphenol Taiwan Corporation				
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10	DESIGNED JOAN LU	CHECKED Sondra Sang	APPROVED Hank Hsu	2024-12-09					UNIT mm	SIZE A3	PART No. GH10V1XXXXXHR	
				2024-12-11								
				2024-12-11								
ANGULAR ±2°		DWG TYPE CUST DWG		PROJECT CODE TXXX			SCALE NA	SHEET 5 OF 11	DWG No. GH10V1XXXXXHR	REV. AX6		
UNLESS OTHERWISE SPECIFIED												

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38P PIN ASSIGNMENT					
X4+10 SIDEBAND SOLUTION PIN ASSIGNMENT					
	PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION
B1	B1	GND		A1	GND
	B2	TX		A2	RX
	B3	TX		A3	RX
B4	B4	GND		A4	GND
	B5	TX		A5	RX
	B6	TX		A6	RX
B7	B7	GND		A7	GND
	B8	SIDEBAND	- - - -	A8	SIDEBAND
	B9	SIDEBAND	- - - -	A9	SIDEBAND
B10	B10	SIDEBAND	- - - -	A10	SIDEBAND
	B11	SIDEBAND	- - - -	A11	SIDEBAND
	B12	SIDEBAND	- - - -	A12	SIDEBAND
B13	B13	GND		A13	GND
	B14	TX		A14	RX
	B15	TX		A15	RX
B16	B16	GND		A16	GND
	B17	TX		A17	RX
	B18	TX		A18	RX
B19	B19	GND		A19	GND

38P PIN ASSIGNMENT					
X6 SOLUTION PIN ASSIGNMENT					
	PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION
B1	B1	GND		A1	GND
	B2	TX		A2	RX
	B3	TX		A3	RX
B4	B4	GND		A4	GND
	B5	TX		A5	RX
	B6	TX		A6	RX
B7	B7	GND		A7	GND
	B8	TX		A8	RX
	B9	TX		A9	RX
B10	B10	GND		A10	GND
	B11	TX		A11	RX
	B12	TX		A12	RX
B13	B13	GND		A13	GND
	B14	TX		A14	RX
	B15	TX		A15	RX
B16	B16	GND		A16	GND
	B17	TX		A17	RX
	B18	TX		A18	RX
B19	B19	GND		A19	GND

TOLERANCE	DRAWN	APPROVALS	DATE	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE		Amphenol® Amphenol Corporation Amphenol Taiwan Corporation		
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10	DESIGNED	JOAN LU	2024-12-09					
	CHECKED	Sondra Sang	2024-12-11					
	APPROVED	Hank Hsu	2024-12-11					
ANGULAR ±2°	DWG TYPE	CUST DWG	PROJECT CODE	TXXX		PART No. GH10V1XXXXXHR		
UNLESS OTHERWISE SPECIFIED						UNIT mm	SIZE A3	REV. AX6
						SCALE NA	SHEET 6 OF 11	
						DWG No. GH10V1XXXXXHR		

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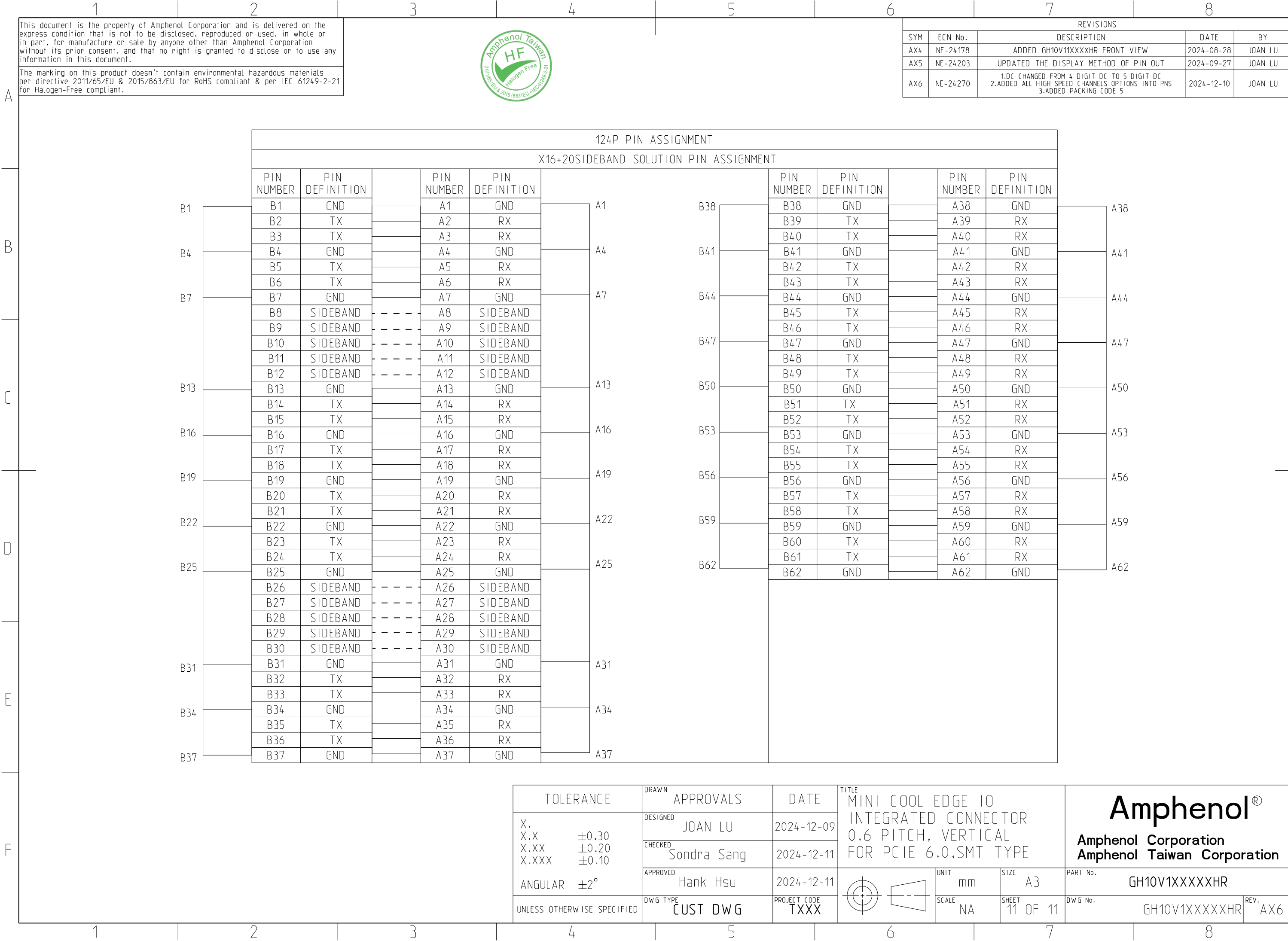


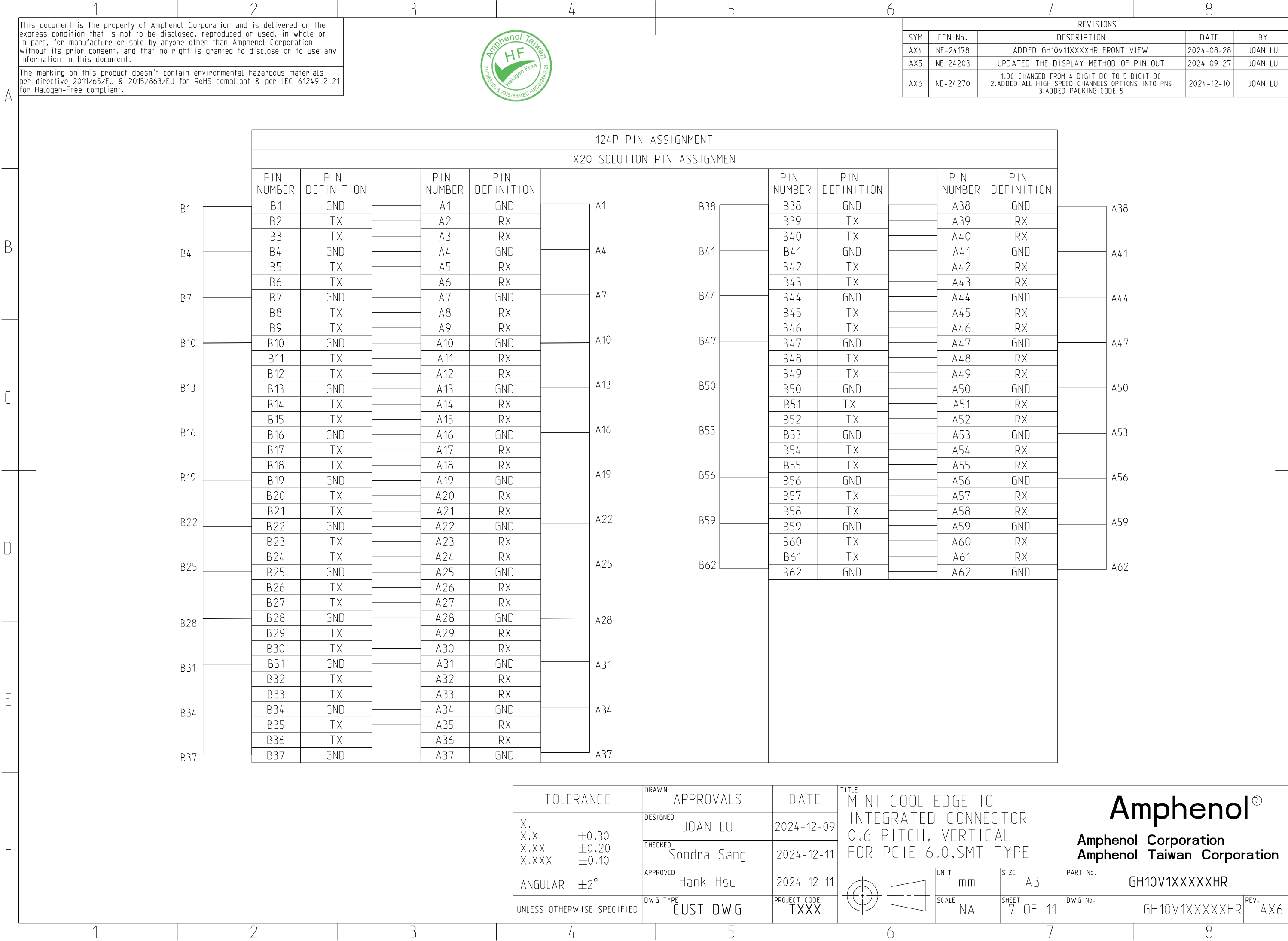
REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
AX4	NE-24178	ADDED GH10V11XXXXHR FRONT VIEW	2024-08-28	JOAN LU
AX5	NE-24203	UPDATED THE DISPLAY METHOD OF PIN OUT	2024-09-27	JOAN LU
AX6	NE-24270	1.DC CHANGED FROM 4 DIGIT DC TO 5 DIGIT DC 2.ADDED ALL HIGH SPEED CHANNELS OPTIONS INTO PINS 3.ADDED PACKING CODE 5	2024-12-10	JOAN LU

74P PIN ASSIGNMENT					
X8+20SIDE BAND SOLUTION PIN ASSIGNMENT					
	PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION
B1	B1	GND		A1	GND
	B2	TX		A2	RX
	B3	TX		A3	RX
B4	B4	GND		A4	GND
	B5	TX		A5	RX
	B6	TX		A6	RX
B7	B7	GND		A7	GND
	B8	SIDE BAND	---	A8	SIDE BAND
	B9	SIDE BAND	---	A9	SIDE BAND
B10	B10	SIDE BAND	---	A10	SIDE BAND
	B11	SIDE BAND	---	A11	SIDE BAND
	B12	SIDE BAND	---	A12	SIDE BAND
B13	B13	GND		A13	GND
	B14	TX		A14	RX
	B15	TX		A15	RX
B16	B16	GND		A16	GND
	B17	TX		A17	RX
	B18	TX		A18	RX
B19	B19	GND		A19	GND
	B20	TX		A20	RX
	B21	TX		A21	RX
B22	B22	GND		A22	GND
	B23	TX		A23	RX
	B24	TX		A24	RX
B25	B25	GND		A25	GND
	B26	SIDE BAND	---	A26	SIDE BAND
	B27	SIDE BAND	---	A27	SIDE BAND
B28	B28	SIDE BAND	---	A28	SIDE BAND
	B29	SIDE BAND	---	A29	SIDE BAND
	B30	SIDE BAND	---	A30	SIDE BAND
B31	B31	GND		A31	GND
	B32	TX		A32	RX
	B33	TX		A33	RX
B34	B34	GND		A34	GND
	B35	TX		A35	RX
	B36	TX		A36	RX
B37	B37	GND		A37	GND

74P PIN ASSIGNMENT					
X12 SOLUTION PIN ASSIGNMENT					
	PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION
B1	B1	GND		A1	GND
	B2	TX		A2	RX
	B3	TX		A3	RX
B4	B4	GND		A4	GND
	B5	TX		A5	RX
	B6	TX		A6	RX
B7	B7	GND		A7	GND
	B8	TX		A8	RX
	B9	TX		A9	RX
B10	B10	GND		A10	GND
	B11	TX		A11	RX
	B12	TX		A12	RX
B13	B13	GND		A13	GND
	B14	TX		A14	RX
	B15	TX		A15	RX
B16	B16	GND		A16	GND
	B17	TX		A17	RX
	B18	TX		A18	RX
B19	B19	GND		A19	GND
	B20	TX		A20	RX
	B21	TX		A21	RX
B22	B22	GND		A22	GND
	B23	TX		A23	RX
	B24	TX		A24	RX
B25	B25	GND		A25	GND
	B26	TX		A26	RX
	B27	TX		A27	RX
B28	B28	GND		A28	GND
	B29	TX		A29	RX
	B30	TX		A30	RX
B31	B31	GND		A31	GND
	B32	TX		A32	RX
	B33	TX		A33	RX
B34	B34	GND		A34	GND
	B35	TX		A35	RX
	B36	TX		A36	RX
B37	B37	GND		A37	GND

TOLERANCE	DRAWN APPROVALS	DATE	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE			Amphenol® Amphenol Corporation Amphenol Taiwan Corporation		
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10 ANGULAR ±2°	DESIGNED JOAN LU	2024-12-09						
	CHECKED Sondra Sang	2024-12-11						
	APPROVED Hank Hsu	2024-12-11						
UNLESS OTHERWISE SPECIFIED	DWG TYPE CUST DWG	PROJECT CODE TXXX	SCALE NA	SHEET 6 OF 11	PART No. GH10V1XXXXXHR	DWG No. GH10V1XXXXXHR	REV. AX6	





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REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
AX4	NE-24178	ADDED GH10V11XXXXHR FRONT VIEW	2024-08-28	JOAN LU
AX5	NE-24203	UPDATED THE DISPLAY METHOD OF PIN OUT	2024-09-27	JOAN LU
AX6	NE-24270	1.DC CHANGED FROM 4 DIGIT DC TO 5 DIGIT DC 2.ADDED ALL HIGH SPEED CHANNELS OPTIONS INTO PNS 3.ADDED PACKING CODE 5	2024-12-10	JOAN LU

148P PIN ASSIGNMENT											
X16+40 SIDEBAND SOLUTION PIN ASSIGNMENT											
	PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION
B1	B1	GND		A1	GND	A1	B38	GND		A38	GND
	B2	TX		A2	RX		B39	TX		A39	RX
	B3	TX		A3	RX		B40	TX		A40	RX
B4	B4	GND		A4	GND	A4	B41	GND		A41	GND
	B5	TX		A5	RX		B42	TX		A42	RX
	B6	TX		A6	RX		B43	TX		A43	RX
B7	B7	GND		A7	GND	A7	B44	GND		A44	GND
	B8	SIDEBAND	- - - -	A8	SIDEBAND		B45	SIDEBAND	- - - -	A45	SIDEBAND
	B9	SIDEBAND	- - - -	A9	SIDEBAND		B46	SIDEBAND	- - - -	A46	SIDEBAND
B13	B10	SIDEBAND	- - - -	A10	SIDEBAND	A13	B47	SIDEBAND	- - - -	A47	SIDEBAND
	B11	SIDEBAND	- - - -	A11	SIDEBAND		B48	SIDEBAND	- - - -	A48	SIDEBAND
	B12	SIDEBAND	- - - -	A12	SIDEBAND		B49	SIDEBAND	- - - -	A49	SIDEBAND
B16	B13	GND		A13	GND	A16	B50	GND		A50	GND
	B14	TX		A14	RX		B51	TX		A51	RX
	B15	TX		A15	RX		B52	TX		A52	RX
B19	B16	GND		A16	GND	A19	B53	GND		A53	GND
	B17	TX		A17	RX		B54	TX		A54	RX
	B18	TX		A18	RX		B55	TX		A55	RX
B22	B19	GND		A19	GND	A22	B56	GND		A56	GND
	B20	TX		A20	RX		B57	TX		A57	RX
	B21	TX		A21	RX		B58	TX		A58	RX
B25	B22	GND		A22	GND	A25	B59	GND		A59	GND
	B23	TX		A23	RX		B60	TX		A60	RX
	B24	TX		A24	RX		B61	TX		A61	RX
B31	B25	GND		A25	GND	A31	B62	GND		A62	GND
	B26	SIDEBAND	- - - -	A26	SIDEBAND		B63	SIDEBAND	- - - -	A63	SIDEBAND
	B27	SIDEBAND	- - - -	A27	SIDEBAND		B64	SIDEBAND	- - - -	A64	SIDEBAND
B34	B28	SIDEBAND	- - - -	A28	SIDEBAND	A34	B65	SIDEBAND	- - - -	A65	SIDEBAND
	B29	SIDEBAND	- - - -	A29	SIDEBAND		B66	SIDEBAND	- - - -	A66	SIDEBAND
	B30	SIDEBAND	- - - -	A30	SIDEBAND		B67	SIDEBAND	- - - -	A67	SIDEBAND
B37	B31	GND		A31	GND	A37	B68	GND		A68	GND
	B32	TX		A32	RX		B69	TX		A69	RX
	B33	TX		A33	RX		B70	TX		A70	RX
B37	B34	GND		A34	GND	A71	B71	GND		A71	GND
	B35	TX		A35	RX		B72	TX		A72	RX
	B36	TX		A36	RX		B73	TX		A73	RX
	B37	GND		A37	GND		B74	GND		A74	GND

TOLERANCE	DRAWN	APPROVALS	DATE	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE		Amphenol® Amphenol Corporation Amphenol Taiwan Corporation			
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10	DESIGNED	JOAN LU	2024-12-09						
	CHECKED	Sondra Sang	2024-12-11			UNIT	mm	SIZE	A3
ANGULAR ±2°	APPROVED	Hank Hsu	2024-12-11			SCALE	NA	SHEET	9 OF 11
UNLESS OTHERWISE SPECIFIED	DWG TYPE	CUST DWG	PROJECT CODE	TXXX	PART No.		GH10V1XXXXXHR		
					DWG No.		GH10V1XXXXXHR		REV. AX6

A

B

C

D

E

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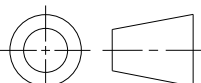
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REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	BY
AX4	NE-24178	ADDED GH10V11XXXXHR FRONT VIEW	2024-08-28	JOAN LU
AX5	NE-24203	UPDATED THE DISPLAY METHOD OF PIN OUT	2024-09-27	JOAN LU
AX6	NE-24270	1.DC CHANGED FROM 4 DIGIT DC TO 5 DIGIT DC 2.ADDED ALL HIGH SPEED CHANNELS OPTIONS INTO PNS 3.ADDED PACKING CODE 5	2024-12-10	JOAN LU

		148P PIN ASSIGNMENT															
		X24 SOLUTION PIN ASSIGNMENT															
		PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION			PIN NUMBER	PIN DEFINITION		PIN NUMBER	PIN DEFINITION				
B1		B1	GND		A1	GND		A1	B38		B38	GND		A38	GND		A38
		B2	TX		A2	RX					B39	TX		A39	RX		
		B3	TX		A3	RX					B40	TX		A40	RX		
B4		B4	GND		A4	GND		A4	B41		B41	GND		A41	GND		A41
		B5	TX		A5	RX					B42	TX		A42	RX		
		B6	TX		A6	RX					B43	TX		A43	RX		
B7		B7	GND		A7	GND		A7	B44		B44	GND		A44	GND		A44
		B8	TX		A8	RX					B45	TX		A45	RX		
		B9	TX		A9	RX					B46	TX		A46	RX		
B10		B10	GND		A10	GND		A10	B47		B47	GND		A47	GND		A47
		B11	TX		A11	RX					B48	TX		A48	RX		
		B12	TX		A12	RX					B49	TX		A49	RX		
B13		B13	GND		A13	GND		A13	B50		B50	GND		A50	GND		A50
		B14	TX		A14	RX					B51	TX		A51	RX		
		B15	TX		A15	RX					B52	TX		A52	RX		
B16		B16	GND		A16	GND		A16	B53		B53	GND		A53	GND		A53
		B17	TX		A17	RX					B54	TX		A54	RX		
		B18	TX		A18	RX					B55	TX		A55	RX		
B19		B19	GND		A19	GND		A19	B56		B56	GND		A56	GND		A56
		B20	TX		A20	RX					B57	TX		A57	RX		
		B21	TX		A21	RX					B58	TX		A58	RX		
B22		B22	GND		A22	GND		A22	B59		B59	GND		A59	GND		A59
		B23	TX		A23	RX					B60	TX		A60	RX		
		B24	TX		A24	RX					B61	TX		A61	RX		
B25		B25	GND		A25	GND		A25	B62		B62	GND		A62	GND		A62
		B26	TX		A26	RX					B63	TX		A63	RX		
		B27	TX		A27	RX					B64	TX		A64	RX		
B28		B28	GND		A28	GND		A28	B65		B65	GND		A65	GND		A65
		B29	TX		A29	RX					B66	TX		A66	RX		
		B30	TX		A30	RX					B67	TX		A67	RX		
B31		B31	GND		A31	GND		A31	B68		B68	GND		A68	GND		A68
		B32	TX		A32	RX					B69	TX		A69	RX		
		B33	TX		A33	RX					B70	TX		A70	RX		
B34		B34	GND		A34	GND		A34	B71		B71	GND		A71	GND		A71
		B35	TX		A35	RX					B72	TX		A72	RX		
		B36	TX		A36	RX					B73	TX		A73	RX		
B37		B37	GND		A37	GND		A37	B74		B74	GND		A74	GND		A74

TOLERANCE		DRAWN APPROVALS		DATE		TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR 0.6 PITCH, VERTICAL FOR PCIE 6.0,SMT TYPE		Amphenol® Amphenol Corporation Amphenol Taiwan Corporation					
X. X.X ±0.30 X.XX ±0.20 X.XXX ±0.10 ANGULAR ±2°		DESIGNED JOAN LU		2024-12-09									
		CHECKED Sondra Sang		2024-12-11									
		APPROVED Hank Hsu		2024-12-11									
UNLESS OTHERWISE SPECIFIED		DWG TYPE CUST DWG		PROJECT CODE TXXX				UNIT mm		SIZE A3		PART No. GH10V1XXXXXHR	
								SCALE NA		SHEET 10 OF 11		DWG No. GH10V1XXXXXHR	