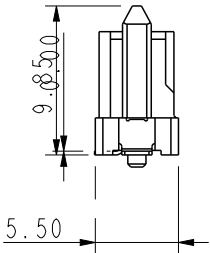
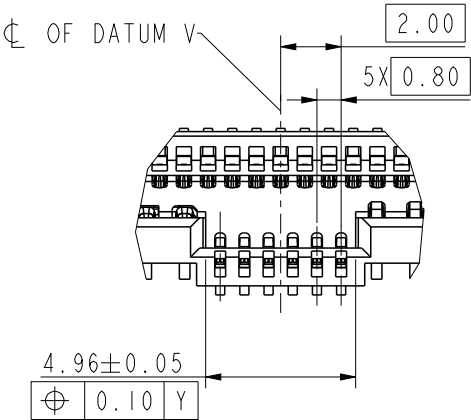
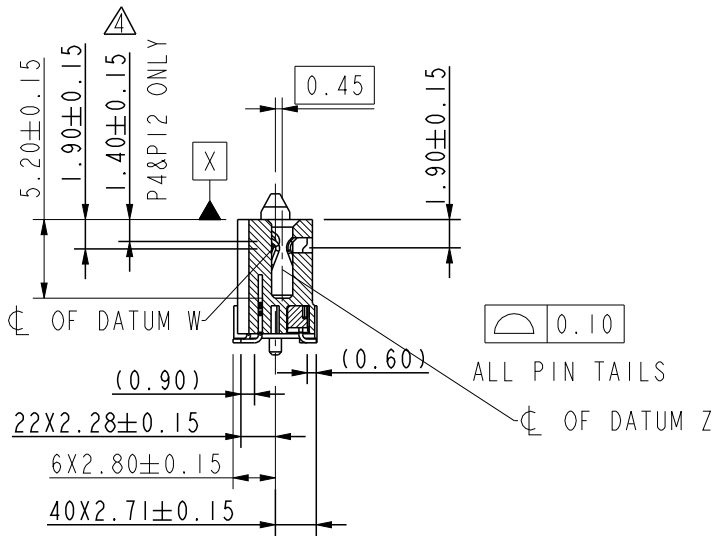
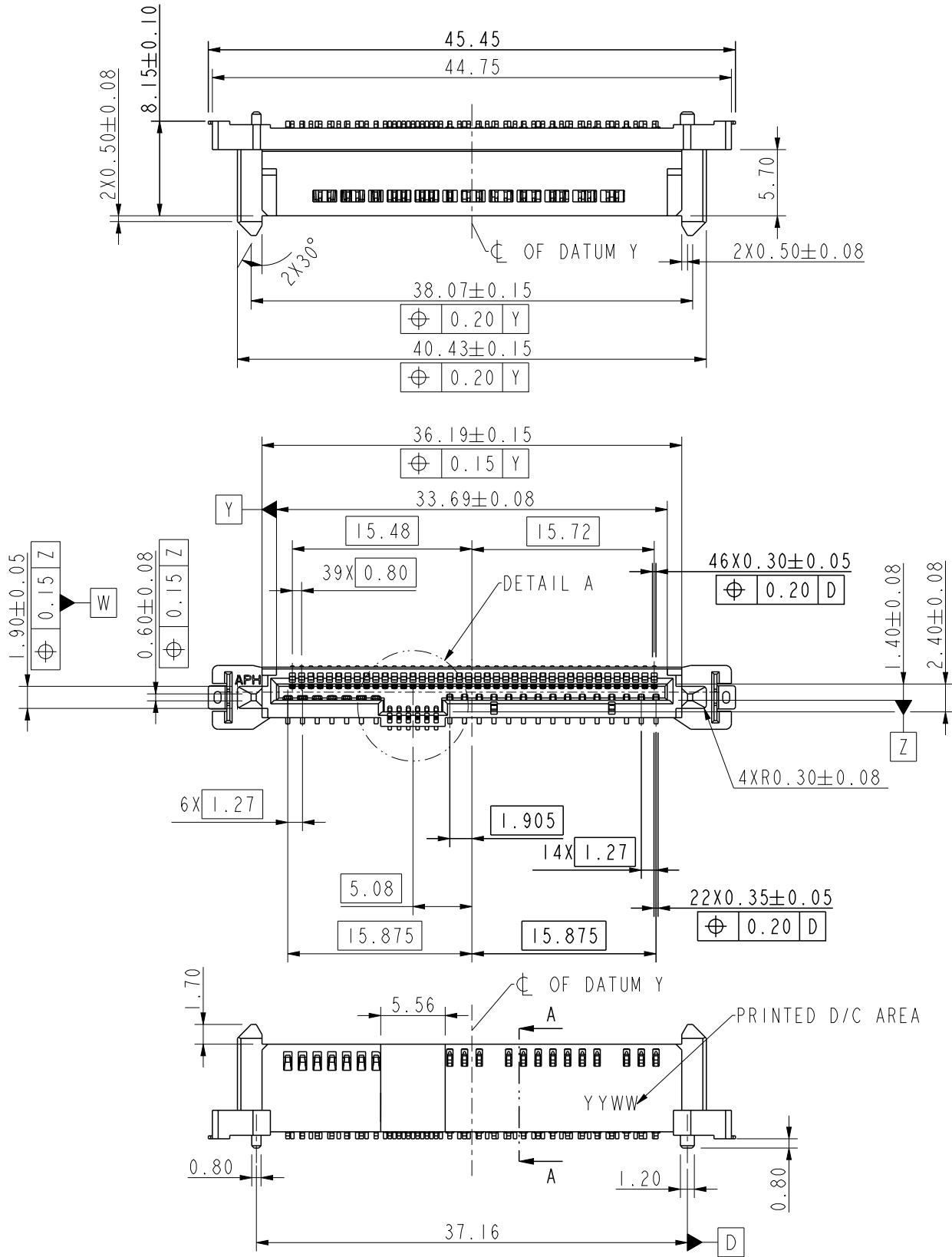


revision history				
rev	ecn no	description	dr	date
X1	ELX-CD-FXXXX-I	PROPOSAL DRAWING	Teng	2023/03/22
X2	ELX-CD-FXXXX-I	-55°C~+105°C WAS 0°C~+55°C	Teng	2023/04/19
A	ELX-CD-F3182-I	FIRST RELEASE	Teng	2023/04/25



SECTION A
SCALE 4:1

spec ref				dr		Teng Cao		2023/07/19		Amphenol				mm		scale		2:1		size		A3					
tolerance std		TOLERANCES UNLESS OTHERWISE SPECIFIED				eng		Teng Cao						2023/07/20		ecn no											
						rvwr		Feng, Alice(CD)						2023/07/19		amphenol-icc.com											
						appr		XD Hu						2023/07/20								rel level					
surface		✓		projection				title		PCIe/SAS VERTICAL SMT						drag no		PSAS5F313022XXX						rev		A	
		linear		0.X		±0.50																					
				0.XX		±0.25																					
				0.XXX		±0.20																					
		angular		0°		±3°																					
								product family				PSAS						sheet 1 of 3									

revision history				
rev	ecn no	description	dr	date
-	----	REFER TO SHEET 1/3	-	-

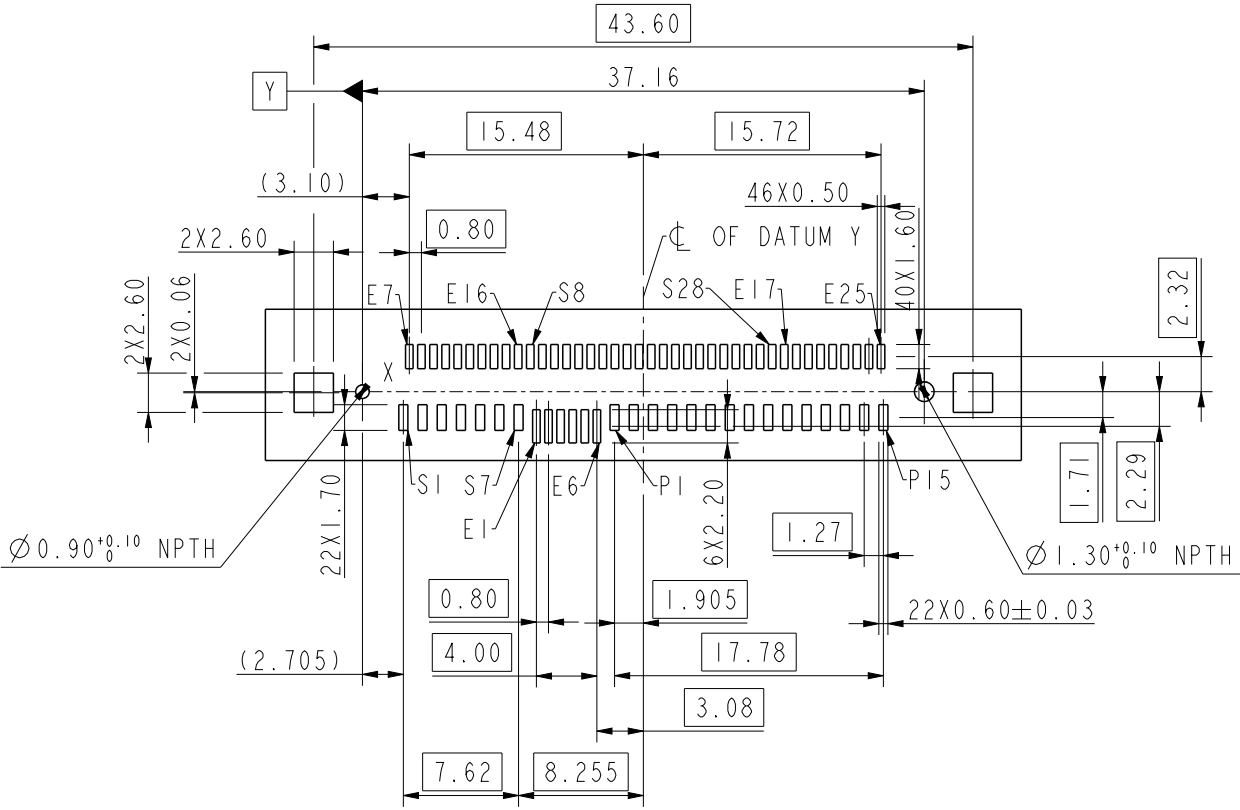
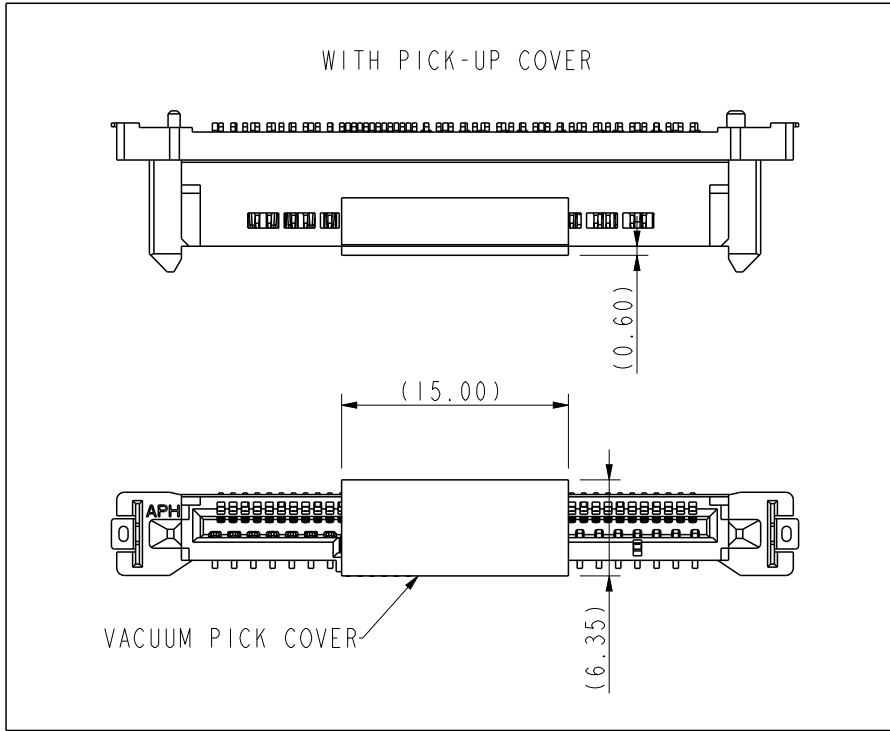
- NOTE:
- 1. MATERIAL:
 - 1.1 HOUSING: HIGH TEMPERATURE THERMOPLASTIC,BLACK,UL94-V0,HALOGEN FREE.
 - 1.2 CONTACT: PHOSPHOR BRONZE
 - 1.3 BOARD LOCK: COPPER ALLOY
 - 2. PLATING:
 - 2.1 CONTACTS:MATING AREA PLEASE REFER TO PN SYSTEM,1.27um MIN Ni UNDERPLATED
SOLDER TAIL 2.54um MIN MATTE TIN,1.27um MIN Ni UNDERPLATED
 - 2.2 BOARD LOCK:2.54um MIN MATTE TIN 1.27um MIN Ni UNDERPLATED
 - 3. ELECTRICAL PERFORMANCE:
 - 3.1 CURRENT RATING: 1.5A/PER PIN (POWER SECTION).
 - 3.2 CONTACT RESISTANCE: 30mΩ MAX (INITIAL)
 - 3.3 INSULATION RESISTANCE: 1000MΩ MIN
 - 3.4 DIELECTRIC STRENGTH: 500 VAC FOR 1 MINUTE
 - 3.5 OPERATION TEMPERATURE: -55°C~+105°C
 - 4. PREMATING AT P4 & P12.
 - 5. SUPPORT PCIe GEN5.
 - 6. ALL GND PINS CONNECT TOGETHER:
E9,E12,E15,S8,S11,S14,S16,S19,S22,S25,S28,E19,E22
 - 7. MATERIAL SHOULD BE FULFILLED AMPHENOL SPEC#S-SN-002 AND SPEC#S-SN-004
 - 8. ORDER P/N SYSTEM: PSAS5F3I3022XXX

MATING AREA PLATING PACKING SYSTEM

1- 0.76um MIN Au. C- TRAY WITH PICK-UP CAP

2- 0.38um MIN Au. TR- TAPE AND REEL WITH PICK-UP CAP

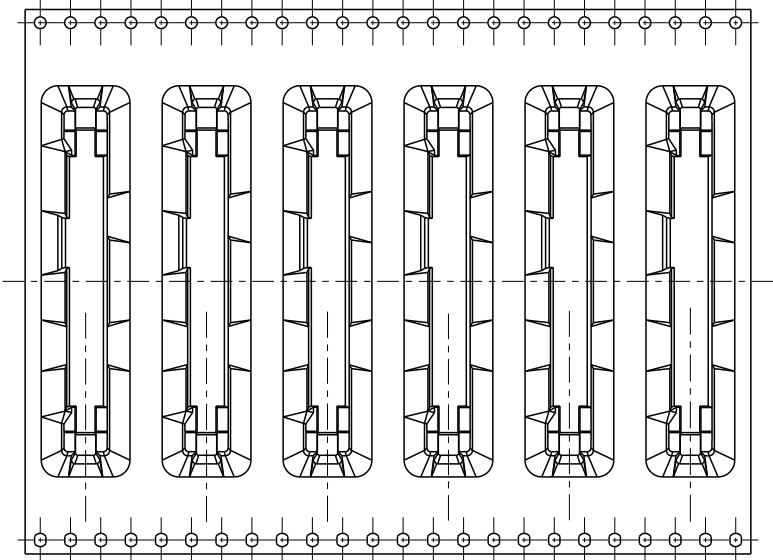
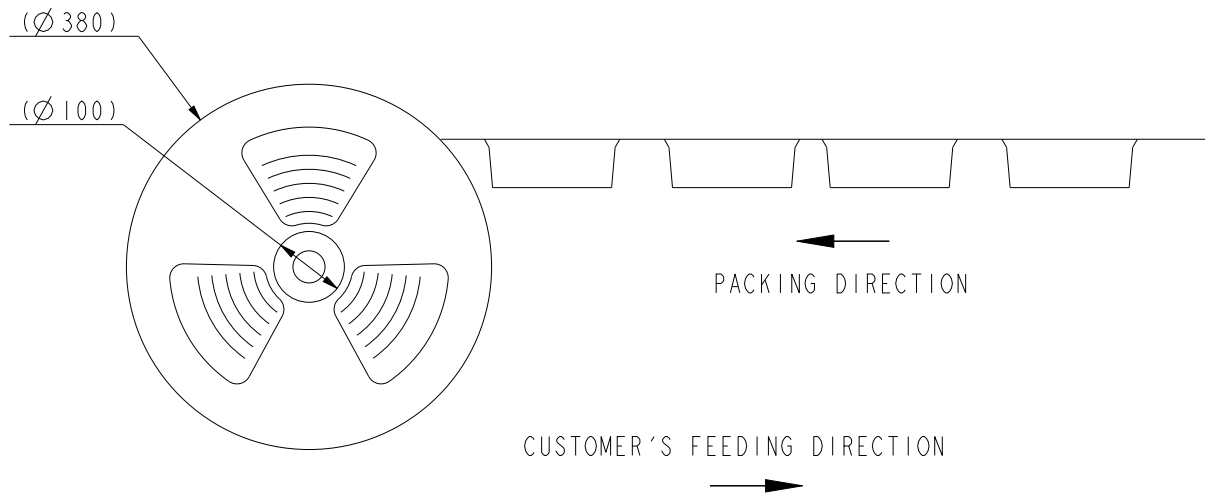
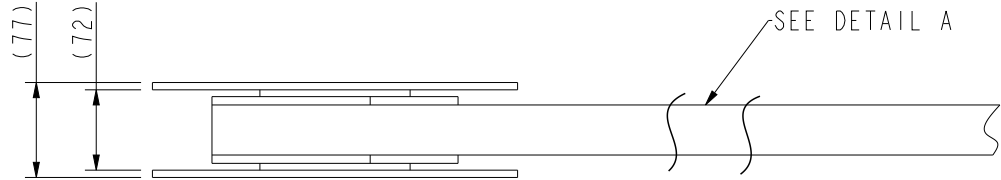
3- GOLD FLASH NT- TAPE AND REEL WITHOUT PICK-UP CAP



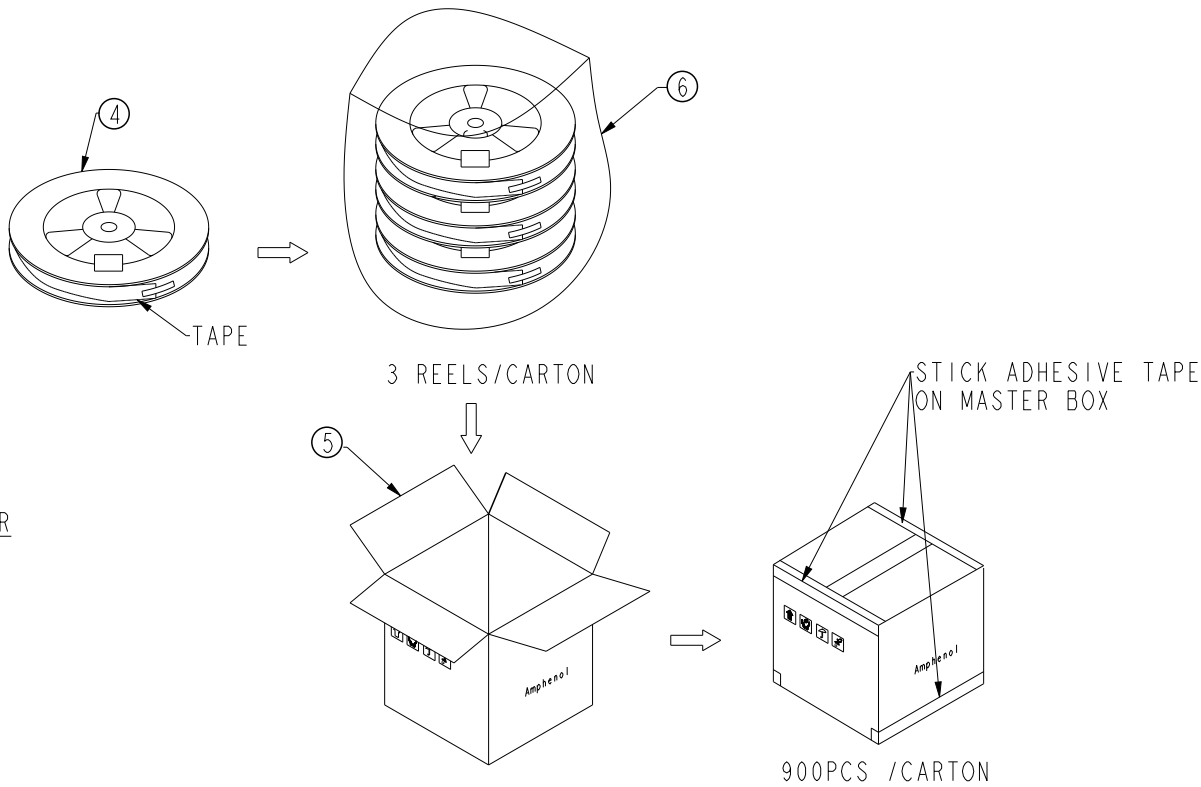
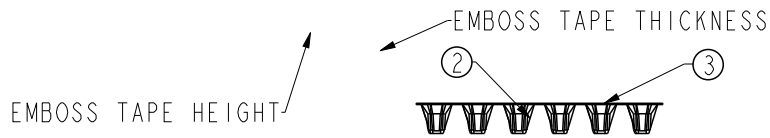
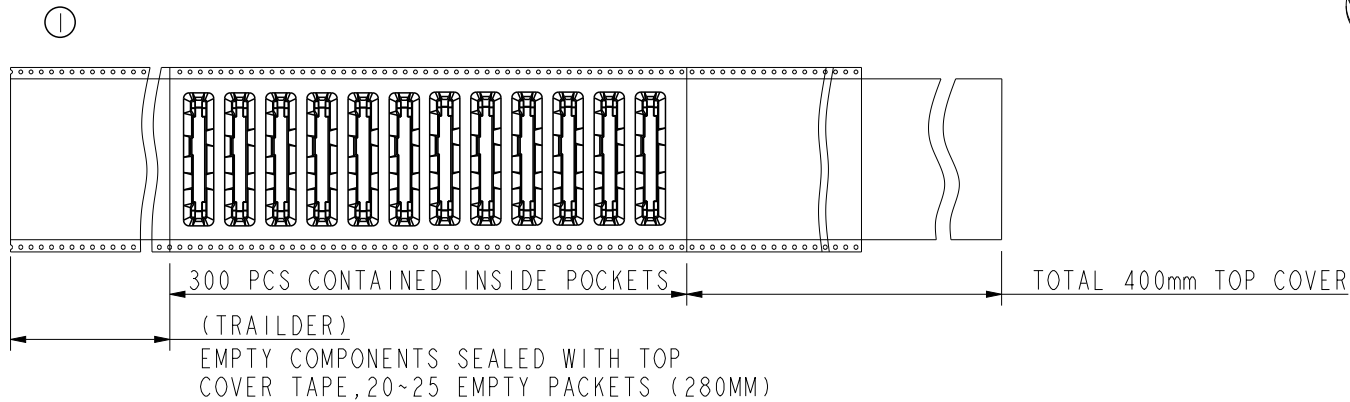
RECOMMENDED PCB LAYOUT(TOP VIEW)
GENERAL TOLERANCE ±0.05(PCB THICKNESS:1.0~2.0MM)

spec ref				dr		Teng Cao		2023/07/19		Amphenol				mm		scale		2:1		size		A3					
tolerance std		TOLERANCES UNLESS OTHERWISE SPECIFIED				eng		Teng Cao						2023/07/20		ecn no											
						rvwr		Feng, Alice(CD)						2023/07/19		amphenol-icc.com											
						appr		XD Hu						2023/07/20													
surface		✓		linear		0.X		±0.50		projection		title		PCIe/SAS VERTICAL SMT				drag no		PSAS5F3I3022XXX				rev		A	
						0.XX		±0.25																			
						0.XXX		±0.20																			
						angular		0°		±3°								product family				PSAS					
														cat. no.													

revision history				
rev	ecn no	description	dr	date
-	- - - -	REFER TO SHEET 1/3	-	-



SECLA 1:1
DETAIL A



6	PLASTIC BAG	NA
5	MASTER BOX	CORRUGATED FIBERBOARD
4	PP REEL	PP
3	TOP COVER TAPE	PET
2	EMBOSS TAPE	POLYSTYRENE
1	CONNECTOR	NA
ITEM	DESCRIPTION	MATERIAL

spec ref				dr		Teng Cao		2023/07/19		Amphenol				mm		scale		2:1		size		A3																	
tolerance std				TOLERANCES UNLESS OTHERWISE SPECIFIED				eng						Teng Cao		2023/07/20		ecn no																					
								rvwr						Feng, Alice(CD)		2023/07/19																							
								appr						XD Hu		2023/07/20		amphenol-icc.com				rel level								Released									
surface				linear				projection				title				PCIe/SAS VERTICAL SMT				drag no		PSAS5F313022XXX								rev		A							
																				cat. no.																			
																				product family				PSAS				sheet 3 of 3											
																				0.X				±0.50															
																				0.XX				±0.25															
0.XXX				±0.20																																			
angular				0°				±3°																															