EGP10A - EGP10K

Features

- Superfast recovery time for high efficiency.
- Low forward voltage, high current capability.
- Low leakage current.
- High surge current capability.



DO-41

Fast Rectifiers (Glass Passivated)

Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter	Value								Units
-		10A	10B	10C	10D	10F	10G	10J	10K	
V_{RRM}	Maximum Repetitive Reverse Voltage	50	100	150	200	300	400	600	800	V
I _{F(AV)}	Average Rectified Forward Current, .375 " lead length @ T ₁ = 55°C			•	Α					
I _{FSM}	Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave		30						Α	
T _{stg}	Storage Temperature Range		-65 to +150							°C
T _J	Operating Junction Temperature -65 to +150				°C					

 $[\]hbox{^{\bigstar}} These \ ratings \ are \ limiting \ values \ above \ which \ the \ service ability \ of \ any \ semiconductor \ device \ may \ be \ impaired.$

P_D	Power Dissipation	2.5	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	50	°C/W

Electrical Characteristics T_A = 25°C unless otherwise noted

Symbol	Parameter	Device								Units
•		10A	10B	10C	10D	10F	10G	10J	10K	
V_{F}	Forward Voltage @ 1.0 A	0.95			1.25		1.7		V	
t _{rr}	Reverse Recovery Time $I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		50						' 5	ns
I _R	Reverse Current @ rated V_R $T_A = 25$ °C $T_A = 125$ °C	5.0 100			μA μA					
C_T	Total Capacitance V _R = 4.0 V, f = 1.0 MHz		22			15				pF

Typical Characteristics

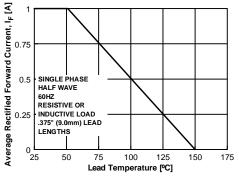
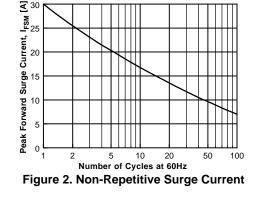


Figure 1. Forward Current Derating Curve



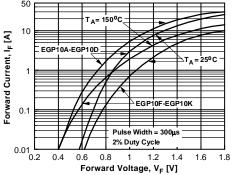


Figure 3. Forward Voltage Characteristics

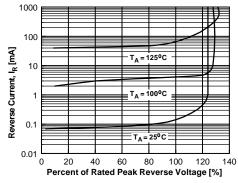


Figure 4. Reverse Current vs Reverse Voltage

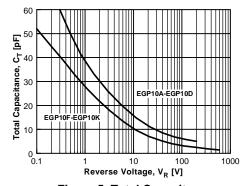
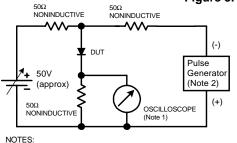


Figure 5. Total Capacitance



1. Rise time = 7.0 ns max; Input impedance = 1.0 megaohm 22 pf. 2. Rise time = 10 ns max; Source impedance = 50 ohms. -1.0A --- 1.0cm --- SET TIME BASE FOR 5/10 ns/ cm

Reverse Recovery Time Characterstic and Test Circuit Diagram

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

SMART START™ VCX^{TM} FAST ® OPTOLOGIC™ STAR*POWER™ FASTr™ Bottomless™ OPTOPLANAR™ Stealth™ CoolFET™ FRFET™ PACMAN™ SuperSOT™-3 CROSSVOLT™ GlobalOptoisolator™ POP™ SuperSOT™-6 DenseTrench™ GTO™ Power247™ $HiSeC^{TM}$ SuperSOT™-8 $Power Trench^{\, @}$ DOME™ SyncFET™ EcoSPARK™ ISOPLANAR™ QFET™ TinyLogic™ E²CMOSTM LittleFET™ OS^{TM}

QT Optoelectronics™

MicroFET™

STAR*POWER is used under license

DISCLAIMER

EnSigna™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

TruTranslation™

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification Product Status		Definition						
Advance Information Formative or In Design		This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.						
Preliminary First Production		This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.						
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.						
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.						

Rev. H4