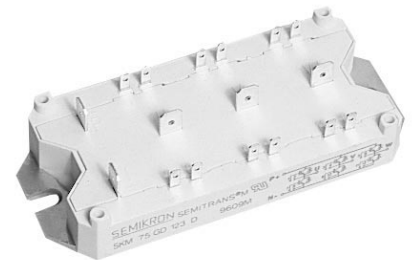


Absolute Maximum Ratings		Values	Units
Symbol	Conditions <sup>1)</sup>		
V <sub>CES</sub>		1200	V
V <sub>CGR</sub>	R <sub>GE</sub> = 20 kΩ	1200	V
I <sub>C</sub>	T <sub>case</sub> = 25/80 °C	25 / 15	A
I <sub>CM</sub>	T <sub>case</sub> = 25/80 °C; t <sub>p</sub> = 1 ms	50 / 30	A
V <sub>GES</sub>		± 20	V
P <sub>tot</sub>	per IGBT, T <sub>case</sub> = 25 °C	145	W
T <sub>j</sub> , (T <sub>stg</sub> )		- 40 ... +150 (125)	°C
V <sub>isol</sub>	AC, 1 min.	2 500	V
humidity	DIN 40 040	Class F	
climate	DIN IEC 68 T.1	40/125/56	
<b>Inverse Diode</b>			
I <sub>F</sub> = - I <sub>C</sub>	T <sub>case</sub> = 25/80 °C	25 / 15	A
I <sub>FM</sub> = - I <sub>CM</sub>	T <sub>case</sub> = 25/80 °C; t <sub>p</sub> = 1 ms	50 / 30	A
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms; sin.; T <sub>j</sub> = 150 °C	200	A
I <sub>t</sub> <sup>2</sup>	t <sub>p</sub> = 10 ms; T <sub>j</sub> = 150 °C	200	A <sup>2</sup> s

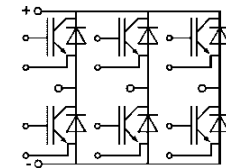
Characteristics					
Symbol	Conditions <sup>1)</sup>	min.	typ.	max.	Units
V <sub>(BR)GES</sub>	V <sub>GE</sub> = 0, I <sub>C</sub> = 0,5 mA	≥ V <sub>CES</sub>	-	-	V
V <sub>GE(th)</sub>	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub> = 1 mA	4,5	5,5	6,5	V
I <sub>CES</sub>	V <sub>GE</sub> = 0 } T <sub>j</sub> = 25 °C	-	0,3	0,5	mA
		V <sub>CE</sub> = V <sub>CES</sub> } T <sub>j</sub> = 125 °C	-	1,8	-
I <sub>GES</sub>	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0	-	-	150	nA
V <sub>CEsat</sub>	I <sub>C</sub> = 15 A } V <sub>GE</sub> = 15 V; } T <sub>j</sub> = 25 (125) °C	-	2,5(3,1)	3(3,7)	V
V <sub>CEsat</sub>		I <sub>C</sub> = 22 A	-	3(3,7)	-
g <sub>fs</sub>	V <sub>CE</sub> = 20 V, I <sub>C</sub> = 15 A	-	12	-	S
C <sub>CHC</sub>	per IGBT	-	-	300	pF
C <sub>ies</sub>	V <sub>GE</sub> = 0 } V <sub>CE</sub> = 25 V } f = 1 MHz	-	1000	-	pF
C <sub>oes</sub>		-	150	-	pF
C <sub>res</sub>		-	70	-	pF
L <sub>CE</sub>		-	-	60	nH
t <sub>d(on)</sub>	V <sub>CC</sub> = 600 V } V <sub>GE</sub> = + 15 V / - 15 V <sup>3)</sup> } I <sub>C</sub> = 15 A, ind. load } R <sub>Gon</sub> = R <sub>Goff</sub> = 52 Ω } T <sub>j</sub> = 125 °C	-	40	-	ns
t <sub>r</sub>		-	35	-	ns
t <sub>d(off)</sub>		-	350	-	ns
t <sub>f</sub>		-	70	-	ns
E <sub>on</sub> <sup>5)</sup>		-	2	-	mWs
E <sub>off</sub> <sup>5)</sup>		-	1,4	-	mWs
<b>Inverse Diode <sup>8)</sup></b>					
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 15 A } V <sub>GE</sub> = 0 V; } T <sub>j</sub> = 25 (125) °C	-	2,0(1,8)	2,5	V
V <sub>F</sub> = V <sub>EC</sub>		I <sub>F</sub> = 25 A	-	2,3(2,1)	-
V <sub>TO</sub>	T <sub>j</sub> = 125 °C	-	1,1	1,2	V
r <sub>T</sub>	T <sub>j</sub> = 125 °C	-	45	70	mΩ
I <sub>RR</sub>	I <sub>F</sub> = 15 A; T <sub>j</sub> = 25 (125) °C <sup>2)</sup>	-	12(16)	-	A
Q <sub>rr</sub>	I <sub>F</sub> = 15 A; T <sub>j</sub> = 25 (125) °C <sup>2)</sup>	-	1(2,7)	-	μC
<b>Thermal Characteristics</b>					
R <sub>thjc</sub>	per IGBT	-	-	0,86	°C/W
R <sub>thjc</sub>	per diode <sup>8)</sup>	-	-	1,5	°C/W
R <sub>thch</sub>	per module	-	-	0,05	°C/W

## SEMITRANS® M IGBT Modules

**SKM 22 GD 123 D**  
**SKM 22 GD 123 D L\*)**



### Sixpack



GD

### Features

- MOS input (voltage controlled)
- N channel, homogeneous Si
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to 6 \* I<sub>Cnom</sub>
- Latch-up free
- Fast & soft inverse CAL diodes<sup>8)</sup>
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (9 mm) and creepage distances (13 mm).

### Typical Applications

- Switched mode power supplies
- Three phase inverters for AC motor speed control
- General power switching applications
- Pulse frequencies also above 15 kHz

<sup>1)</sup> T<sub>case</sub> = 25 °C, unless otherwise specified

<sup>2)</sup> I<sub>F</sub> = - I<sub>C</sub>, V<sub>R</sub> = 600 V, - di<sub>F</sub>/dt = 400 A/μs, V<sub>GE</sub> = 0 V

<sup>3)</sup> Use: V<sub>GEoff</sub> = -5 ... -15 V

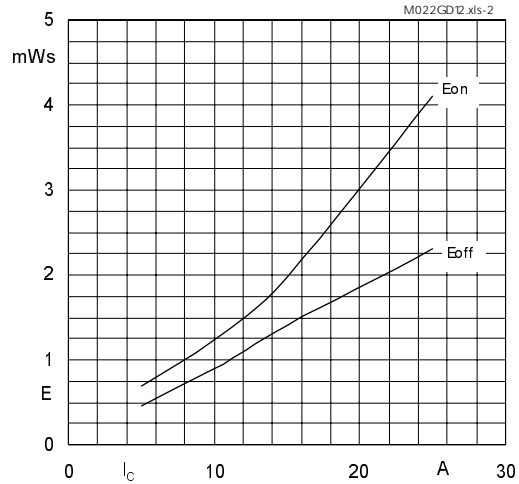
<sup>5)</sup> See fig. 2 + 3; R<sub>Goff</sub> = 52 Ω

<sup>8)</sup> CAL = Controlled Axial Lifetime Technology.

**\*) Main terminals = 2 mm dia. Cases and mech. data → B6 - 68 Sixpack**

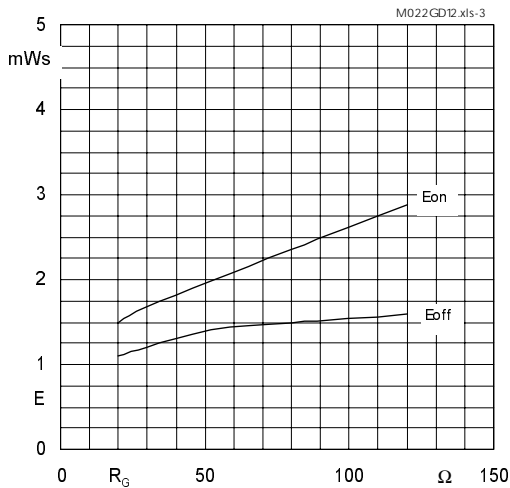


Fig. 1 Rated power dissipation  $P_{tot} = f(T_C)$



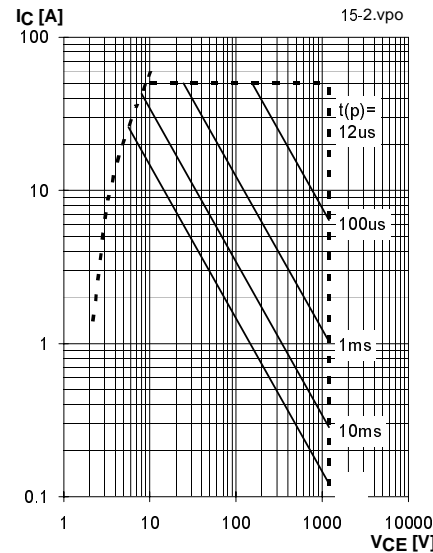
$T_j = 125\text{ °C}$   
 $V_{CE} = 600\text{ V}$   
 $V_{GE} = \pm 15\text{ V}$   
 $R_G = 52\text{ }\Omega$

Fig. 2 Turn-on /-off energy  $= f(I_C)$



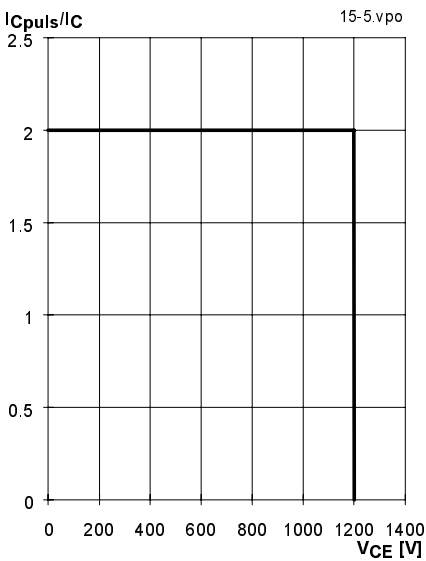
$T_j = 125\text{ °C}$   
 $V_{CE} = 600\text{ V}$   
 $V_{GE} = \pm 15\text{ V}$   
 $I_C = 15\text{ A}$

Fig. 3 Turn-on /-off energy  $= f(R_G)$



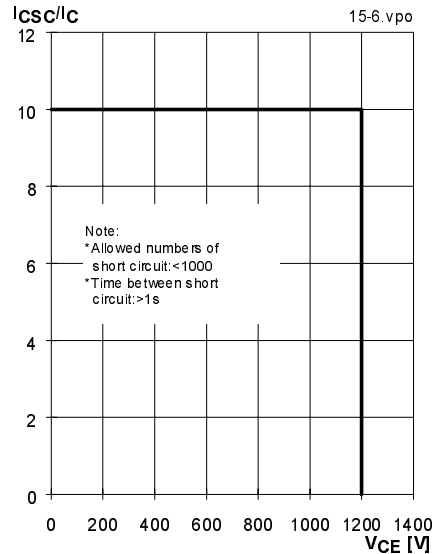
1 pulse  
 $T_C = 25\text{ °C}$   
 $T_j \leq 150\text{ °C}$

Fig. 4 Maximum safe operating area (SOA)  $I_C = f(V_{CE})$



$T_j \leq 150\text{ °C}$   
 $V_{GE} = \pm 15\text{ V}$   
 $R_{Goff} = 52\text{ }\Omega$   
 $I_C = 15\text{ A}$

Fig. 5 Turn-off safe operating area (RBSOA)



$T_j \leq 150\text{ °C}$   
 $V_{GE} = \pm 15\text{ V}$   
 $t_{sc} \leq 10\text{ ms}$   
 $L < 25\text{ nH}$   
 $I_{CN} = 15\text{ A}$

Fig. 6 Safe operating area at short circuit  $I_C = f(V_{CE})$

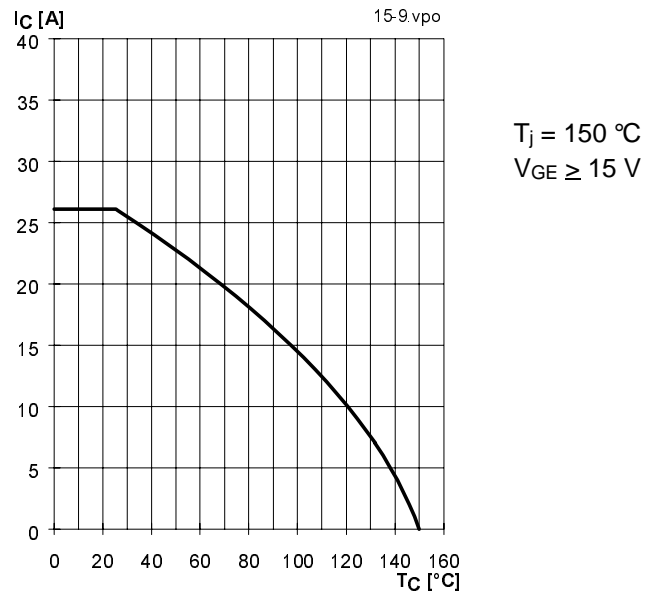


Fig. 8 Rated current vs. temperature  $I_c = f(T_c)$

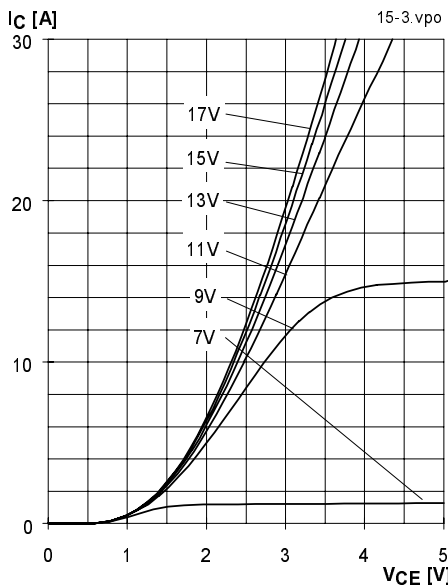


Fig. 9 Typ. output characteristic,  $t_p = 80 \mu s$ ;  $25 \text{ }^\circ\text{C}$

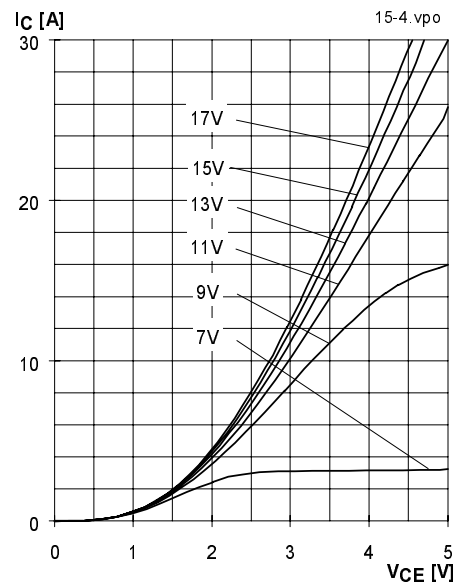


Fig. 10 Typ. output characteristic,  $t_p = 80 \mu s$ ;  $125 \text{ }^\circ\text{C}$

$$P_{\text{cond}}(t) = V_{\text{CEsat}}(t) \cdot I_c(t)$$

$$V_{\text{CEsat}}(t) = V_{\text{CE(TO)(Tj)}} + r_{\text{CE(Tj)}} \cdot I_c(t)$$

$$V_{\text{CE(TO)(Tj)}} \leq 1,5 + 0,002 (T_j - 25) \text{ [V]}$$

$$\text{typ.: } r_{\text{CE(Tj)}} = 0,067 + 0,00027 (T_j - 25) \text{ [\Omega]}$$

$$\text{max.: } r_{\text{CE(Tj)}} = 0,100 + 0,00033 (T_j - 25) \text{ [\Omega]}$$

$$\text{valid for } V_{\text{GE}} = +15 \begin{matrix} +2 \\ -1 \end{matrix} \text{ [V]; } I_c > 0,3 I_{\text{Cnom}}$$

Fig. 11 Saturation characteristic (IGBT)  
Calculation elements and equations

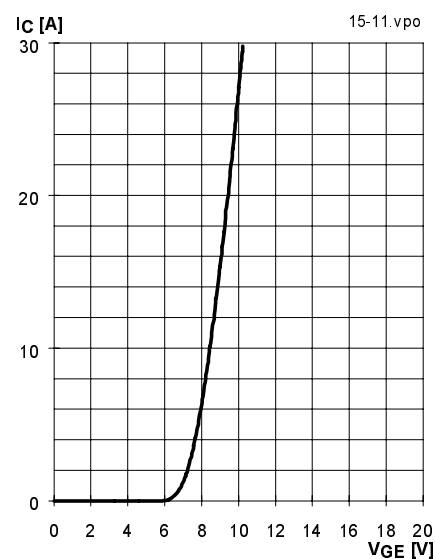


Fig. 12 Typ. transfer characteristic,  $t_p = 80 \mu s$ ;  $V_{\text{CE}} = 20 \text{ V}$

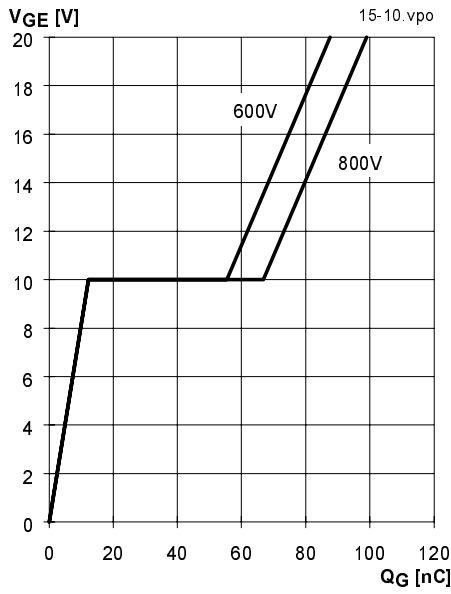
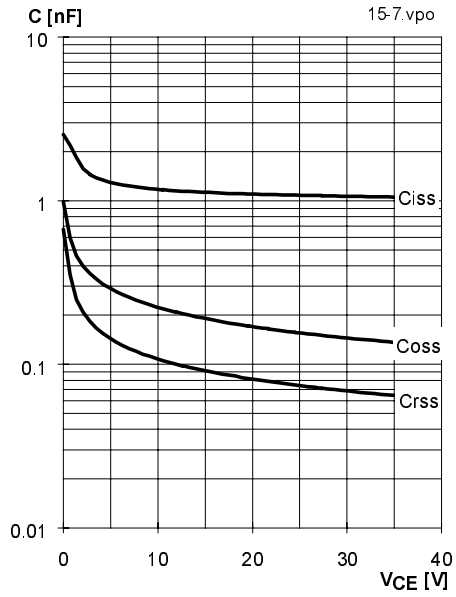


Fig. 13 Typ. gate charge characteristic

$I_{Cpuls} = 15 \text{ A}$



$V_{GE} = 0 \text{ V}$   
 $f = 1 \text{ MHz}$

Fig. 14 Typ. capacitances vs.  $V_{CE}$

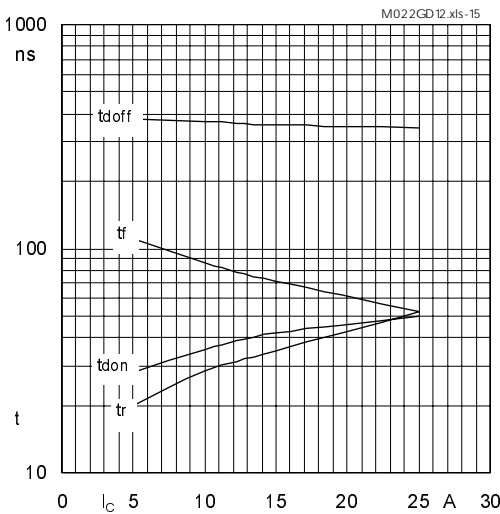
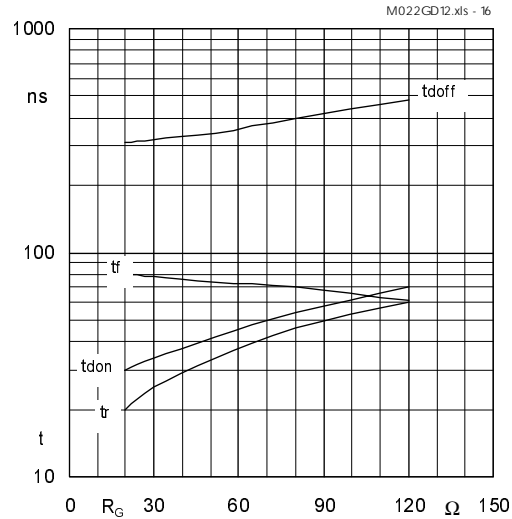


Fig. 15 Typ. switching times vs.  $I_C$

$T_j = 125 \text{ }^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{Gon} = 52 \text{ } \Omega$   
 $R_{Goff} = 52 \text{ } \Omega$   
induct. load



$T_j = 125 \text{ }^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 15 \text{ A}$   
induct. load

Fig. 16 Typ. switching times vs. gate resistor  $R_G$

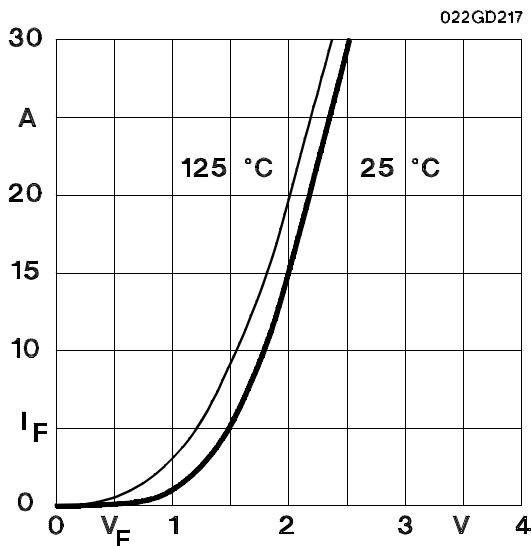


Fig. 17 Typ. CAL diode forward characteristic

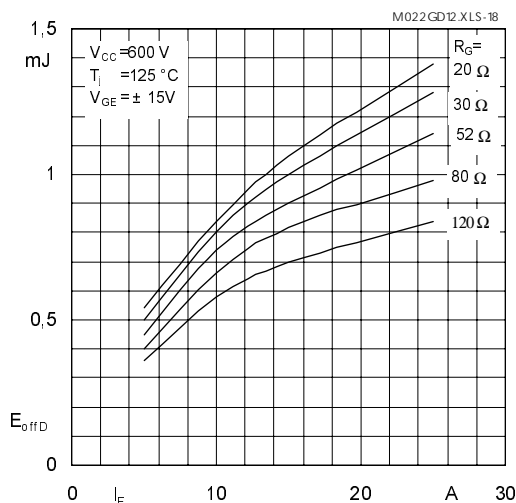


Fig. 18 Diode turn-off energy dissipation per pulse

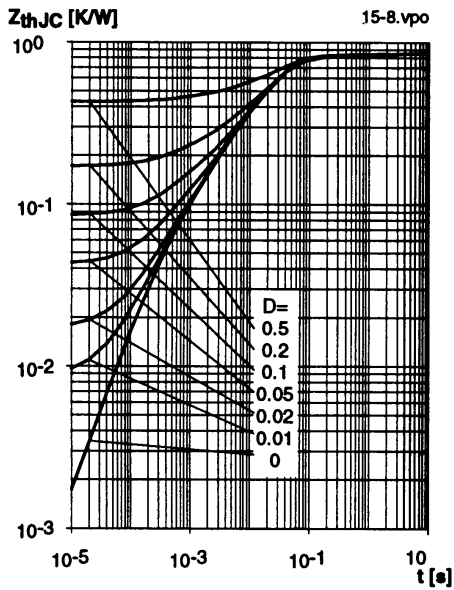


Fig. 19 Transient thermal impedance of IGBT  
 $Z_{thJC} = f(t_p)$ ;  $D = t_p / t_c = t_p \cdot f$

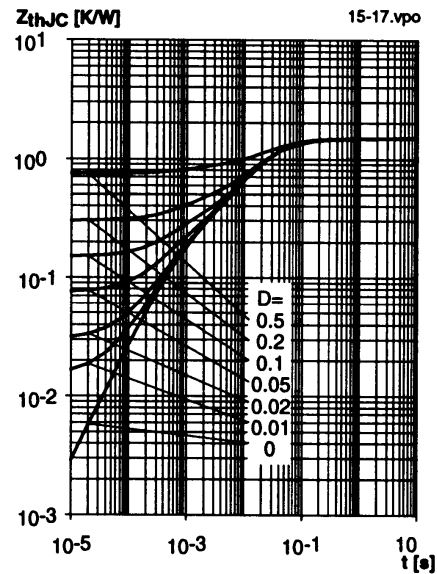


Fig. 20 Transient thermal impedance of inverse CAL diodes  
 $Z_{thJC} = f(t_p)$ ;  $D = t_p / t_c = t_p \cdot f$

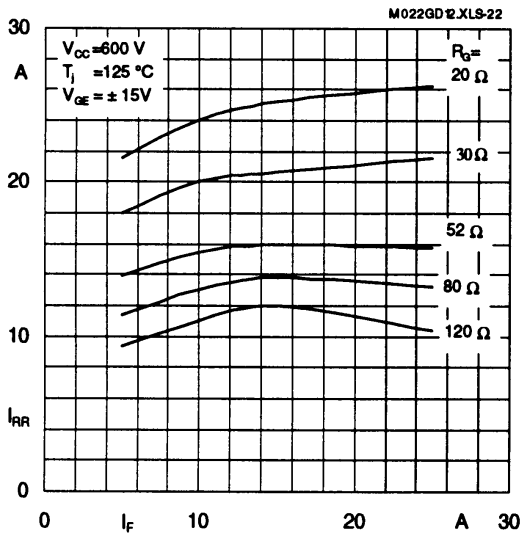


Fig. 22 CAL diode peak reverse recovery current  
 $I_{RR} = f(I_F; R_G)$

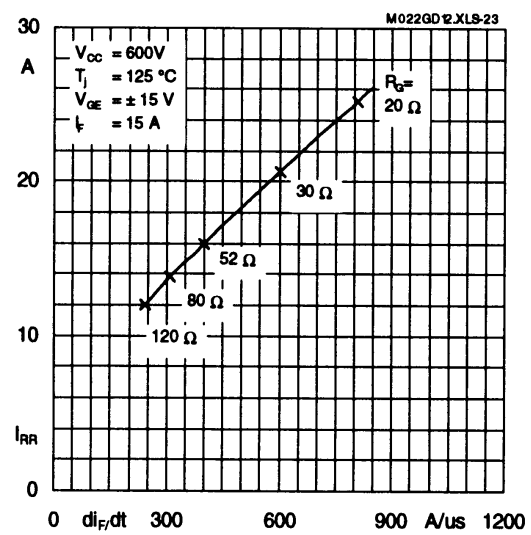


Fig. 23 CAL diode peak reverse recovery current  
 $I_{RR} = (di/dt)$

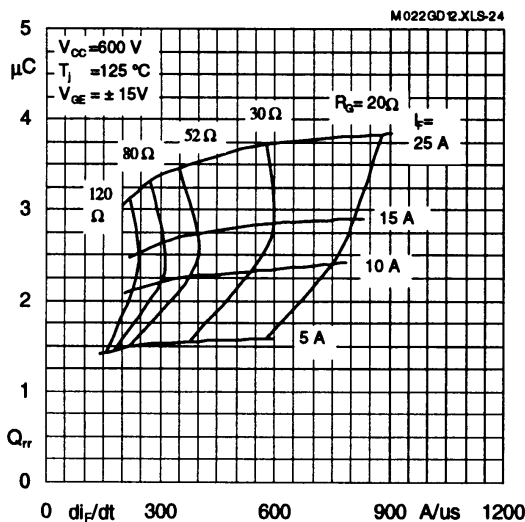


Fig. 24 CAL diode recovered charge  $Q_{rr} = f(di/dt)$

# SKM 22 GD 123 D ...

## SEMITRANS Sixpack

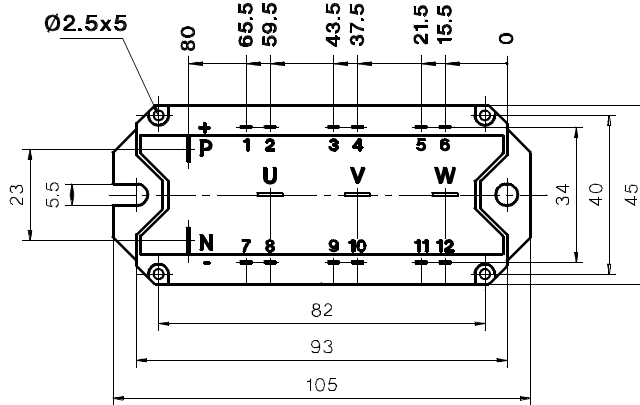
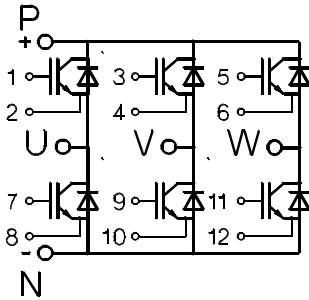
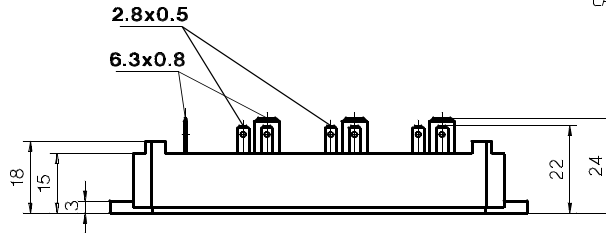
Case D 67

UL Recognized

File no. E 63 532

**SKM 22 GD 123 D**

CASED67



## SEMITRANS Sixpack

Case D 68

UL Recognized

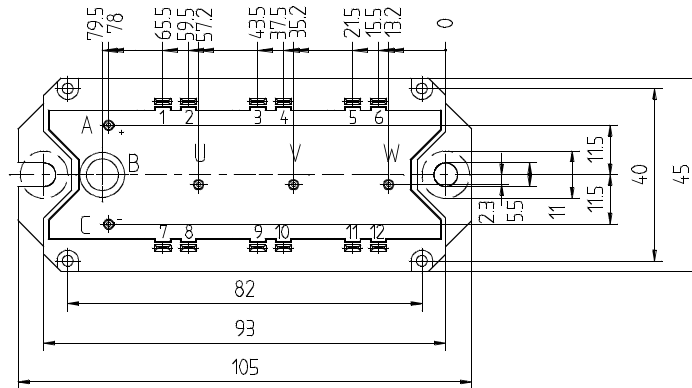
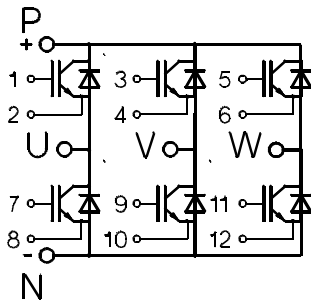
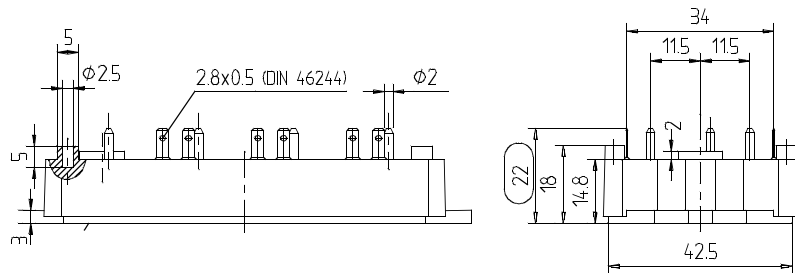
Special version on request

**SKM 22 GD 123 DL**

**SKM 40 GD 123 DL**

**SKM 75 GD 123 DL**

CASED68



Dimensions in mm

Case outlines and circuit diagrams

Mechanical Data			Values			Units
Symbol	Conditions		min.	typ.	max.	
M <sub>1</sub>	to heatsink, SI Units to heatsink, US Units	(M5)	4	—	5	Nm lb.in.
a			—	—	5x9,81	m/s <sup>2</sup>
w			—	—	175	g

**This is an electrostatic discharge sensitive device (ESD). Please observe the international standard IEC 747-1, Chapter IX.**

Two devices are supplied in one SEMIBOX A. Larger packing units (10 and 20 pieces) are used if suitable. SEMIBOX → C - 1.