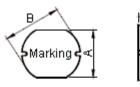
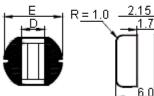
	PART NO.			REVISIONS						
	F F	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
		-	А	RELEASED	Arun	10/2/11	Jagan	10/2/11	Farnell	24/2/11

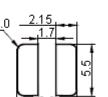
Configurations and Dimensions





Top View Side View





Bottom View

Suggest PCB Layout

Dimensions : Millimetres

Marking : 560

Electrical Characteristics

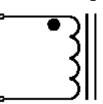
(at 25°C)

Test Condition		
100KHz 0.25V	L	56μH ±10%
at 25°C	DCR	240mΩ (Maximum)
100KHz 0.25V I _{rms} = 0.94A	ΔΤ	Temperature Rise 40°C (Maximum)

Operating temperature : -55°C to +130°C

A	7 ±0.3 mm	-
В	7.8 ±0.3 mm	-
С	5 ±0.5 mm	-
D	3 mm	(Reference)
E	8 ±0.5 mm	-

Schematic Diagram





Note:

(1) Wire Ø0.3mm x 1P 2UEWF 155°C (2) 38.5TS (Reference)

Test Data for Mechanical

Test Item	A mm	B mm	C mm	D mm	E mm
Specification	7 ±0.3	7.8 ±0.3	5 ±0.5	3 (Reference)	8 ±0.5
1	7.03	7.83	5.03	2.37	7.8
2	7	7.81	5.04	2.51	7.69
3	/	7.8	5.03	2.5	7.87
4	7.02	7.82	5.06	2.57	7.8
5	7	7.8	5.04	2.34	7.76
Average	7.01	7.81	5.04	2.46	7.78

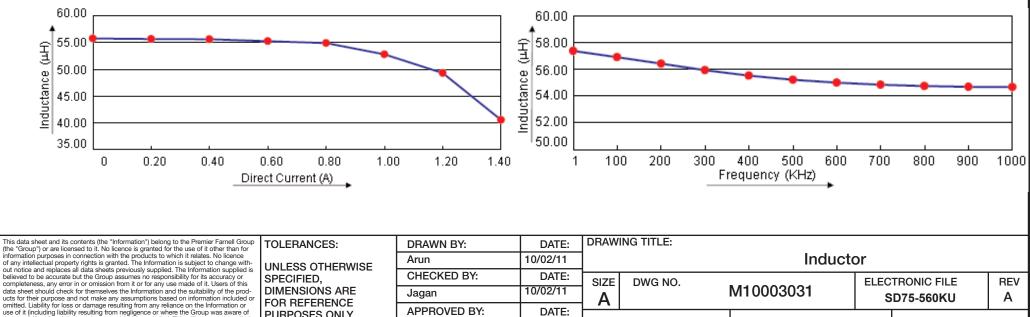
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lieved to be accurate but the Group assumes no responsibility for its accuracy or moleteness, any error in or omission from it or for any use made of it. Users of this	SPECIFIED,	CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV
data sheet should check for themselves the Information and the suitability of the prod- ucts for their purpose and not make any assumptions based on information included or	DIMENSIONS ARE	Jagan	10/02/11	Δ	M10003031	SD75-560KU	Α
childred Edulity for food of during room any foliation of the montation of		APPROVED BY:	DATE:				
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	PABT NO.			REVISIONS						
🐼 multicomp	MCSD75-560KU	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
		-	А	RELEASED	Arun	10/2/11	Jagan	10/2/11	Farnell	24/2/11

Test Data for Electrical

Test Item	L µH	DCR mΩ	ΔΤ
Condition	100KHz 0.25V	at 25°C	100KHz 0.25V I _{rms} = 0.94A
Specification	56 ±10%	240 (Maximum)	Temperature Rise 40°C (Maximum)
1	55.9	188	OK
2	55.57	193	OK
3	54.76	187	OK
4	54.84	188	OK
5	55.9	191	OK
Average	55.39	189.4	ОК

Electric Characteristics



24/02/11

SCALE: NTS

SHEET: 2 OF 3

U.O.M.: mm

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Farnell

	PART NO.		REVISIONS								
🔊 multicomp	-	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE	
	MCSD75-560KU	-	А	RELEASED	Arun	10/2/11	Jagan	10/2/11	Farnell	24/2/11	

Reliability Test

Test Item	Specifications	Test Method and Remarks
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat.
Storage condition	Ambient temperature: 0°C to 40°CHumidity: Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.
Moisture sensitivity	Appearance: No abnormality No damageDCR change: Within ±20%Inductance change: Within ±20%	According to J-STD-020B level 3 Test condition : 60°C 60% RH Test duration : 40 hours Recovery : 1 to 2 hours of recovery under the standard condition after the removal from the test chamber.
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B Steam aging category : 97°C 98% RH Steam aging duration : 8 hours Solder : Lead-free solder Solder temperature : 260 ±5°C Dip time : 5 +0/-0.5 seconds.

Material List

No.	ltem	Material Description
1	Core	R5A CDR7.8 x 5 (ST) B2.9 F2.5
2	Wire	Ø0.3mm x 1P 2UEWF 155°C
3	Solder (Lead Free)	Sn99.3% / Cu0.7%

Part Number Table

Description	Part Number
Description	Fait Number
Inductor, 56µH, 940mA, 10%	MCSD75-560KU

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		CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV
		Jagan	10/02/11	Δ	M10003031	SD75-560KU	Α
		APPROVED BY:	DATE:				
		Farnell	24/02/11	SCALE: NTS	U.O.M.: mm	SHEET: 3 O	0F 3