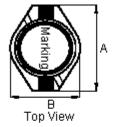
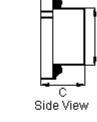
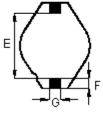
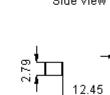
e muticomp	PART NO.			REVISIONS						
		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	MCBFS7330-101MU	-	А	RELEASED	Arun	19/2/11	Jagan	19/2/11	Farnell	7/3/11

## **Configurations and Dimensions**









Bottom View

Marking :

Dimensions : Millimetres YY : Year WW : Week

Suggest PCB Layout

#### **Electrical Characteristics**

101 YYWW

(at 25°C)

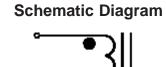
Test Condition		
100KHz 0.1V	L	100μH ±20%
at 25°C	DCR	207m $\Omega$ (Maximum)
100KHz 0.1V I <sub>rms</sub> = 1.7A	L at I <sub>rms</sub>	∆T40°C (Maximum)

Operating temperature: -55°C to +130°C

#### Note: $\mathbf{I}_{rms}$ Temperature Rise 40°C

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		CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV		
		Jagan	19/02/11		M10003466	BFS7330-101MU	Α		
		APPROVED BY:	DATE:				<u> </u>		
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А	18.54 mm	Maximum
В	15.24 mm	Maximum
С	7.62 mm	Maximum
D	12.7 ±0.3 mm	-
E	12.7 mm	Reference
F	2.54 mm	Reference
G	2.54 mm	Reference



(2) 38.5TS (Reference)

(1) Wire Ø0.37mm x 1P 2UEWF 155°C

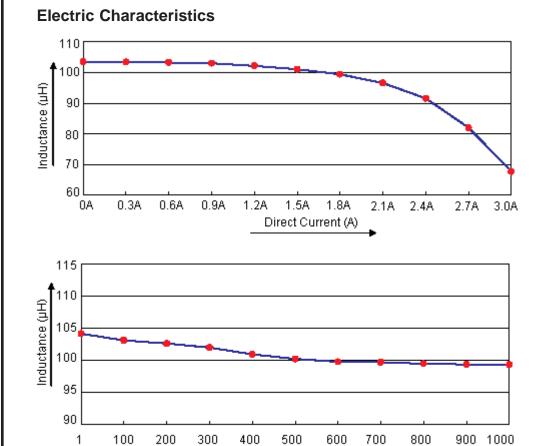
Note:



## **Test Data for Mechanical**

Test Item	A mm	B mm	C mm	D mm	E mm	F mm	G mm	
Specification	18.54 (Maximum)	15.24 (Maximum)	7.62 (Maximum)			2.54 (Reference)	2.54 (Reference)	
1	17.96	14.01	6.95	12.68	12.9	2.54	2.54	
2	17.95	14.03	6.96	12.65	12.91	2.51	2.55	
3	17.96	14.01	6.95	12.67	12.86	2.01	2.54	
4	17.93	14.05	6.98	12.64	12.89	2.56	2.51	
5	17.95	14.01	6.97	12.66	12.9	2.50	2.55	
Average	17.95	14.02	6.96	12.66	12.89	2.54	2.54	

💿 multicomp	PART NO.			REVISIONS							
		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE	
		MCBFS7330-101MU	-	А	RELEASED	Arun	19/2/11	Jagan	19/2/11	Farnell	7/3/11



Frequency (KHz)

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1

## **Test Data for Electrical**

Test Item	L µH	DCR mΩ	L at I <sub>rms</sub> μΗ
Condition	100KHz 0.1V	at 25°C	100KHz 0.1V I <sub>rms</sub> = 1.7A
Specification	100 ±20%	207 (Maximum)	∆T40°C (Maximum)
1	102.07	147.85	OK
2	100.87	150.05	OK
3	99.73	148	OK
4	100.81	147.17	OK
5	101.52	149.59	OK
Average	101	148.53	ОК

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		CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV		
		Jagan	19/02/11	Δ	M10003466	BFS7330-101MU	А		
	PURPOSES ONLY.	APPROVED BY:	DATE:		I		<u> </u>		
		Farnell	07/03/11	SCALE: NTS	U.O.M.: mm	SHEET: 2 OI	F 3		

muticomp	PART NO.			REVISIONS							
		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE	
	MCBFS7330-101MU	-	А	RELEASED	Arun	19/2/11	Jagan	19/2/11	Farnell	7/3/11	

# **Reliability Test**

Test Item	Specifications	Test Method and Remarks
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat.
Storage condition	Ambient temperature: 0°C to 40°CHumidity: Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.
Moisture sensitivity	Appearance : No abnormality No damage DCR change : Within ±20%	According to J-STD-020B level 3 Test condition : 60°C 60% RH Test duration : 40 hours Recovery : 1 to 2 hours of recovery under the standard
Solderability	Inductance change : Within ±20%   All termination shall exhibit a continuous solder coating free from defects for a minimum 90% of the surface area of any individual lead.	According to J-STD-002B   Steam aging category 97°C 98% RH   Steam aging duration : 8 hours   Solder : Lead-free solder   Solder temperature : 260 ±5°C   Dip time : 5 +0/-0.5 seconds.

# Material List

No.	ltem	Material Description
1	Core	N5D DR9.7 x 5.8 N5D RI12.7 x 5.7 x 10.8
2	Wire	Ø0.37mm x 1P 2UEWF 155°C
3	Solder (Lead Free)	99.3%Sn / 0.7%Cu
4	Glue	TH320D / TH320-3
3	Base	DR13-RT-A DAP

# Part Number Table

							•			
	Description	Part Number					http://v	www.newark.com		
	Inductor, 100µ H, 20%, 2A	MCBFS7330-101MU					http://v	www.cpc.co.uk		
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data shee ucts for th	t should check for themselves the Information and the suitability of the pro- eir purpose and not make any assumptions based on information included		Jagan	19/02/11		M10003466		S7330-101MU	A	
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