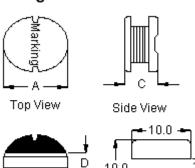


PART NO.

MCSDC1006-471KU

REVISIONS								
ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
-	Α	RELEASED	Arun	10/2/11	Jagan	10/2/11	Farnell	24/2/11

Configurations and Dimensions



Α	9.8 mm	(Maximum)
С	5.8 mm	(Maximum)
D	2.9 mm	(Reference)

Schematic Diagram



RoHS

Note:

- 1. Wire Ø0.2mm x 1P 2UEF1/U 155°C
- 2. 101.5TS (Reference)

Test Data for Mechanical

Test Item	A mm	C mm	D mm	
Specification	9.8 (Maximum)	5.8 (Maximum)	2.9 (Reference)	
1	9.56	5.54	2.81	
2	9.54	5.61	2.83	
3	9.52	5.57	2.79	
4	9.49	5.53	2.76	
5	9.51	5.58	2.84	
Average	9.52	5.57	2.81	

Marking: 471

Bottom View

Electrical Characteristics

(at 25°C)

Test Condition		
1KHz 1V L		470μH ±10%
at 25°C	DCR	1.48Ω (Maximum)
1KHz 1V Irms = 0.42A	ΔΤ	Temperature rise 40°C (Maximum)

Suggest PCB Layout Dimensions : Millimetres

Operating temperature: -55°C to +130°C

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URPOSES ONLY.	APPROVED BY:	DATE:
	Farnell	24/02/11

	DRAWING TITLE:							
	Inductor							
:	SIZE	DWG NO.	N4100000E0	ELEC	REV			
	Α		M10003053	SDO	C1006-471KU	Α		
_	SCALE: NTS		U.O.M.: mm		SHEET: 1 O	F 3		



PART NO.

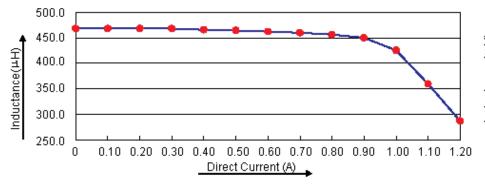
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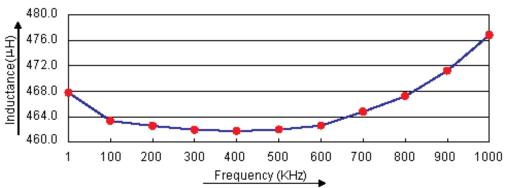
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Test Data for Electrical

Test Item	L μH	DCR Ω	ΔΤ		
Condition	1KHz 1V	at 25°C	1KHz 1V I _{rms} = 0.42A		
Specification	470 ±10%	1.48 (Maximum)	Temperature rise 40° (Maximum)		
1	468.35	1.19	OK		
2	467.9	1.2	OK		
3	468.1	1.21	OK		
4	468.3	1.19	OK		
5	467.75	1.2	OK		
Average	468.08	1.2	ОК		

Electric Characteristics





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Farnell	24/02/11

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Reliability Test

Test Item	Specifications	Test Method and Remarks		
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat.		
Storage Condition	Ambient temperature : 0°C to 40°C Humidity : Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.		
Moisture sensitivity	Appearance : No abnormality No damage DCR change : Within ±20% Inductance change : Within ±20%	According to J-STD-020B level 3 Test condition :60°C 60% RH Test duration :40 hours Recovery :1 to 2 hours of recovery under the standard condition after the removal from the test chamber.		
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B Steam aging category : 97°C 98% RH Steam aging duration : 8 hours		

Material List

No.	Item	Material Description			
1	Core	K22 DRM 9.5 x 5.5 RB-R B = 4.5 F = 3			
2	Wire	Ø0.2mm x 1P 2UEF1/U 155°C			
3	Solder (Lead Free)	Sn99.3%/Cu0.7%			

Part Number Table

Description	Part Number		
Inductors, 470μH, 10%, SMD	MCSDC1006-471KU		

http://www.farnell.com

http://www.newark.com

http://www.cpc.co.uk

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	Jagan	10/02/11
	APPROVED BY:	DATE:
	Farnell	24/02/11

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Inductor					
SIZE A	DWG NO.	M10003053	I -	TRONIC FILE C1006-471KU	REV A
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