	PABT NO.			REVISIONS						
🐼 multicomp		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	MCSD105-820KU	-	А	RELEASED	Sidhu	09/2/11	Jagan	09/2/11	Farnell	23/2/11

-

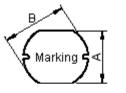
-

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(Reference)

-

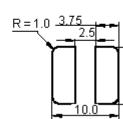
Configurations and Dimensions





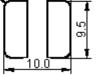
Top View





YΥ

WW



Suggest PCB Layout

Dimensions : Millimetres

Marking: 820 YYWW

Bottom View

: Year : Week

Electrical Characteristics

(at 25°C)

9 ±0.4 mm

10 ±0.4 mm

5.4 ±0.5 mm

3.5 mm

10.2 ±0.5 mm

А

В

С

D

Е

Test Condition		
100KHz 0.25V	L	82μH ±10%
at 25°C	DCR	$0.25 m\Omega$ (Maximum)
100KHz 0.25V I _{rms} = 1A	ΔT	Temperature rise 40°C (Maximum)

Operating temperature: -55°C to +130°C







Note:

1. Wire Ø0.35mm x 1P 2UEWF 155°C

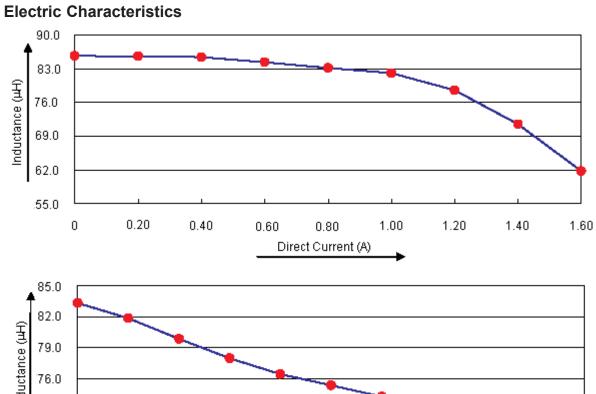
2. 39.5TS (Reference)

Test Data for Mechanical

Test Item	A mm	B mm	C mm	D mm	E mm
Specification	9 ±0.4	10 ±0.4	5.4 ±0.5	3.5 (Reference)	10.2 ±0.5
1	9.01	10.1	5.63	3.42	9.8
2	9.02	10.07	5.56	3.9	9.81
3	9.05	10.21	5.58	3.5	9.85
4	9.08	10.23	5.56	3.26	9.83
5	9.18	10.11	5.6	3.89	9.82
Average	9.07	10.14	5.59	3.59	9.82

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of a	mation purposes in connection with the products to which it relates. No licence ny intellectual property rights is granted. The Information is subject to change with-	UNLESS OTHERWISE	sidhu	09/02/11		Inducto	or	
belie	notice and replaces all data sheets previously supplied. The Information supplied is eved to be accurate but the Group assumes no responsibility for its accuracy or poleteness, any error in or omission from it or for any use made of it. Users of this	SPECIFIED,	CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV
data ucts	a sheet should check for themselves the Information and the suitability of the prod- tor their purpose and not make any assumptions based on information included or	DIMENSIONS ARE	Jagan	09/02/11	Δ	M10002622	SD105-820KU	A
use	addit Elability for loop of damage recalling north any foliation of the information of	PURPOSES ONLY.	APPROVED BY:	DATE:				
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	MCSD105-820KU	-	А	RELEASED	Sidhu	09/2/11	Jagan	09/2/11	Farnell	23/2/11



Test Data for Electrical

Test Item	L µH	DCR Ω	ΔΤ
Condition	100KHz 0.25V	at 25°C	100KHz 0.25V I _{rms} = 1A
Specification	82 ±10%	0.25 (Maximum)	temperature rise 40°C(Maximum)
1	85.48		ОК
2	85.86		ОК
3	85.8	0.15	ОК
4	84.62		ОК
5	85.08		ОК
Average	85.368	0.15	ОК

▲ 85.	οΓ										
82	٥Ē	_									
· 크 	• -			_							
(Hrt) 79. 76. 76.	0 -				_						
밑 73.	0 -										
70.	οL										
	1	100	200	300	400	500	600	700	800	900	1000
					Fred	quency (K	Hz)				

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	PART NO.			REVISIONS						
🐢 multicomp		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	MCSD105-820KU	-	А	RELEASED	Sidhu	09/2/11	Jagan	09/2/11	Farnell	23/2/11

Reliability Test

Test Item	Specifications	Test Method and Remarks
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat
Storage Condition	Ambient temperature: 0°C to 40°CHumidity: Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.
Moisture sensitivity	Appearance : No abnormality No damage DCR change : Within ±20%	According to J-STD-020B level 3 Test condition :60°C 60% RH Test duration :40 hours Recovery :1 to 2 hours of recovery under the standard
	Inductance change : Within ±20%	condition after the removal from the test chamber.
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B Steam aging category : 97°C 98% RH Steam aging duration : 8 hours Solder : Lead-free solder Solder temperature : 260 ±5°C
		Dip time : 5 +0/-0.5 seconds.

Material List

No.	ltem	Material Description
1	Core	R5A CDR10 x 5.4 (ST) B3.8 F2.6
2	Wire	Ø0.35mm x 1P 2UEWF 155°C
3	Solder (Lead Free)	99.3%Sn0.7%Cu

Part Number Table

Description	Part Number
Inductor, 82µH, 1A, 10%	MCSD105-820KU

http://www.farnell.com

http://www.newark.com

http://www.cpc.co.uk

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		CHECKED BY:	DATE:	SIZE DWG NO.	M10002622	ELECTRONIC FILE SD105-820KU	REV A
		Jagan	09/02/11				
		APPROVED BY:	DATE:		I		<u> </u>
		Farnell	23/02/11	SCALE: NTS U.O.M.: mm		SHEET: 3 OI	F 3