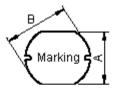
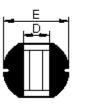
	PART NO.			REVISIONS						
		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	MCSD105-821KU	-	A	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

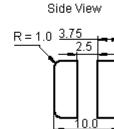
Configurations and Dimensions





Top View





Bottom View

Suggest PCB Layout Dimensions : Millimetres

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Marking: 821 YΥ YYWW ww

: Year : Week

Electrical Characteristics

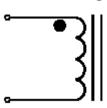
(at 25°C)

Test Condition		
100KHz 0.25V	L	820μH ±10%
at 25°C	DCR	2.55 Ω (Maximum)
100KHz 0.25V 1rms = 0.24A	ΔΤ	Temperature rise 40°C (Maximum)

Operating temperature: -55°C to +130°C

A	9 ±0.4 mm	-
В	10 ±0.4 mm	-
С	5.4 ±0.5 mm	-
D	3.5 mm	(Reference)
E	10.2 ±0.5 mm	-

Schematic Diagram





Note:

1. Wire Ø0.2mm x 1P 2UEWF 155°C

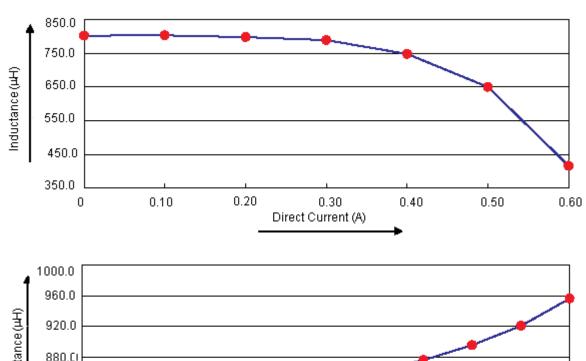
2. 125.5TS (Reference)

Test Data for Mechanical

Test Item	A mm	B mm	C mm	D mm	E mm
Specification	9 ±0.4	10 ±0.4	5.4 ±0.5	3.5 (Reference)	10.2 ±0.5
1	9	9.86	5.57	3.49	9.83
2	9.05	9.89	5.62	3.64	9.81
3	9.05	9.95	5.59	3.63	9.89
4	9.03	9.76	5.56	3.16	9.83
5	9.01	9.96	5.6	3.25	9.89
Average	9.03	9.88	5.59	3.43	9.85

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out notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this	SPECIFIED,	CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV
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	MCSD105-821KU	-	А	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11



Electric Characteristics

Test Data for Electrical

Test Item	L µH	DCR Ω	ΔΤ
Condition	100KHz 0.25V	at 25°C	100KHz 0.25V I _{rms} = 0.24A
Specification	820 ±10%	2.55 (Maximum)	Temperature rise 40°C (Maximum)
1	798	1.44	ОК
2	807.85	1.44	ОК
3	815		ОК
4	819.4	1.43	ОК
5	809.55		ОК
Average	809.96	1.43	ОК

	1000.0										
I	960.0										-
Inductance (µH)	920.0										4
tanc	880.01							_			
Induc	840.0										_
•	800.0				I					1	
	1	100	200	300	400	500	600	700	800	900	1000
					Freq	uency (Kł	Hz)				

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Reliability Test

Test Item	Specifications	Test Method and Remarks			
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat			
Storage Condition	Ambient temperature: 0°C to 40°CHumidity: Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.			
Appearance : No abnormality No damage	According to J-STD-020B level 3 Test condition :60°C 60% RH				
Moisture sensitivity	Moisture sensitivity DCR change : Within ±20% Inductance change : Within ±20%	Test duration :40 hours Recovery :1 to 2 hours of recovery under the standard condition after the removal from the test chamber.			
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B Steam aging category : 97°C 98% RH Steam aging duration : 8 hours Solder : Lead-free solder Solder temperature : 260 ±5°C Dip time : 5 +0/-0.5 seconds.			

Material List

No.	ltem	Material Description					
1	Core	R5A CDR10 x 5.4 (ST) B3.8 F2.6					
2	Wire	Ø0.2mm x 1P 2UEWF 155°C					
3	Solder (Lead Free)	99.3%Sn0.7%Cu					

Part Number Table

Description	Part Number		
Inductor, 820µH, 240mA, 10%	MCSD105-821KU		

http://www.farnell.com

http://www.newark.com

http://www.cpc.co.uk

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		Jagan	09/02/11				
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