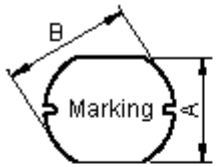
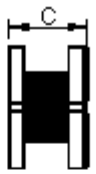
PART NO.	REVISIONS								
MCSD105-802KU	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	-	A	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

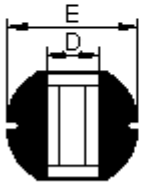
Configurations and Dimensions



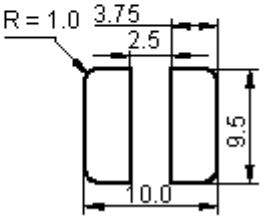
Top View



Side View



Bottom View

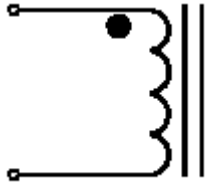


Suggest PCB Layout

Dimensions : Millimetres

A	9 ±0.4 mm	-
B	10 ±0.4 mm	-
C	5.4 ±0.5 mm	-
D	3.5 mm	(Reference)
E	10.2 ±0.5 mm	-

Schematic Diagram



RoHS Compliant

Note:

1. Wire Ø0.1mm x 1P 2UEWF 155°C
2. 380.5TS (Reference)

Marking : 802 **YY : Year**
YYWW **WW : Week**

Electrical Characteristics

(at 25°C)

Test Condition		
100KHz 0.25V	L	8mH ±10%
at 25°C	DCR	18Ω (Maximum)
100KHz 0.25V 1_{rms} = 0.18A	ΔT	Temperature rise 40°C (Maximum)

Operating temperature: -55°C to +130°C

Test Data for Mechanical

Test Item	A mm	B mm	C mm	D mm	E mm
Specification	**9 ±0.4**	**10 ±0.4**	**5.4 ±0.5**	**3.5 (Reference)**	**10.2 ±0.5**
1	9.06	10.04	5.52	3.27	9.99
2	9.08	9.98	5.48	3.41	9.89
3	9.04	10.1	5.47	3.49	9.99
4	9.08	9.96	5.48	3.47	10.01
5	9.05	9.93	5.46	3.33	10.02
Average	**9.06**	**10**	**5.48**	**3.39**	**9.98**

TOLERANCES:

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

DRAWN BY:	DATE:
Shashi	09/02/11
CHECKED BY:	DATE:
Jagan	09/02/11
APPROVED BY:	DATE:
Farnell	23/02/11

DRAWING TITLE: Inductor

SIZE	DWG NO.	ELECTRONIC FILE	REV
A	M10002784	SD105-802KU	A

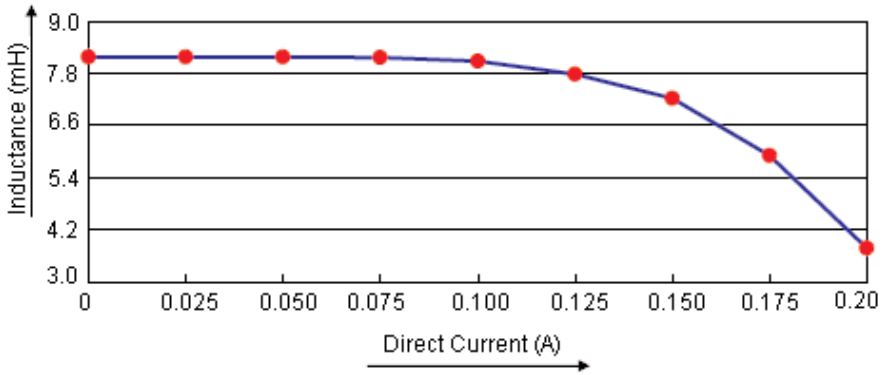
SCALE: NTS | U.O.M.: mm | SHEET: 1 OF 3

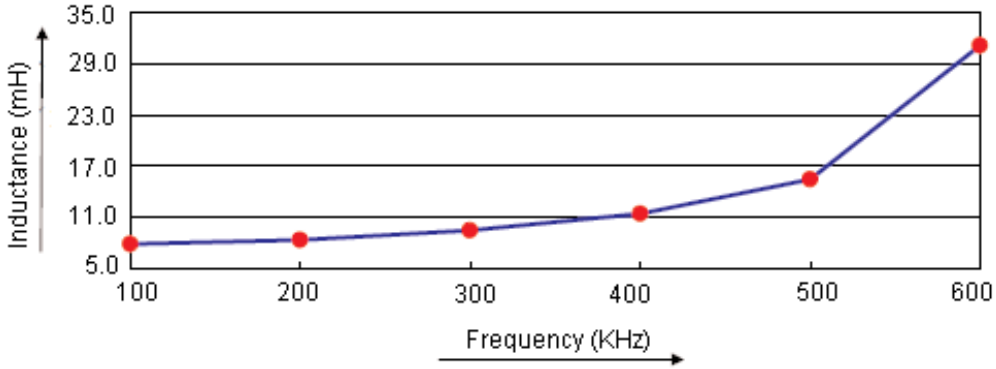
PART NO.	REVISIONS								
MCSD105-802KU	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	-	A	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

Test Data for Electrical

Test Item	L µH	DCR mΩ	ΔT
Condition	**100KHz 0.25V**	**at 25°C**	**100KHz 0.25V I_{rms} = 0.18A**
Specification	**8 ±10%**	**18 (Maximum)**	**Temperature rise 40°C (Maximum)**
1	8.11	14.91	OK
2	8.25	14.98	OK
3		15.02	OK
4	8.3	15.04	OK
5	8.27	14.98	OK
Average	**8.236**	**14.99**	**OK**

Electric Characteristics





TOLERANCES:

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DRAWN BY:	DATE:
Shashi	09/02/11
CHECKED BY:	DATE:
Jagan	09/02/11
APPROVED BY:	DATE:
Farnell	23/02/11

DRAWING TITLE: Inductor

SIZE	DWG NO.	ELECTRONIC FILE	REV
A	M10002784	SD105-802KU	A

SCALE: NTS | U.O.M.: mm | SHEET: 2 OF 3

multicomp	PART NO. MCSD105-802KU	REVISIONS									
		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
		-	A	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

Reliability Test

Test Item	Specifications	Test Method and Remarks
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat
Storage Condition	Ambient temperature : 0°C to 40°C Humidity : Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.
Moisture sensitivity	Appearance : No abnormality No damage DCR change : Within ±20% Inductance change : Within ±20%	According to J-STD-020B level 3 Test condition :60°C 60% RH Test duration :40 hours Recovery :1 to 2 hours of recovery under the standard condition after the removal from the test chamber.
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B Steam aging category : 97°C 98% RH Steam aging duration : 8 hours Solder : Lead-free solder Solder temperature : 260 ±5°C Dip time : 5 +0/-0.5 seconds.

Material List

No.	Item	Material Description
1	Core	R5A CDR10 x 5.4 (ST) B3.8 F2.6
2	Wire	Ø0.1mm x 1P 2UEWF 155°C
3	Solder (Lead Free)	99.3%Sn0.7%Cu

Part Number Table

Description	Part Number
Inductor, 8mH, 10%, 2pins	MCSD105-802KU

http://www.farnell.com

http://www.newark.com

http://www.cpc.co.uk

TOLERANCES:

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

DRAWN BY:	DATE:
Shashi	09/02/11
CHECKED BY:	DATE:
Jagan	09/02/11
APPROVED BY:	DATE:
Farnell	23/02/11

DRAWING TITLE: Inductor

SIZE	DWG NO.	ELECTRONIC FILE	REV
A	M10002784	SD105-802KU	A

SCALE: NTS | U.O.M.: mm | SHEET: 3 OF 3