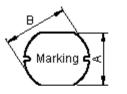
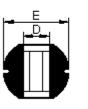
	PABT NO.			REVISIONS						
multicomp	-	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	MCSD106-402KU	-	А	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

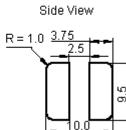
## **Configurations and Dimensions**





Top View





Bottom View

Suggest PCB Layout Dimensions : Millimetres

Marking : 402 YY : Year YYWW WW : Week

.....

## **Electrical Characteristics**

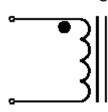
(at 25°C)

Test Condition		
100KHz 0.25V	L	4mH ±10%
at 25°C	DCR	8Ω (Maximum)
100KHz 0.25V 1rms = 0.25A	ΔΤ	Temperature rise 40°C (Maximum)

Operating temperature: -55°C to +130°C

А	9 ±0.4 mm	-
В	10 ±0.4 mm	-
С	6.5 ±0.4 mm	-
D	3.5 mm	Reference
E	10 ±0.5 mm	-

#### Schematic Diagram





Note:

1. Wire Ø0.14mm x 1P 2UEWF 155°C

2. 304.5TS (Reference)

#### **Test Data for Mechanical**

Test Item	A mm	B mm	C mm	D mm	E mm
Specification	9 ±0.4	10 ±0.4	6.5 ±0.4	3.5 (Reference)	10 ±0.5
1	9.16	9.85	6.55	2.62	9.92
2	9.14	9.91	6.58	2.70	9.82
3	9.09	9.81	6.59	2.64	9.87
4	9.1	9.01	6.52	2.68	9.07
5	9.08	9.82	6.54	2.89	9.82
Average	9.11	9.84	6.56	2.71	9.87

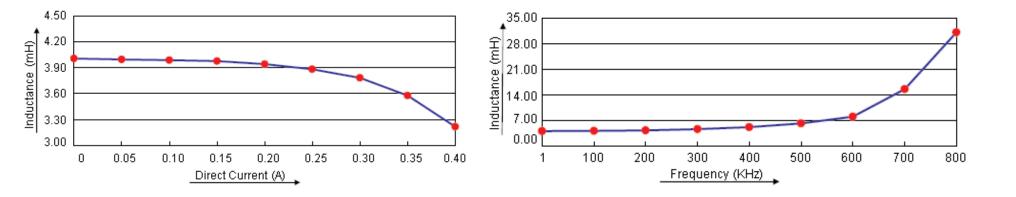
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believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this	SPECIFIED,	CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV
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<b>multicomp</b>	PART NO.			REVISIONS						
	- F	ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	MCSD106-402KU	-	А	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

### **Test Data for Electrical**

Test Item	L mH	DCR Ω	ΔΤ
Condition	100KHz 0.25V	at 25°C	100KHz 0.25V I <sub>rms</sub> = 0.25A
Specification	4 ±10%	8 (Maximum)	Temperature rise 40°C (Maximum)
1	3.97	7.69	ОК
2	4.06	7.67	ОК
3	3.99	7.58	ОК
4	4.03	7.34	OK
5	4.05	7.62	ОК
Average	4.02	7.58	ОК

#### **Electric Characteristics**



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out notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this	SPECIFIED,	CHECKED BY:	DATE:	SIZE DWG NO.		ELECTRONIC FILE	REV
data sheet should check for themselves the Information and the suitability of the prod- ucts for their purpose and not make any assumptions based on information included or	DIMENSIONS ARE FOR REFERENCE	Jagan	09/02/11	Δ	M10002787	SD106-402KU	A
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	PART NO.			REVISIONS						
		ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
		-	Α	RELEASED	Shashi	09/2/11	Jagan	09/2/11	Farnell	23/2/11

# **Reliability Test**

Test Item	Specifications	Test Method and Remarks
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat
Storage Condition	Ambient temperature : 0°C to 40°C Humidity : Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.
	Appearance : No abnormality No damage	According to J-STD-020B level 3 Test condition :60°C 60% RH
Moisture sensitivity	DCR change : Within ±20% Inductance change : Within ±20%	Test duration :40 hours Recovery :1 to 2 hours of recovery under the standard condition after the removal from the test chamber.
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B   Steam aging category : 97°C 98% RH   Steam aging duration : 8 hours   Solder : Lead-free solder   Solder temperature : 260 ±5°C   Dip time : 5 +0/-0.5 seconds.

# Material List

No.	ltem	Material Description
1	Core	R5A CDR10 x 6.5 (ST) B4.7 F4
2	Wire	Ø0.14mm x 1P 2UEWF 155°C
3	Solder (Lead Free)	99.3%Sn0.7%Cu

## Part Number Table

Description	Part Number
Inductor, 4mH, 10%, 2pins	MCSD106-402KU

http://www.farnell.com

http://www.newark.com

http://www.cpc.co.uk

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		Jagan	09/02/11				
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		Farnell	23/02/11	SCALE: NTS	U.O.M.: mm	SHEET: 3 OI	F 3