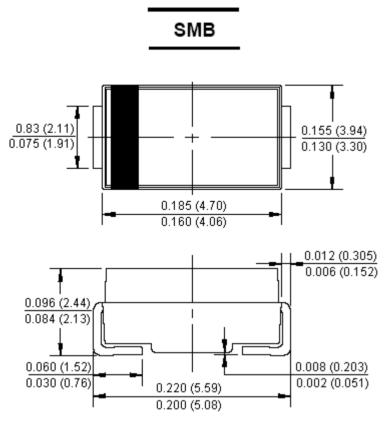
# Surface Mount Schottky Barrier Rectifiers



Reverse Voltage - 20 to 100 Volts Forward Current - 1.0 Amperes



Dimensions : Inches (Millimetres)

## **Mechanical Data:**

Case	: Moulded plastic
Polarity	: Colour band denotes cathode
Weight	: 0.003 ounces,0.093 grams
Mounting position	: Any

## **Maximum Ratings and Electrical Characteristics:**

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%

### Features:

- For surface mounted applications
- Metal-semiconductor junction with guarding
- Epitaxial construction
- Very low forward votage drop
- High current capability
- Plastic material has UL flammability classification 94V-0
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications.





# Surface Mount Schottky Barrier Rectifiers



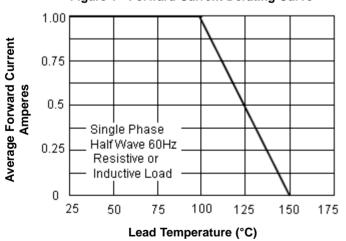
Characteristics	Symbol	SS12B	SS13B	SS110B	Unit
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	20	30	100	V
Maximum RMS Voltage	V <sub>RMS</sub>	14	14	70	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	20	30	100	V
Maximum Average Forward Rectified Current @T <sub>L</sub> =100°C	I <sub>(AV)</sub>		1.0		A
Peak Forward Surage Current 8.3ms Single Half Sine-Wave Super Imposed On Rated Load (JEDEC Method)	I <sub>FSM</sub>		40		A
Peak Forward Voltage at 1.0A DC	V <sub>F</sub>	0.45	0.55	0.85	V
Maximum DC Reverse Current@T_J=25°Cat Rated DC Bolcking Voltage@T_J=100°C	IR	1.0 10		mA	
Typical Junction Capacitance (Note1)	CJ	110		pF	
Typical Thermal Resistance (Note2)	Rejl	20		°C/W	
Operating Temperature Range	TJ	-55 to +150		°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150		0	°C

#### Notes:

1. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

2. Thermal resistance junction to lead.

#### **Rating and Characteristic Curves**



### Figure 1 - Forward Current Derating Curve

Figure 2 - Maximum Non-repetitive Surge Current 40 **Peak Forward Surge Current** 30 (Amperes) 20 Pulse Width 8.3mS 10 Single Half-sine-wave (JEDEC METHOD) 0 1 2 5 10 20 50 100 Number of Cycles at 60Hz

http://www.farnell.com http://www.newark.com http://www.cpc.co.uk

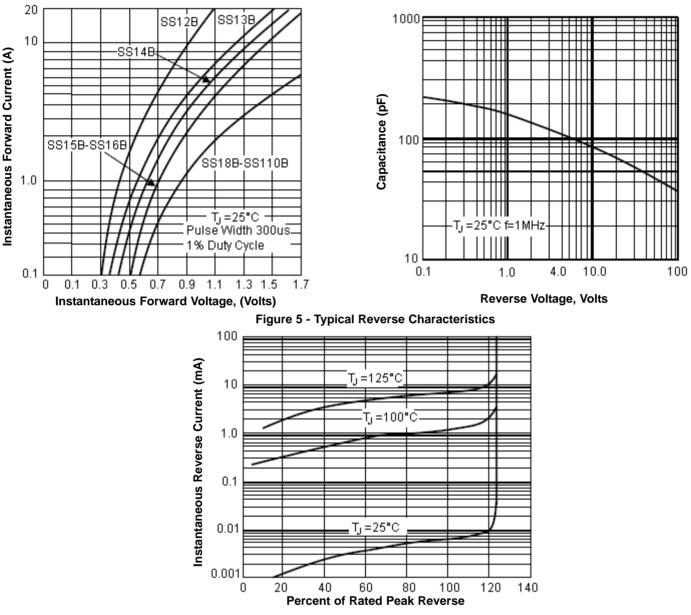


# Surface Mount Schottky Barrier Rectifiers





Figure 4 - Typical Junction Capacitance



### **Part Number Table**

Description	Part Number		
Surface Mount Schottky Barrier Rectifiers	SS110B		
Surface Mount Schottky Barrier Rectifiers	SS12B		
Surface Mount Schottky Barrier Rectifiers	SS13B		

Disclaimer This data sheet and its contents (the "Information") belong to the Premier Farnell Group (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheets previously from the purpose and not make any assumptions based on information included or omitted. Liability for loss or damage resulting from any reliance on the Information or use of it (including liability resulting from negligence or where the Group was aware of the possibility of such loss or damage arising) is excluded. This will not operate to limit or restrict the Group's liability for death or personal injury resulting from its negligence. SPC Multicomp is the registered trademark of the Group. © Premier Farnell plc 2010.

http://www.farnell.com http://www.newark.com http://www.cpc.co.uk

