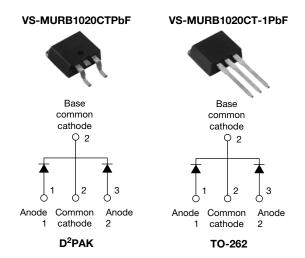


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HALOGEN

FREE

Ultrafast Rectifier, 2 x 5 A FRED Pt®

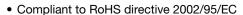


PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 5 A			
V_R	200 V			

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C





• AEC-Q101 qualified



MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		V_{RRM}		200	V
per leg				5	
Average rectified forward current	total device	I _{F(AV)}	Rated V _R , T _C = 149 °C	10	۸
Non-repetitive peak surge current per leg		I _{FSM}		50	А
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 149 °C	10	
Operating junction and storage tem	peratures	T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	200	-	-		
		I _F = 5 A, T _J = 125 °C	-	0.87	0.99	V	
Forward voltage	V _F	I _F = 10 A, T _J = 125 °C	-	1.02	1.20		
		I _F = 10 A, T _J = 25 °C	-	1.12	1.25		
Develope legicore eviment	I _B	$V_R = V_R$ rated	-	-	10		
Reverse leakage current I _F		T _J = 150 °C, V _R = V _R rated	-	-	250	μA	
Junction capacitance	C _T	V _R = 200 V	-	8	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time t _{rr}		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
		I _F = 0.5 A, I _R = 1.0 A, I _{REC} = 0.25 V		-	-	25	
	۲rr	T _J = 25 °C		-	24	-	ns
		T _J = 125 °C	I _F = 5 A dI _F /dt = 200 A/μs	-	35	-	
Peak recovery current I _{RRM}	I	T _J = 25 °C		-	3.3	-	Α
	T _J = 125 °C	V _R = 160 V	-	5.0	-		
Reverse recovery charge Q _{rr}	Q _{rr}	T _J = 25 °C		-	33	-	~ C
		T _J = 125 °C		-	76	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R_{thJC}		-	-	5	
Thermal resistance, junction to ambient per leg	R_{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Waight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking davisa		Case style D ² PAK	MURB1020CT			•
Marking device		Case style TO-262	MURB1020CT-1			

For technical questions, contact: $\underline{\text{diodestech@vishay.com}}$



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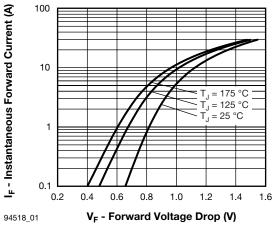


Fig. 1 - Typical Forward Voltage Drop Characteristics

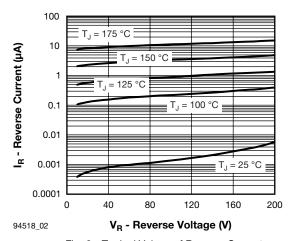


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

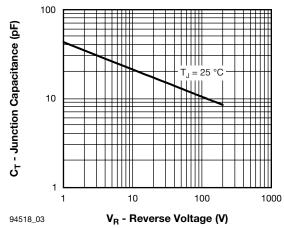


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

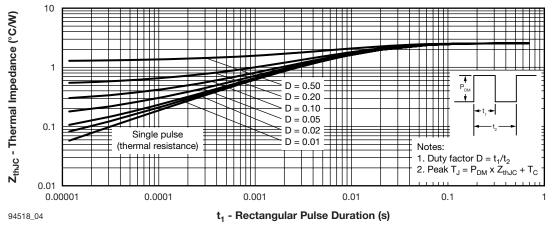


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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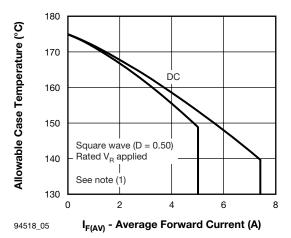


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

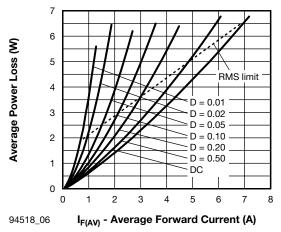


Fig. 6 - Forward Power Loss Characteristics

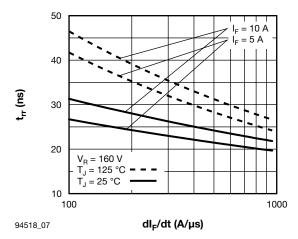


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

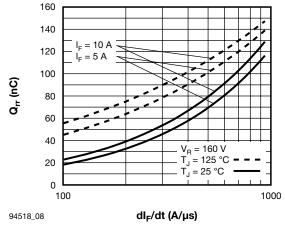


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at } V_{R1} = \text{Rated } V_R \\ \end{array}$

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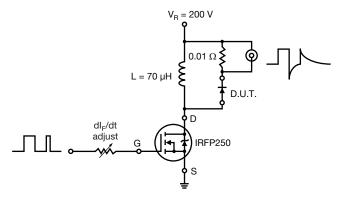
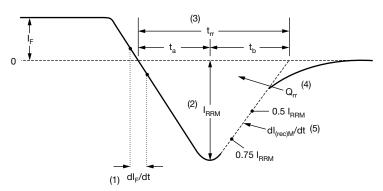


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

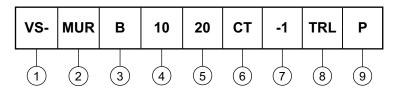
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ORDERING INFORMATION TABLE

Device code



- 1 HPP product suffix
- 2 Ultrafast MUR series
- 4 Current rating (10 = 10 A)
- 5 Voltage rating (20 = 200 V)
- 6 CT = Center tap (dual)
- 7 • None = D²PAK
 - -1 = TO-262
- None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented, for D²PAK package)
 - TRR = Tape and reel (right oriented, for D²PAK package)
- 9 • PbF = Lead (Pb)-free (for TO-262 and D²PAK tube)
 - P = Lead (Pb)-free (for D²PAK TRR and TRL)

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			

For technical questions, contact: diodestech@vishay.com

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