

INTEGRATED CIRCUITS – CMOS

(COMPLEMENTARY METAL OXIDE SILICON)

<p>NTE4066B 14-Lead DIP, See Diag. 247 Quad Bilateral Switch</p> <p>In/Out A 1 14 VDD Out/In A 2 13 Cont A Out/In B 3 12 Cont D In/Out B 4 11 In/Out D Cont B 5 10 Out/In D Cont C 6 9 Out/In C VSS 7 8 In/Out C</p>	<p>NTE4067B 24-Lead DIP, See Diag. 343 Analog, Single 16-Channel Multiplexer/Demultiplexer</p> <p>Common Out/In 1 24 VDD Ch 7 In/Out 2 23 Ch 8 In/Out Ch 6 In/Out 3 22 Ch 9 In/Out Ch 5 In/Out 4 21 Ch 10 In/Out Ch 4 In/Out 5 20 Ch 11 In/Out Ch 3 In/Out 6 19 Ch 12 In/Out Ch 2 In/Out 7 18 Ch 13 In/Out Ch 1 In/Out 8 17 Ch 14 In/Out Ch 0 In/Out 9 16 Ch 15 In/Out A 10 15 Inhibit B 11 14 C VSS 12 13 D</p>	<p>NTE4068B 14-Lead DIP, See Diag. 247 8-Input NAND/AND Gate (High Voltage Type)</p> <p>K (Note 1) 1 14 VDD A 2 13 J (Note 2) B 3 12 H C 4 11 G D 5 10 F N.C. 6 9 E VSS 7 8 N.C.</p> <p>Note 1. $K = A + B + C + D + E + F + G + H$ Note 2. $J = A + B + C + D + E + F + G + H$</p>
<p>NTE4069 14-Lead DIP, See Diag. 247 Hex Inverter (High Voltage Type)</p> <p>A 1 14 VDD G = \bar{A} 2 13 F B 3 12 L = F H = B 4 11 E C 5 10 K = E I = \bar{C} 6 9 D VSS 7 8 J = \bar{D}</p>	<p>NTE4070B 14-Lead DIP, See Diag. 247 Quad Exclusive OR Gate</p> <p>I₁ 1 14 VDD I₂ 2 13 I₈ O₁ 3 12 I₇ O₂ 4 11 O₄ I₃ 5 10 O₃ I₄ 6 9 I₆ VSS 7 8 I₅</p>	<p>NTE4071B 14-Lead DIP, See Diag. 247 Quad 2-Input OR Gate</p> <p>A 1 14 VDD B 2 13 H J = A + B 3 12 G K = C + D 4 11 M = G + H C 5 10 L = E + F D 6 9 F VSS 7 8 E</p>
<p>NTE4072B 14-Lead DIP, See Diag. 247 Dual 4-Input OR Gate</p> <p>J = A + B + C + D 1 14 VDD A 2 13 K = E + F + G + H B 3 12 H C 4 11 G D 5 10 F I.C. 6 9 E VSS 7 8 I.C.</p> <p>Note: I.C. = Internal Connection. DO NOT USE.</p>	<p>NTE4073B 14-Lead DIP, See Diag. 247 Triple 3-Input AND Gate</p> <p>A 1 14 VDD B 2 13 G D 3 12 H E 4 11 I F 5 10 L = G + H + I K = D + E + F 6 9 J = A + B + C VSS 7 8 C</p>	<p>NTE4075B 14-Lead DIP, See Diag. 247 Triple 3-Input OR Gate</p> <p>A 1 14 VDD B 2 13 G D 3 12 H E 4 11 I F 5 10 L = G + H + I K = D + E + F 6 9 J = A + B + C VSS 7 8 C</p>
<p>NTE4076B 16-Lead DIP, See Diag. 249 4-Bit D-Type Register (High Voltage Type)</p> <p>Disable M Output 1 16 VDD Disable N Output 2 15 Reset Q1 3 14 Data 1 Q2 4 13 Data 2 Q3 5 12 Data 3 Q4 6 11 Data 4 Clock 7 10 Disable G2 Input VSS 8 9 Disable G1 Input</p>	<p>NTE4077B 14-Lead DIP, See Diag. 247 Quad Exclusive NOR Gate</p> <p>A 1 14 VDD B 2 13 H J = $\bar{A} \oplus B$ 3 12 G K = $\bar{C} \oplus D$ 4 11 M = $\bar{G} \oplus H$ C 5 10 L = $\bar{E} \oplus F$ D 6 9 F VSS 7 8 E</p>	<p>NTE4078B 14-Lead DIP, See Diag. 247 8-Input NOR Gate</p> <p>K (Note 1) 1 14 VDD A 2 13 J (Note 2) B 3 12 H C 4 11 G D 5 10 F N.C. 6 9 E VSS 7 8 N.C.</p> <p>Note 1. $K = A + B + C + D + E + F + G + H$ Note 2. $J = A + B + C + D + E + F + G + H$</p>

See Diagrams, beginning on Page 1-293