T-1 (3mm) QUAD-LEVEL LED INDICATOR

Part Number: L-710A8SB/4ID

High Efficiency Red

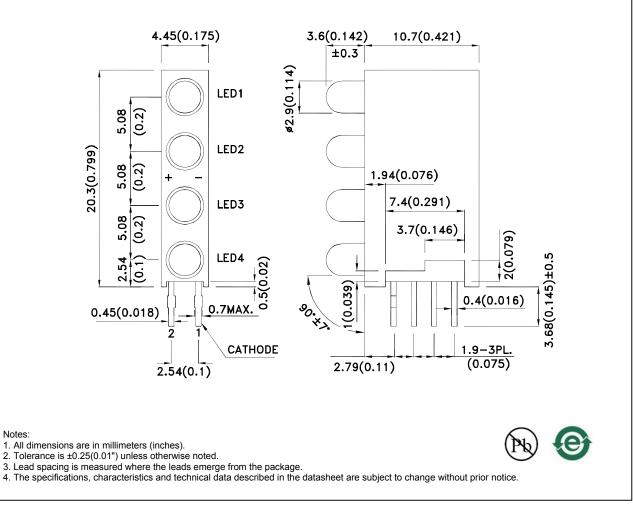
Features

- Quad-level design, save board space.
- Different color combination available.
- Black case enhances contrast ratio.
- Housing UL rating:94V-0.
- Housing material: type 66 nylon.
- RoHS compliant.

Description

The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.

Package Dimensions



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Part No.	Dice	Lens Type	lv (mcd) [2] @ 10mA		Viewing Angle [1]
			Min.	Тур.	201/2
	High Efficiency Red (GaAsP/GaP)	RED DIFFUSED	12	25	40°
L-710A8SB/4ID		KED DIFFUSED	*6	*12	

Notes:

01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
Luminous intensity/ luminous Flux: +/-15%.
*Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.		Max.	Units	Test Conditions
λpeak	Peak Wavelength	High Efficiency Red	627	*627		nm	I⊧=20mA
λD [1]	Dominant Wavelength	High Efficiency Red	625	*617		nm	I⊧=20mA
Δλ1/2	Spectral Line Half-width	High Efficiency Red	45			nm	I⊧=20mA
С	Capacitance	High Efficiency Red	15			pF	VF=0V;f=1MHz
Vf [2]	Forward Voltage	High Efficiency Red	2		2.5	V	IF=20mA
lR	Reverse Current	High Efficiency Red			10	uA	VR = 5V

Notes:

1.Wavelength: +/-1nm.

2. Forward Voltage: +/-0.1V. *Wavelength value is traceable to the CIE127-2007 compliant national standards.

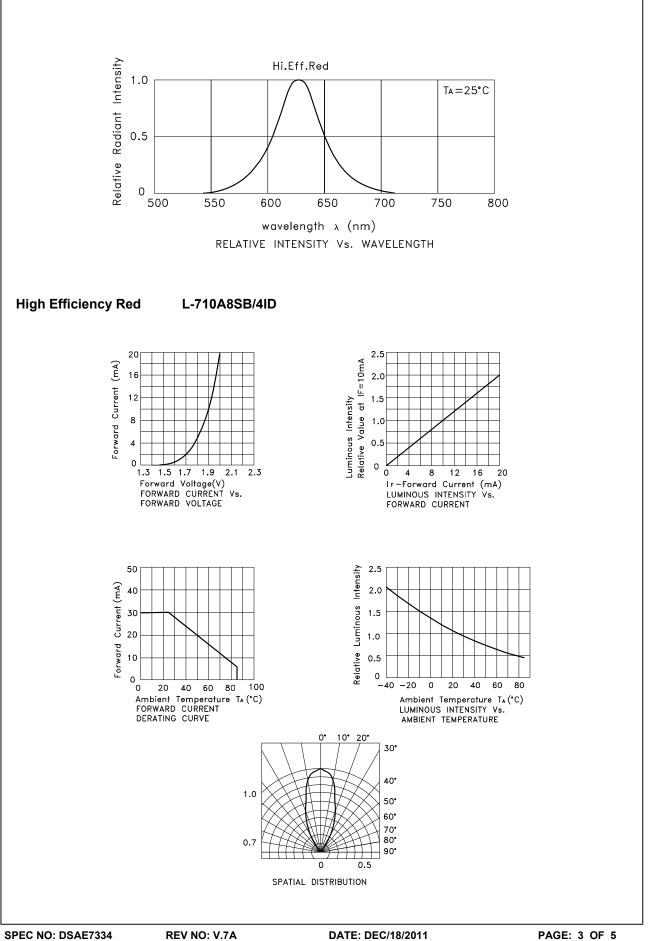
Absolute Maximum Ratings at TA=25°C

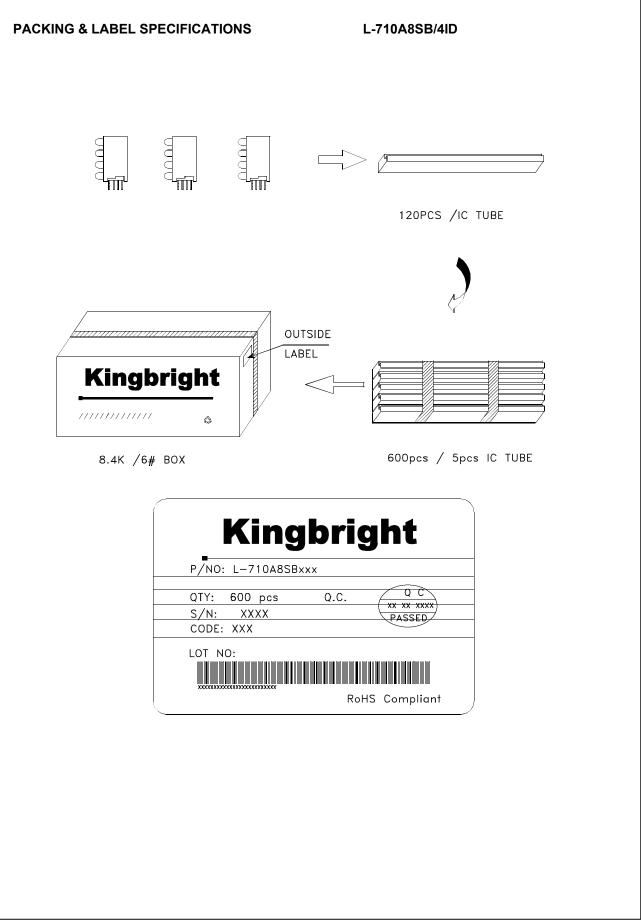
Parameter	High Efficiency Red	Units	
Power dissipation	75	mW	
DC Forward Current	30		
Peak Forward Current [1]	160	mA	
Reverse Voltage	5	V	
Operating/Storage Temperature	-40°C To +85°C	· · · ·	
Lead Solder Temperature [2]	260°C For 3 Seconds		
Lead Solder Temperature [3]	260°C For 5 Seconds		

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

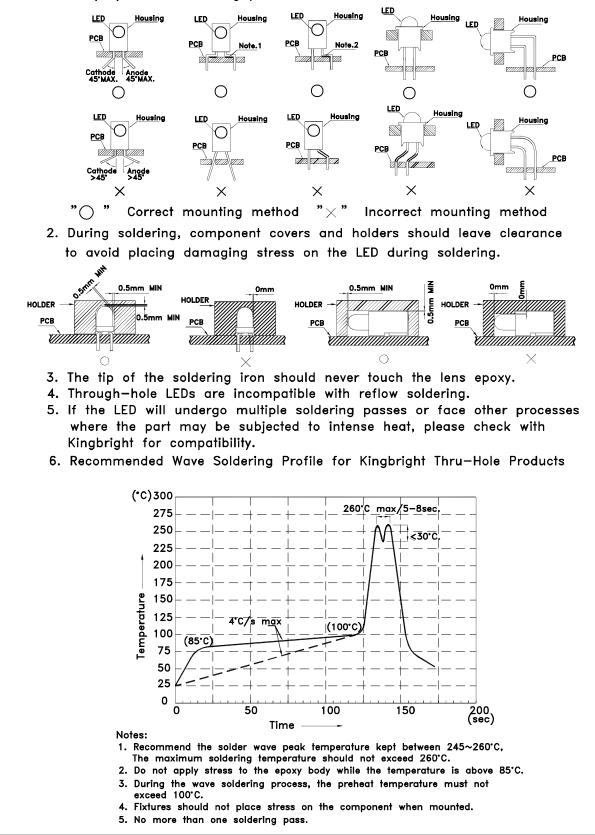
2. 2mm below package base.
3. 5mm below package base.





PRECAUTIONS

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead—forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.



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