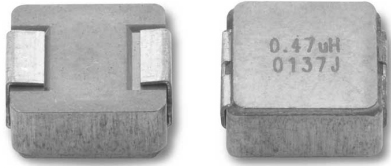


## Low Profile, High Current IHLP® Inductor



Manufactured under one or more of the following:  
**US Patents; 6,198,375/6,204,744/6,449,829/6,460,244.**  
 Several foreign patents, and other patents pending.

**FEATURES**

- Shielded construction
- Frequency range up to 1.0 MHz
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- 100 % lead (Pb)-free and RoHS compliant

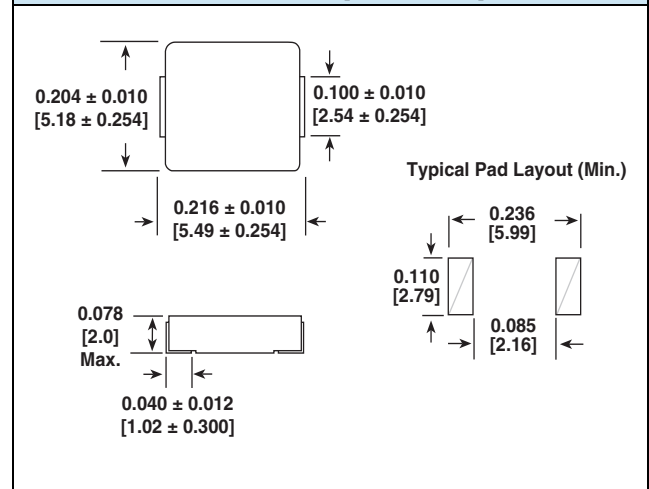

**RoHS  
COMPLIANT**
**APPLICATIONS**

- PDA/Notebook/Desktop/Server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS				
$L_0$ INDUCTANCE (µH) ± 20 % AT 100 kHz, 0.25 V, 0 A	DCR mΩ TYP. 25 °C	DCR mΩ MAX. 25 °C	HEAT RATING CURRENT DC AMPS <sup>3</sup> TYP.	SATURATION CURRENT DC AMPS <sup>4</sup> TYP.
0.10	2.7	2.9	21.0	25.0
0.22	4.1	4.5	17.0	13.0
0.33	5.5	5.9	13.0	7.5
0.47	7.1	7.7	12.5	8.0
1.0	16.8	18.1	7.5	7.0
2.2	34.9	37.7	5.0	5.5
3.3	53.5	57.8	4.1	4.7
4.7	75.3	81.3	3.2	3.0
5.6	85.2	92.0	3.0	2.2
10.0	169.3	182.8	2.2	2.0

**Notes**

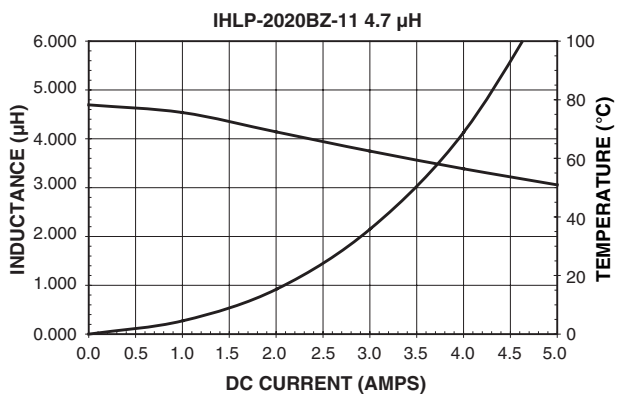
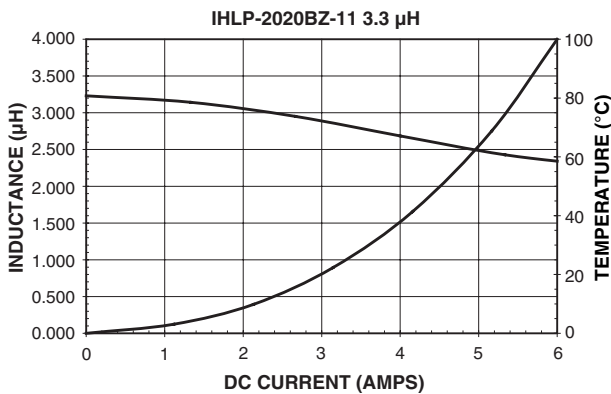
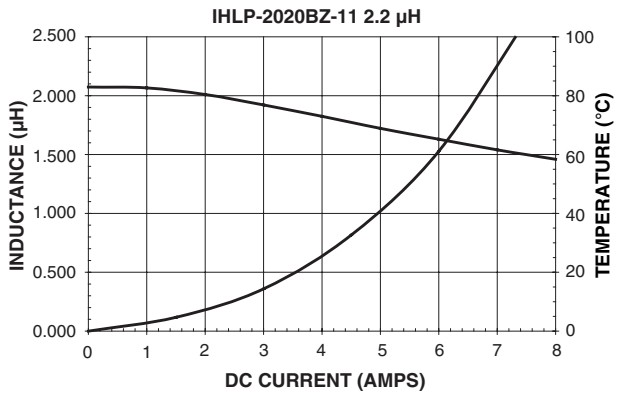
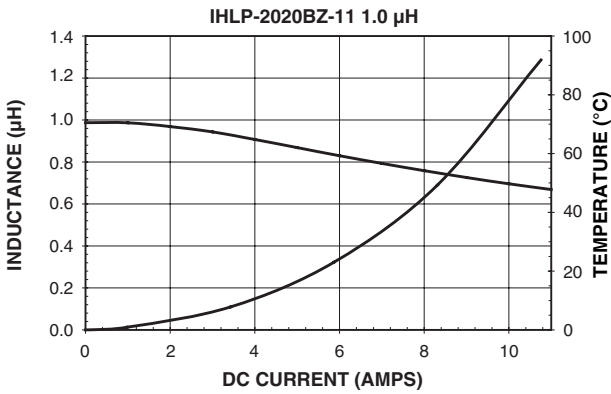
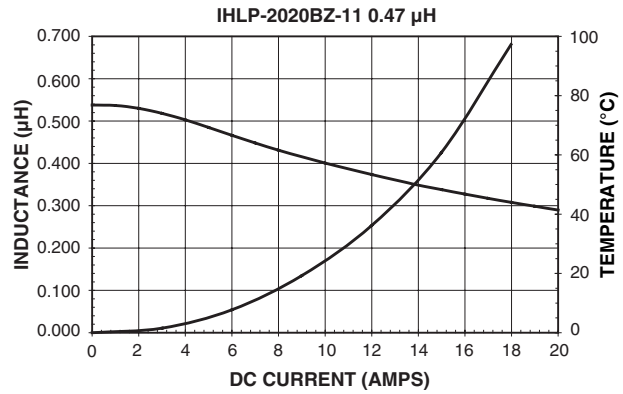
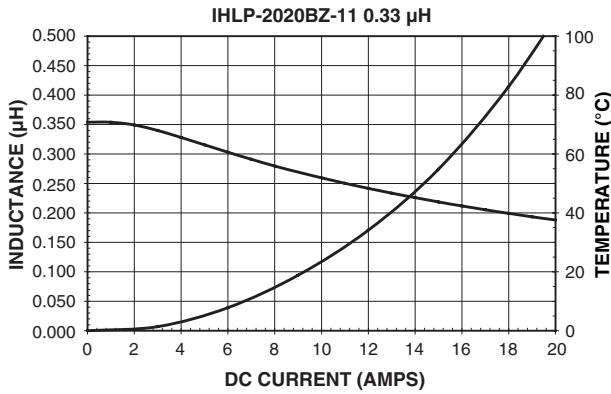
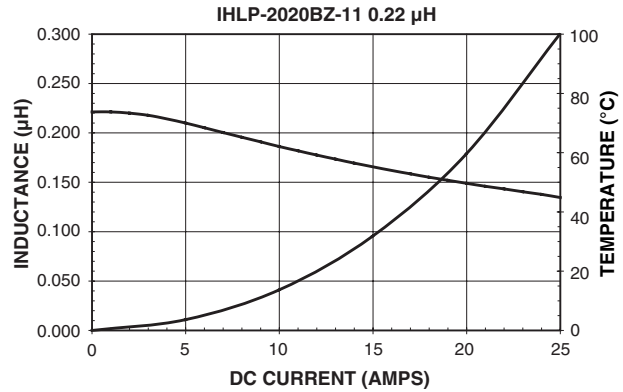
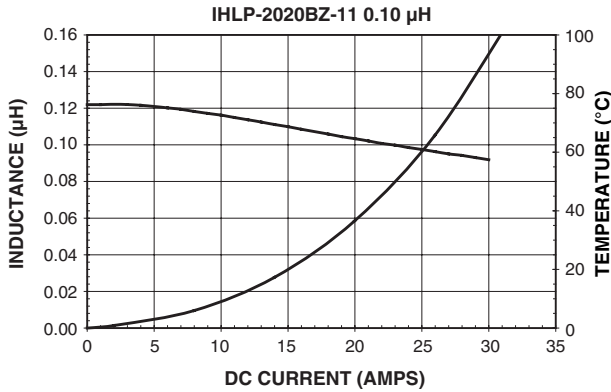
- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range - 55 °C to + 125 °C
- (3) DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- (4) DC current (A) that will cause  $L_0$  to drop approximately 20 %
- (5) The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

**DIMENSIONS** in inches [millimeters]


DESCRIPTION				
IHLP-2020BZ-11	4.7 µH	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC LEAD (Pb)-FREE STANDARD

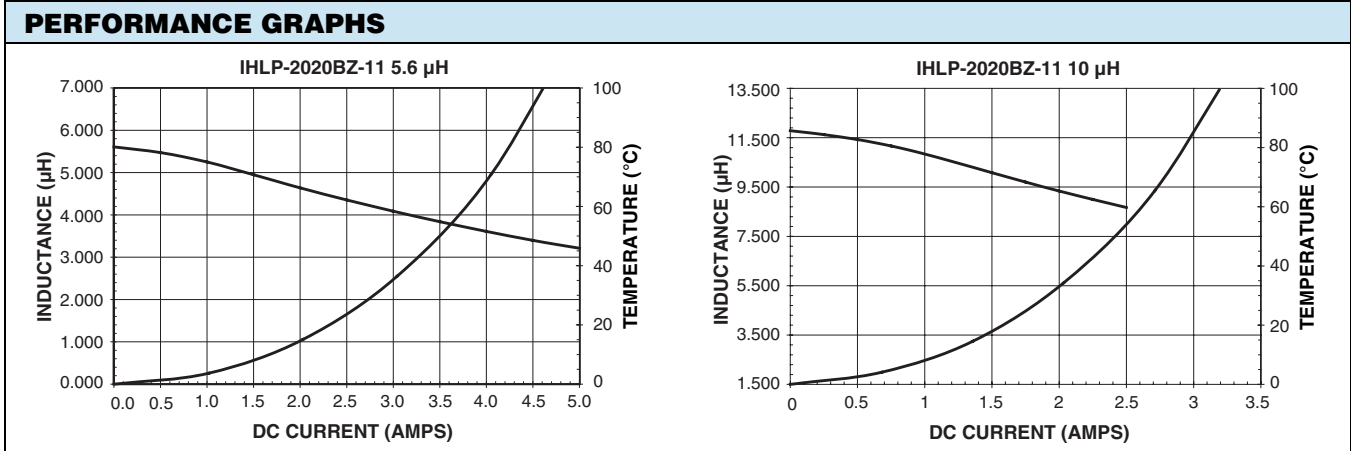
GLOBAL PART NUMBER																	
I	H	L	P	2	0	2	0	B	Z	E	R	4	R	7	M	1	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE			TOL	SERIES			

## PERFORMANCE GRAPHS





**PERFORMANCE GRAPHS**





## Disclaimer

All product specifications and data are subject to change without notice.

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