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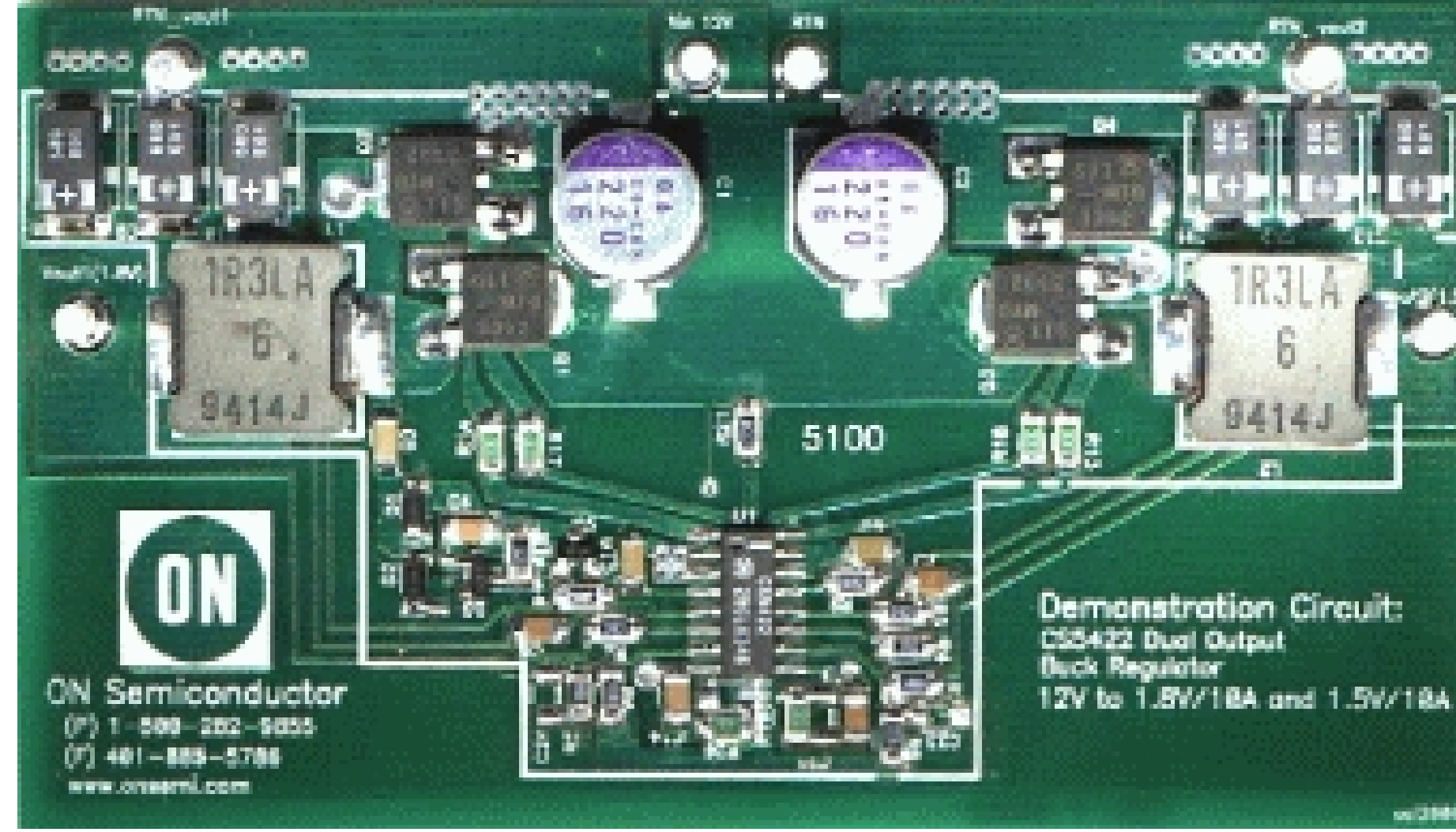
NCP5422EVB:NCP5422 Dual Out -of-Phase Synchronous Buck Controller Evaluation Board with Current Limit

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Evaluation Board Description

The NCP5422 evaluation board is a 4.0 in by 2.2 in two-layer printed circuit board. This demonstration circuit can be used to evaluate the performance and functionality of NCP5422, Dual Out



of-Phase Synchronous Buck Controller. With onw 12 V input, two independent synchronous Buck switching regulators provide 1.8 V/10 A and 1.5 V/10 A outputs. The NCP5422 controller contains all the circuitry required for teo independent Buck regulators and uses the V2™ control scheme to achieve the fastest possible transient response and best overall regulation, while using the least number of external components. Another feature of the NCP5422 is out of-phase synchronizarion between the channels, which rduces the input filter requirement. The NCP5422 also provides under -vo;tage lockout, soft start, built -in adaptive FET non -overlap, and hiccup -node overcurrent protection.

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Features and Applications

Features

- ? Dual Synchronous Buck Topology
- ? V2™ Control Method
- ? 150 ns Transient Response
- ? 0 or 100% Duty Cycle During Load Transient
- ? Programmable Soft Start
- ? Out -of-Phase Synchronization Between the Channels
- ? Hiccup Mode Overcurrent Protection
- ? Undervoltage Lockout
- ? Excellent Line and Load Regulation
- ? Low Output Voltage Ripple

Evaluation Board Information

Evaluation Board	Status	Pb-free	Short Description	Parts Used	Action
NCP5422EVB	Active		NCP5422 Dual Out -of-Phase Synchronous Buck Controller Evaluation Board with Current Limit	NCP5422ADR2 , NCP5422ADR2G	Contact Local Sales Office

Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCP5422EVB Bill of Materials	NCP5422EVB_BOM.PDF - 54.0 KB	0
Eval Board: Gerber	NCP5422EVB Gerber Layout Files (Zip Format)	NCP5422EVB_GERBER.ZIP - 47.0 KB	0
Eval Board: Schematic	NCP5422 Schematic	NCP5422EVB_SCHEMATIC.PDF - 39.0 KB	0
Eval Board: Test Procedure	NCP5422EVB Test Procedure	NCP5422EVB_TEST_PROCEDURE.PDF - 15.0 KB	0