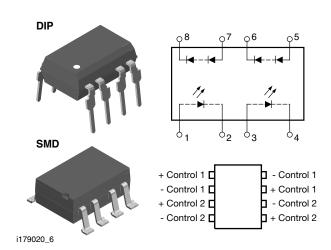
LH1262CAC, LH1262CACTR, LH1262CB

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Dual Photovoltaic MOSFET Driver Solid-State Relay



DESCRIPTION

The LH1262CB, LH1262CAC photovoltaic MOSFET driver consists of two LEDs optically coupled to two photodiode arrays. The photodiode array provides a floating source with adequate voltage and current to drive high-power MOSFET transistors. Optical coupling provides a high I/O isolation voltage. In order to turn the MOSFET off, an external resistance (gate-to-source) is required for gate discharge.

FEATURES

- High open circuit voltage
- · High short circuit current
- Isolation test voltage 5300 V_{RMS}
- · Logic compatible input
- · High reliability
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC





ROHS

APPLICATIONS

- · High-side driver
- · Solid state relays
- · Floating power supply
- Power control
- Data acquisition
- ATE
- · Isolated switching

Note

• See "Solid-State Relays" (application note 56)

AGENCY APPROVALS

UL1577: pending BSI/BABT: pending DIN EN: pending FIMKO: pending

ORDERING INFORMATION				
L H 1 2 6 2 # PART NUMBER ELECTOR VARIA	TR. PACKAGE	T R TAPE AND REEL	7.62 mm	SMD > 0.1 mm
PACKAGE		UL, BSI, VDE, FIMKO		
SMD-8		LH1262CAC		
SMD-8, tape and reel		LH1262CACTR		
DIP-8		LH1262CB		



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT			
SSR							
LED input ratings continuous forward current		I _F	50	mA			
LED input ratings reverse voltage	I _R ≤ 10 μA	V _R	5.0	V			
Photodiode array reverse voltage	I _R ≤ 2.0 μA	V _R	100	V			
Ambient operating temperature range		T _{amb}	- 40 to + 85	°C			
Storage temperature range		T _{stg}	- 40 to + 150	°C			
Pin soldering time ⁽¹⁾	t = 7.0 s max.	T _S	270	°C			
Input to output isolation voltage	t = 60 s min.	V _{ISO}	5300	V_{RMS}			

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V
Detector forward voltage	$I_F = 10 \mu A$	V _{F(PDA)}		14		V
Detector reverse voltage	I _R = 2.0 μA	V _{R(PDA)}		200		V
Open circuit voltage (pins 5, 6 or 7, 8)	$I_F = 5.0 \text{ mA}$	V _{OC}	10	12.95	15	V
	$I_F = 10 \text{ mA}$	V _{oc}		13.45		V
	I _F = 20 mA	V _{OC}		13.92		V
Short circuit current (pins 5, 6 or 7, 8)	I _F = 5.0 mA	I _{SC}	1.0	1.6	6.5	μΑ
	I _F = 10 mA	I _{SC}	2.6	3.4	14	μΑ
	I _F = 20 mA	I _{SC}		6.9		μΑ

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION SYMBOL MIN.		MIN.	TYP.	MAX.	UNIT
Turn-on time	I _F = 20 mA ⁽¹⁾	t _{on}		35		μs
Turn-off time	$I_F = 20 \text{ mA}^{(1)}$	t _{off}		90		μs

Note

FUNCTIONAL DESCRIPTION

Figure 1 outlines the IV characteristics of the illuminated photodiode array (PDA). For operation at voltages below V_{OC} , the PDA acts as a nearly constant current source. The actual region of operation depends upon the load.

The amount of current applied to the LED (pins 1 and 2 or 3 and 4) determines the amount of light produced for the PDA. For high temperature operation, more LED current may be required.

⁽¹⁾ f = 1.0 kHz, pulse width = 100 μ s, load (R_L) = 1.0 $M\Omega$, 15 pF; measured at 90 % rated voltage (t_{on}), 10 % rated voltage (t_{off}). Actuation speed depends upon the external t_{on} and t_{off} circuitry and the capacitance of the MOSFET.



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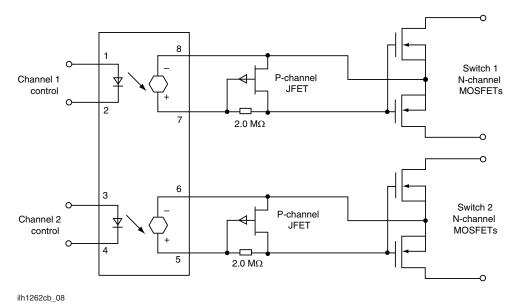
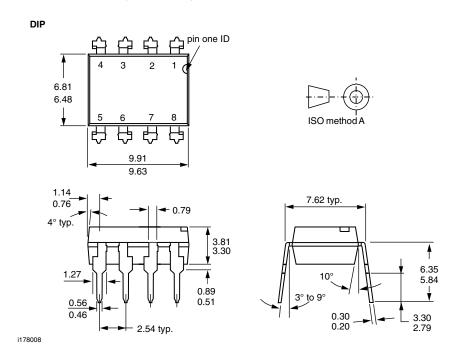


Fig. 1 - Typical Dual Form A Solid-State Relay Application

PACKAGE DIMENSIONS in inches (millimeters)

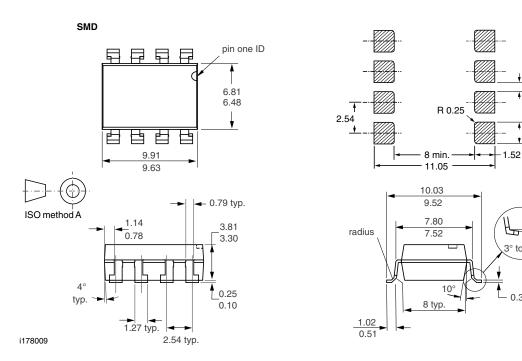




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