



PRODUCT BULLETIN
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ISSUE DATE: 23-May-2012
NOTIFICATION: 15200
TITLE: KINETIS 100Mhz Device Errata Update
EFFECTIVE DATE: 24-May-2012

DEVICE(S)

MPN
MK10DN512VLK10
MK10DN512VLL10
MK10DN512VLQ10
MK10DN512VMD10
MK10DN512ZVLK10
MK10DN512ZVLK10R
MK10DN512ZVLL10
MK10DN512ZVLL10R
MK10DN512ZVLQ10
MK10DN512ZVMB10
MK10DN512ZVMB10R
MK10DN512ZVMC10
MK10DN512ZVMC10R
MK10DN512ZVMD10
MK10DX128ZVMD10
MK10DX128ZVMD10R
MK10DX256VLQ10
MK10DX256VMD10
MK10DX256ZVLQ10
MK10DX256ZVLQ10R
MK10DX256ZVMD10
MK20DN512VLK10
MK20DN512ZAB10R
MK20DN512ZCAB10R
MK20DN512ZVLK10
MK20DN512ZVLL10
MK20DN512ZVLQ10
MK20DN512ZVLQ10R
MK20DN512ZVMB10
MK20DN512ZVMC10
MK20DN512ZVMC10R
MK20DN512ZVMD10
MK20DX128ZVLQ10

MK20DX256VLK10
MK20DX256ZVLK10
MK20DX256ZVLL10
MK20DX256ZVLQ10
MK20DX256ZVMC10
MK20DX256ZVMD10
MK30DN512ZVLK10
MK30DN512ZVLL10
MK30DN512ZVLQ10
MK30DN512ZVLQ10R
MK30DN512ZVMD10
MK30DX256ZVLQ10
MK30DX256ZVMD10
MK40DN512VLK10
MK40DN512VLL10
MK40DN512VLQ10
MK40DN512VMD10
MK40DN512ZVLL10
MK40DN512ZVLQ10
MK40DN512ZVMD10
MK40DX128ZVLQ10
MK40DX256VLQ10
MK40DX256VMD10
MK40DX256ZVLQ10
MK40DX256ZVMB10
MK40DX256ZVMD10
MK50DN512ZCLL10
MK50DN512ZCLQ10
MK50DX256ZCLL10
MK50DX256ZCLQ10
MK51DN256ZCMD10
MK51DN512ZCLL10
MK51DN512ZCLL10
MK51DN512ZCLQ10
MK51DN512ZCMC10
MK51DX256ZCLL10
MK51DX256ZCMB10
MK51DX256ZCMC10
MK52DN512ZCLQ10
MK52DN512ZCMD10
MK53DN512ZCMD10
MK53DN512ZCLQ10
MK53DN512ZCMD10
MK53DX256ZCLQ10

MK53DX256ZCMD10
MK60DN256ZVLL10
MK60DN256ZVLQ10
MK60DN512VLL10
MK60DN512VLQ10
MK60DN512VMD10
MK60DN512ZAB10R
MK60DN512ZCAB10R
MK60DN512ZVLL10
MK60DN512ZVLQ10
MK60DN512ZVLQ10R
MK60DN512ZVMC10
MK60DN512ZVMD10
MK60DX256VLL10
MK60DX256VLQ10
MK60DX256VMD10
MK60DX256ZVLL10
MK60DX256ZVLQ10
MK60DX256ZVMC10
MK60DX256ZVMD10

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The Kinetis 100 MHz devices (4N30D mask set) errata documentation has been updated to include additional errata that have been identified. The updated version has been added to the freescale.com website at: http://cache.freescale.com/files/microcontrollers/doc/errata/KINETIS_4N30D.pdf.

4588 DMAMUX: When using PIT with "always enabled" request, DMA request does not deassert correctly
 4949 Reset and Boot: Device may not exit the power on reset (POR) event correctly with fast ramp-up slew rates.

4945 UART: ISO-7816 T=1 mode receive data format with a single stop bit is not supported

REASON FOR CHANGE

The errata documentation for the Kinetis 100 MHz devices has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

There have been no changes made to the current production device. The errata describe existing conditions identified on current production devices. There are potential software and/or hardware implications to customers.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 09-May-2012

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15200_KINETIS_4N30D.pdf](#)
