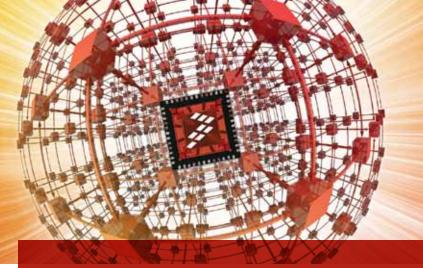




Target Applications

- Barcode scanners
- Portable media players
- Printers
- Programmable logic controllers



32-bit Microcontrollers

Kinetis K20 Family

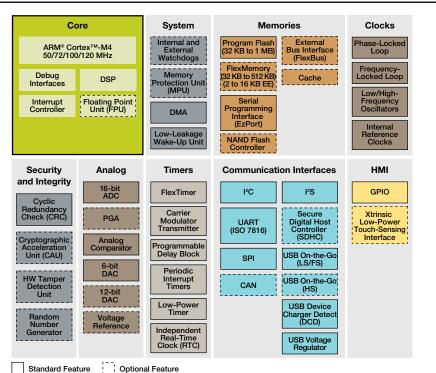
Low-power MCUs with USB On-The-Go

Overview

The Kinetis MCU portfolio consists of multiple pin-, peripheral- and software-compatible MCU families based on the ARM[®] Cortex[™]-M4 core. Families are built from innovative 90 nm thin-film storage (TFS) flash technology with unique FlexMemory (EEPROM) capability, and offer industry-leading low-power and mixed-signal analog integration.

The K20 MCU family is pin-, peripheral- and software-compatible with the K10 MCU family and adds Full- and High-speed USB 2.0 On-The-Go with device charger detect capability. Devices start from 32 KB of flash in 5x5 mm 32 QFN packages extending up to 1 MB in a 144 MAPBGA package with a rich suite of analog, communication, timing and control peripherals. High memory density K20 family devices include a single precision floating point unit and NAND flash controller.

Kinetis K20 Family





One-Stop Enablement Offering-MCU + IDE + RTOS

Features

Freescale Tower System hardware

development environment:

- Integrated development environments
 - Eclipse-based CodeWarrior V10.x IDE and Processor Expert
 - IAR Embedded Workbench
 - Keil MDK
 - CodeSourcery Sourcery G++ (GNU)
- Runtime software and RTOS
 - Math, DSP and encryption libraries
 - Motor control libraries
 - Complimentary bootloaders (USB, Ethernet, RF, serial)
 - Complimentary Freescale embedded GUI
 - Complimentary Freescale MQX™
 - Cost-effective Nano[™] SSL/Nano[™] SSH for Freescale MQX RTOS
 - Micrium uC/OS-III
 - Express Logic ThreadX
 - SEGGER embOS
 - freeRTOS
 - Mocana (security)
- Full ARM ecosystem

K20 Family Options

i outuroo	Bononto
 ARM[®] Cortex[™]-M4 core with DSP instruction support and optional single precision floating point unit Up to 32-channel DMA. Up to 16 KB of cache. Cross bar switch 	 Up to 120 MHz core supporting a broad range of processing bandwidth needs Peripheral and memory servicing with reduced CPU loading. Optimized bus bandwidth and flash execution performance. Concurrent multi-master bus accesses for increased bus bandwidth
USB On-The-Go (Full- and High- Speed) with device charger detect	 Optimized charging current/time for portable USB devices, enabling longer battery life. USB low-voltage regulator supplies up to 120 mA off chip at 3.3V to power external components from 5V input
Memory protection unit	 Provides memory protection for all cross bar switch masters, increasing software reliability
Hardware cyclic redundancy check engine	 Validates memory contents and communication data, increasing system reliability
 Independent-clocked COP. External watchdog monitor 	Prevents code runaway in fail-safe applications. Drives output pin to safe state external components if watchdog event occurs
Cryptographic acceleration unit (CAU)	Secure data transfer and storage. Faster than software implementations and with minimal CPU loading. Supports a wide waiter of alexistic and the approximate and the storage of the second storag
Hardware tamper detection UnitRandom number generator	 variety of algorithms: DES, 3DES, AES, MDS, SHA-1,SHA-256 Secure key storage with internal/external tamper detect for unsecured flash, temperature/clock/supply voltage variations and physical attack
Up to four FlexTimers with up to 20 channels	 General-purpose timers with hardware dead-time insertion and quadrature decoding for motor control
Carrier modulator transmitter	Infrared waveform generation for remote control applications
 4-channel, 32-bit periodic interrupt 	Time base generation for RTOS task scheduler or trigger source for ADC conversion and programmable delay block
	 Enables the connection of external memories and peripherals (e.g., graphics displays)
 FlexBus external bus interface Secure digital host controller NAND flash controller 	 Connection to SD, SDIO, MMC or CE-ATA cards for in-application software upgrades, file systems or adding Wi-Fi[®] or Bluetooth[®] support
	 Supports up to 32-bit ECC current and future NAND types with minimal software overhead
 32 KB–1 MB flash. Up to 128 KB of SBAM 	 High reliability, fast access program memory with 4-level security protection. Independent flash banks allow concurrent code execution and firmware updating
32 KB–512 KB FlexMemory	 FlexMemory provides 32 byte–16 KB of user-segmentable byte write/ erase EEPROM. In addition, FlexNVM from 32 KB–512 KB for extra program code, data or EEPROM backup

Benefits

	Memory						Feature Options														Pack	ages					
			â			Unit	stion		al er	_	s		olifier	Q		FM	FT	LF	MP	LH	LK	мв	LL	ML	мс	LQ	MD
Part Number	CPU (MHz)	Flash (KB)	Flex NVM (KB)	SRAM (KB)	Cache (KB)	Single Precision Floating Point Unit	Memory Protection Unit	CAN	Secure Digital Host Controller	NAND Flash	External Bus	12-bit DAC	Prog. Gain Amplifier	5V Tolerant I/O	Other	32QFN (5x5)	48QFN (7X7)	48LQFP (7X7)	64MAPBGA	64LQFP	80LQFP	81BGA (8X8)	100LQFP	104BGA	121BGA (8x8)	144LQFP	144BGA
MK20DN32Vyy5	50	32		8		ww									USB OTG (FS)	1	\checkmark	\checkmark	\checkmark	1							
MK20DN64Vyy5	50	64		16											USB OTG (FS)	1	\checkmark	\checkmark	1	1							
MK20DN128Vyy5	50	128		16											USB OTG (FS)	1	1	1	1	1							
MK20DN512Vyy10	100	512		128			1	\checkmark	1		1	1	1	1	USB OTG (FS)						1	1	1		\checkmark	1	1
MK20FN1M0Vyy12	120	1 MB		128	16	\checkmark	1	\checkmark	1	1	1	1	1	1	USB OTG (FS/HS)											\checkmark	√
MK20DX32Vyy5	50	32	32	8											USB OTG (FS)	\bigvee	\checkmark	\checkmark									
MK20DX64Vyy5	50	64	32	16											USB OTG (FS)	1	1	√	1	1							
MK20DX128Vyy5	50	128	32	16											USB OTG (FS)	1	1	1	1	1							
MK20DX64Vyy7	72	64	32	16				\checkmark			1	\checkmark	1	1	USB OTG (FS)					1	\checkmark	\checkmark					
MK20DX128Vyy7	72	128	32	32				\checkmark			1	1	1	1	USB OTG (FS)					1	\checkmark	1	1	1			
MK20DX256Vyy7	72	256	32	64				\checkmark			1	1	1	√	USB OTG (FS)					1	\checkmark	\checkmark	1	1			
MK20DX128Vyy10	100	128	128	32			1	\checkmark	\checkmark		\checkmark	\checkmark	\checkmark		USB OTG (FS)											\checkmark	\checkmark
MK20DX256Vyy10	100	256	256	64			1	\checkmark	\checkmark		1	\checkmark	\checkmark	1	USB OTG (FS)						\checkmark	\checkmark	\checkmark		\checkmark	\checkmark	√
MK20FX512Vyy12	120	512	512	128	16	\checkmark	1	\checkmark	\checkmark	1	1	\checkmark	1	1	USB OTG (FS/HS)											1	\checkmark
MK21DX128Vyy5(R)	50	128	64	32											HW Encryption and Tamper Detec						\checkmark	\checkmark					
MK21DX256Vyy5(R)	50	256	64	32											HW Encryption and Tamper Detec						\checkmark	V					
MK21DN512VLK5(R)	50	512		64											HW Encryption and Tamper Detec						\checkmark	V					
MK22DX128Vyy5(R)	50	128	64	32								1			USB OTG (FS)			1		1	1	1					
MK22DX256Vyy5(R)	50	256	64	32								J			USB OTG (FS)			J		1 J	V	V					
MK22DN512VLH5(R)	50	512		64								J			USB OTG (FS)					j	J	J					
MK22DN512VLH5(R)		512		64								\checkmark			USB OTG (FS)					1	\checkmark	\checkmark					Ĺ

yy = package designator



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