



Target Applications

- · GPS receivers
- · Blood glucose meters
- Bike computers
- · Currency counters

Kinetis K40 Family

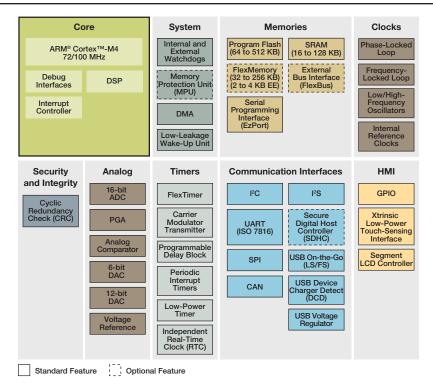
Low-power MCUs with USB and LCD

Overview

The Kinetis MCU portfolio consists of multiple pin-, peripheral- and software-compatible MCU families based on the ARM® CortexTM-M4 core. Families are built from innovative 90 nm thin-film storage (TFS) flash technology with unique FlexMemory (EEPROM) capability, and offer industry-leading low power and mixed signal analog integration.

The K40 MCU family adds full-speed USB 2.0 On-The-Go with device charger detect capability and a flexible, low-power segment LCD controller with support for up to 320 segments. Devices start from 64 KB of flash in 64 LQFN packages extending up to 512 KB in a 144 MAPBGA package with a rich suite of analog, communication, timing and control peripherals.

Kinetis K40 Family





One-Stop Enablement Offering—MCU + IDE + RTOS

Freescale Tower System hardware development environment:

- Integrated development environments
 - Eclipse-based CodeWarrior V10.x IDE and Processor Expert
 - IAR Embedded Workbench
 - Keil MDK
 - CodeSourcery Sourcery G++ (GNU)
- · Runtime software and RTOS
 - Math, DSP and encryption libraries
 - Motor control libraries
 - Complimentary bootloaders (USB, Ethernet, RF, serial)
 - Complimentary Freescale embedded GUI
 - Complimentary Freescale MQX™
 - Cost-effective Nano™ SSL/Nano™ SSH for Freescale MQX RTOS
 - Micrium uC/OS-III
 - Express Logic ThreadX
 - SEGGER embOS
 - o freeRTOS
 - Mocana (security)
- Full ARM ecosystem

Features Benefits

- ARM® CortexTM-M4 core with DSP instruction support
- Up to 16-channel DMA. Cross bar switch
- Up to 100 MHz core supporting a broad range of processing bandwidth needs
- Peripheral and memory servicing with reduced CPU loading. Concurrent multi-master bus accesses for increased bus bandwidth
- USB On-The-Go (full speed) with device charger detect
 Optimized charging current/time for portable USB devices, enabling longer battery life. USB low-voltage regulator supplies up to 120 mA off chip at 3.3V to power external components from 5V input
 - LCD blink mode enables low average power while remaining in low-power mode
 Segment fail detect guards against erroneous readouts and reduces
- Flexible, low-power LCD controller with support for up to 320 segments (40x8 or 44x4)
- LCD test costs

 Frontplane/backplane reassignment provides pin-out flexibility, easing PCB design and allows LCD configuration changes via firmware with no
- hardware re-work
 Supports multiple 3V and 5V LCD panel sizes with fewer segments (pins) than competitive controllers and no external components
- FlexBus external bus interface Secure digital host controller
- (e.g., graphics displays)
 Connection to SD, SDIO, MMC or CE-ATA cards for in-application software upgrades, file systems or adding Wi-Fi® or Bluetooth® support
- Up to three FlexTimers with up to 12 channels
- General-purpose timers with hardware dead-time insertion and quadrature decoding for motor control

Unused LCD pins can be configured as other GPIO functions

Enables the connection of external memories and peripherals

- Carrier modulator transmitter
- Infrared waveform generation for remote control applications
- 4-channel, 32-bit interrupt
- Time base generation for RTOS task scheduler or trigger source for ADC conversion and programmable delay block
- 64 KB–512 KB flash.
 Up to 128 KB of SRAM

• 32 KB-256 KB FlexMemory

- High reliability, fast access program memory with 4-level security protection. Independent flash banks allow concurrent code execution and firmware updating
- FlexMemory provides 32B–4 KB of user-segmentable byte write/erase EEPROM. In addition, Flex NVM 32–256 KB for extra program code, data or EEPROM backup

K40 Family Options

| | | Mer | nory | | Feature Options | | | | | | | | Packages | | | | | | | |
|----------------|-----------|------------|---------------|-----------|------------------------|-----|--------------------------------|------------------------|------------|----------------------|-----------------|---|------------------|------------------|---------------|-------------------|----------------|-----------------------|--------------------------|-------------------------|
| Part Number | CPU (MHz) | Flash (KB) | Flex NVM (KB) | SRAM (KB) | Memory Protection Unit | CAN | Secure Digital Host Controller | External Bus Interface | 12-bit DAC | Prog. Gain Amplifier | 5V Tolerant I/O | Other | 64LQFP (10X10) T | 80LQFP (12X12) T | 81BGA (8X8) W | 100LQFP (14X14) F | 104BGA (8X8) W | 121BGA (8x8) S | 144LQFP (20x20) D | 144BGA (13x13) M |
| MK40DN512Vyy10 | 100 | 512 | | 128 | 1 | 1 | 1 | * | 1 | 1 | 1 | USB OTG (FS), Segment LCD (up to 40x8/44x4) | | 1 | 1 | 1 | | 1 | √ | J |
| MK40DX64Vyy7 | 72 | 64 | 32 | 16 | | J | | | 1 | 1 | 1 | USB OTG (FS), Segment LCD (up to 24x8/28x4) | 1 | 1 | 1 | | | | | |
| MK40DX128Vyy7 | 72 | 128 | 32 | 32 | | J | | | J | J | J | USB OTG (FS), Segment LCD (up to 38x8/42x4) | 1 | 1 | 1 | 1 | V | | | |
| MK40DX256Vyy7 | 72 | 256 | 32 | 64 | | 1 | | | 1 | 1 | 1 | USB OTG (FS), Segment LCD (up to 38x8/42x4) | | 1 | 1 | 1 | 1 | | | |
| MK40DX128yy10 | 100 | 128 | 128 | 32 | 1 | J | 1 | 1 | J | J | J | USB OTG (FS), Segment LCD (up to 40x8/44x4) | | | | | | | ✓ | J |
| MK40DX256yy10 | 100 | 256 | 256 | 64 | 1 | J | 1 | 1 | 1 | 1 | 1 | USB OTG (FS), Segment LCD (up to 40x8/44x4) | | | | | | | J | J |

yy = Package designator *144pin only



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