



USB Power Architecture® BDM Multilink: Development Made Easy

Reliable & Cost-Effective

Overview

P&E's **USB Power Architecture® BDM Multilink** is an easy-to-use debug and programming interface for Freescale's **MPC5xx/8xx** line of Power Architecture microprocessors. The USB Power Architecture BDM Multilink is a member of P&E's family of USB Multilinks, which allow the PC to communicate with a target processor through the USB port of the PC. The Multilink controls the microprocessor by accessing the **Background Debug Mode (BDM)** of the target.

The Multilink's speed and reliability make it ideal for development, and it is supported by P&E software, Freescale's CodeWarrior, and other third-party software (available separately). P&E's **Power Architecture BDM In-Circuit Programmer** software can be used with the Multilink to program internal and external flash memory devices. The USB Power Architecture BDM Multilink also works with P&E's **Power Architecture BDM In-Circuit Debugger** software to control the target processor's execution, read/write registers and memory, and perform full C source-level debug.

More information on the USB Power Architecture BDM Multilink is available at www.pemicro.com.

Target Architectures

- Power Architecture® MPC5xx/8xx

Applications

- Development/Prototyping

Hardware Features

- Fast, hassle-free USB 2.0 communications interface
- Draws power directly from the USB port – no external power supply needed
- Multi-voltage support for targets ranging from 1.8 to 5.5 Volts
- Compact size

Software Compatibility

- P&E Power Architecture BDM In-Circuit Programmer
- P&E Power Architecture BDM In-Circuit Debugger
- P&E Power Architecture BDM Development Package
- Freescale CodeWarrior
- Third-party software