

# **RQJ0302NGDQA**

Silicon P Channel MOS FET Power Switching

R07DS0294EJ0500 (Previous: REJ03G1271-0400) Rev.5.00 Mar 28, 2011

#### **Features**

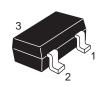
• Low on-resistance

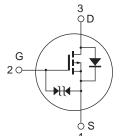
 $R_{\rm DS(on)}$  = 138 m $\Omega$  typ (V  $_{\rm GS}$  = –10 V,  $I_{\rm D}$  = –1.1 A)

- Low drive current
- High speed switching
- 4.5 V gate drive

#### **Outline**

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)





1. Source

GateDrain

) 3. Dr

Note: Marking is "NG".

# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

			(1u-25C)
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-30	V
Gate to source voltage	$V_{GSS}$	+10 / -20	V
Drain current	I <sub>D</sub>	-2.2	Α
Drain peak current	I <sub>D(Pulse)</sub> Note1	-5	Α
Body - drain diode reverse drain current	I <sub>DR</sub>	-2.2	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. When using the glass epoxy board (FR-4:  $40 \times 40 \times 1$  mm)

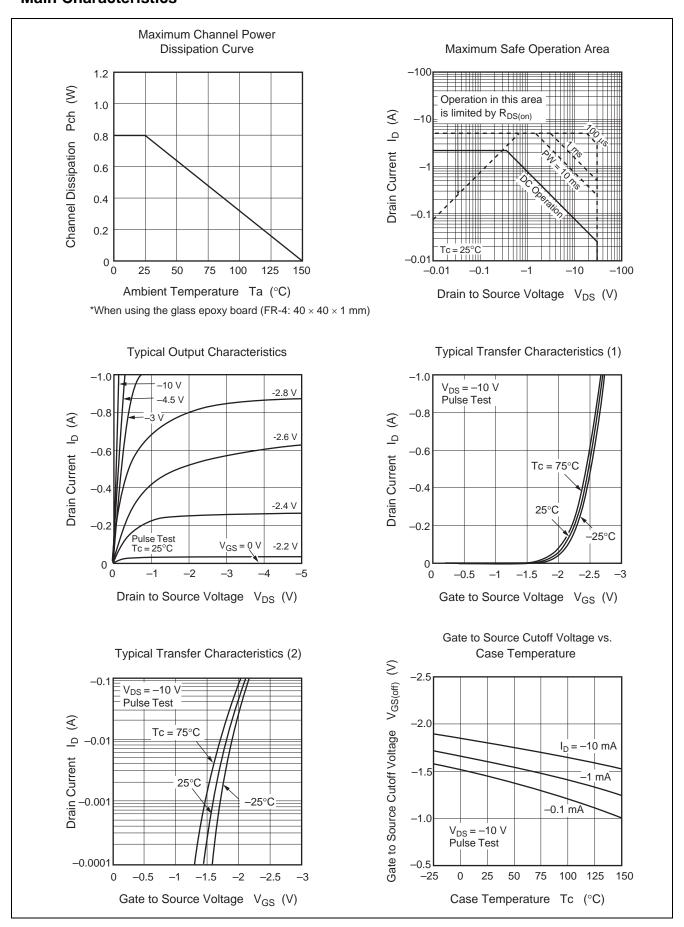
# **Electrical Characteristics**

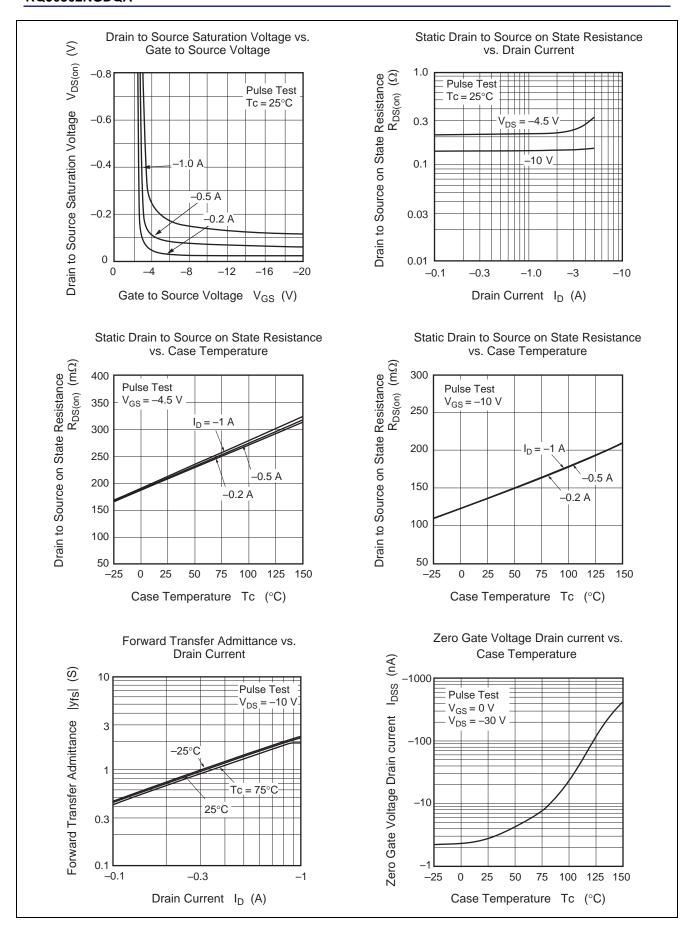
 $(Ta = 25^{\circ}C)$ 

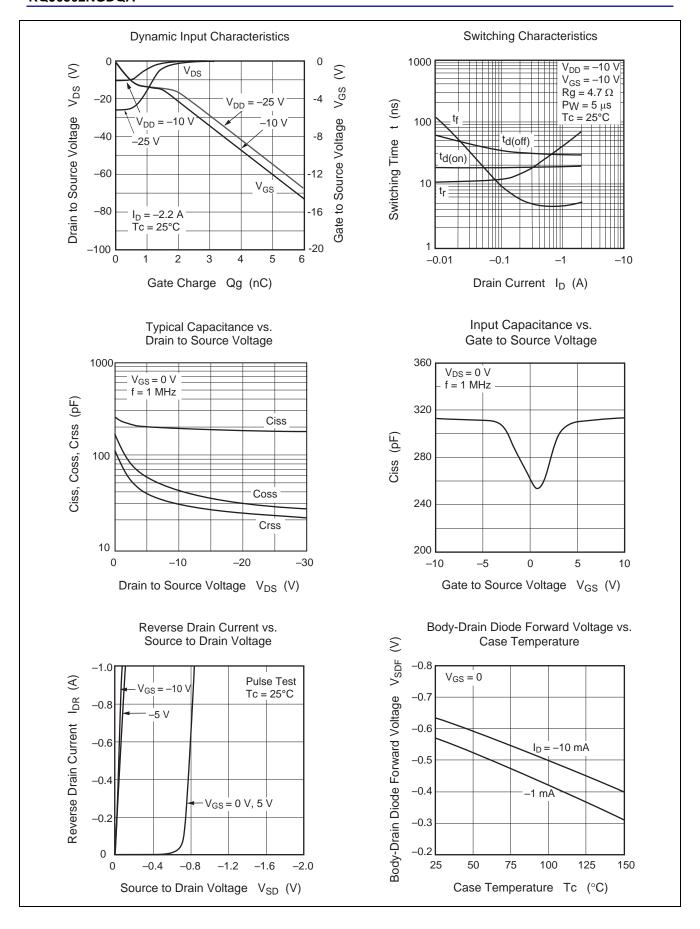
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+10	_	_	V	$I_G = +100 \mu A, V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	-20	_	_	V	$I_G = -100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	+10	μΑ	$V_{GS} = +8 \text{ V}, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	-10	μΑ	$V_{GS} = -16 \text{ V}, V_{DS} = 0$
Drain to source leak current	I <sub>DSS</sub>		_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Drain to source on state resistance	R <sub>DS(on)</sub>	_	138	173	mΩ	$I_D = -1.1 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
	R <sub>DS(on)</sub>	_	216	303	mΩ	$I_D = -1.1 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y <sub>fs</sub>	1.2	2.1	_	S	$I_D = -1.1 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	195	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	42	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	29	_	pF	
Turn - on delay time	$t_{d(on)}$	_	19	_	ns	$I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V},$
Rise time	t <sub>r</sub>	_	25	_	ns	$R_L = 20 \Omega$ , $Rg = 4.7 \Omega$
Turn - off delay time	$t_{d(off)}$	_	30	_	ns	
Fall time	t <sub>f</sub>	_	4.6	_	ns	
Total gate charge	Qg		4.2	_	nC	$V_{DD} = -10 \text{ V}, V_{GS} = -10 \text{ V},$
Gate to source charge	Qgs		0.7	_	nC	$I_D = -2.2A$
Gate to drain charge	Qgd		1.0		nC	
Body - drain diode forward voltage	$V_{DF}$	_	-0.9	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

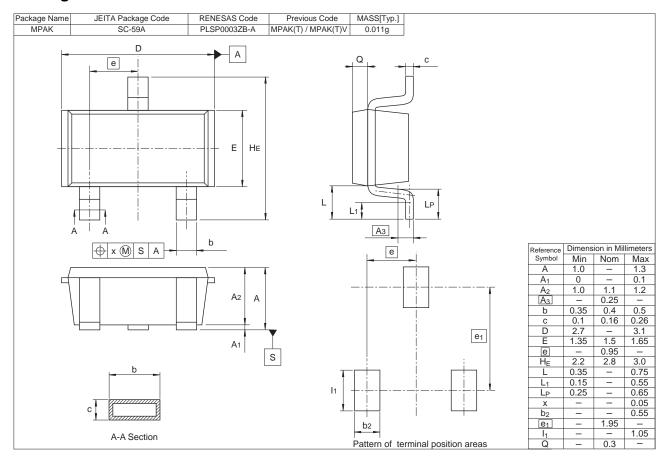
## **Main Characteristics**







## **Package Dimensions**



# **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RQJ0302NGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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