

# **FDC3612**

# 100V N-Channel PowerTrench® MOSFET

## **General Description**

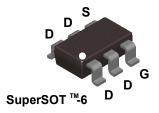
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low  $R_{\text{DS(ON)}}$  and fast switching speed.

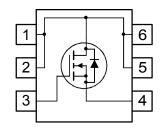
## **Applications**

DC/DC converter

#### **Features**

- 2.6 A, 100 V  $R_{DS(ON)} = 125 \text{ m}\Omega$  @  $V_{GS} = 10 \text{ V}$   $R_{DS(ON)} = 135 \text{ m}\Omega$  @  $V_{GS} = 6 \text{ V}$
- High performance trench technology for extremely low  $R_{\mbox{\scriptsize DS}(\mbox{\scriptsize ON})}$
- Low gate charge (14nC typ)
- High power and current handling capability
- · Fast switching speed





## Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
$V_{DSS}$	Drain-Source Voltage		100	V
V <sub>GSS</sub>	Gate-Source Voltage		± 20	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	2.6	А
	- Pulsed		20	
$P_D$	Maximum Power Dissipation	(Note 1a)	1.6	W
		(Note 1b)	0.8	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

## **Thermal Characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	78	°C/W
Reic	Thermal Resistance, Junction-to-Case	(Note 1)	30	°C/W

**Package Marking and Ordering Information** 

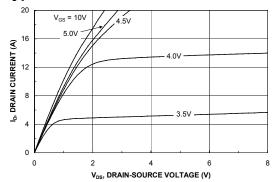
Device Marking		Device	Reel Size	Tape width	Quantity
	.362	FDC3612	7"	8mm	3000 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
				- 71		
W <sub>DSS</sub>	Durce Avalanche Ratings (Note Drain-Source Avalanche Energy	Single Pulse, $V_{DD} = 50 \text{ V}$ , $I_D = 2.6 \text{ A}$			90	mJ
I <sub>AR</sub>	Drain-Source Avalanche Current	Single i dise, V <sub>DD</sub> = 30 V, i <sub>D</sub> =2.0 A			2.6	A
					2.0	
	racteristics	Tv. 044 050 A	100		1	
BV <sub>DSS</sub> ΔBV <sub>DSS</sub>	Drain–Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	100			V
ΔBVDSS ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		99		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V			10	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	V <sub>GS</sub> = -20 V, V <sub>DS</sub> = 0 V			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	2	2.3	4	V
$\Delta V_{GS(th)}$	Gate Threshold Voltage	I <sub>D</sub> = 250 μA, Referenced to 25°C		- 6		mV/°C
$\Delta T_J$	Temperature Coefficient					IIIV/ C
$R_{\text{DS(on)}}$	Static Drain–Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 2.6 \text{ A}$ $V_{GS} = 6.0 \text{ V}, I_D = 2.5 \text{ A}$		86 91	125 135	$m\Omega$
		$V_{GS} = 10 \text{ V}, I_D = 2.6 \text{ A;} T_J = 125^{\circ}\text{C}$		157	240	
I <sub>D(on)</sub>	On–State Drain Current	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 5 V	10			Α
<b>g</b> <sub>FS</sub>	Forward Transconductance	$V_{DS} = 10 \text{ V},  I_{D} = 2.6 \text{ A}$		10		S
Dvnamio	Characteristics					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V,		660		pF
Coss	Output Capacitance	f = 1.0 MHz		55		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			40		pF
Switchin	ng Characteristics (Note 2)				I.	
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 50 \text{ V}, \qquad I_D = 1 \text{ A},$		6	11	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		3.5	7	ns
$t_{d(off)}$	Turn-Off Delay Time			23	37	ns
t <sub>f</sub>	Turn–Off Fall Time			3.7	7.4	ns
Q <sub>q</sub>	Total Gate Charge	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 2.6 A,		14	20	nC
Q <sub>gs</sub>	Gate–Source Charge	V <sub>GS</sub> = 10 V		2.3		nC
Q <sub>qd</sub>	Gate-Drain Charge			3.6		nC
	ource Diode Characteristics	and Maximum Ratings	•			
l <sub>s</sub>	Maximum Continuous Drain-Source	•			1.3	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = 1.3 \text{ A}  \text{(Note 2)}$		0.76	1.2	V
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>E</sub> = 2.6 A		31		nS
Q <sub>rr</sub>	Diode Reverse Recovery Charge	$d_{iF}/d_t = 100 \text{ A/µs}$ (Note 2)		56	1	nC

### Notes:

- 1.  $R_{\theta,JA}$  is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta,JC}$  is guaranteed by design while  $R_{\theta,CA}$  is determined by the user's board design.
  - a. 78°C/W when mounted on a 1in² pad of 2oz copper on FR-4 board.
  - b. 156°C/W when mounted on a minimum pad.
- 2. Pulse Test: Pulse Width  $\leq 300~\mu s,~Duty~Cycle \leq 2.0\%$

# **Typical Characteristics**



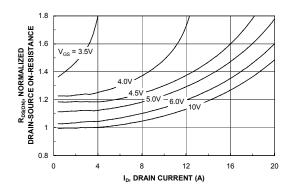
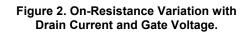
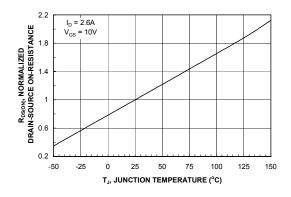


Figure 1. On-Region Characteristics.





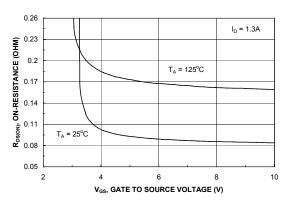
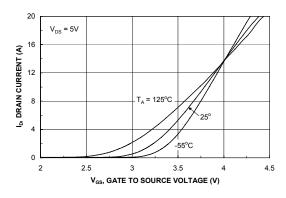


Figure 3. On-Resistance Variation with Temperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



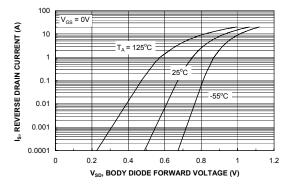
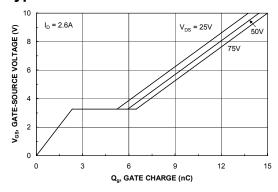


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

# **Typical Characteristics**



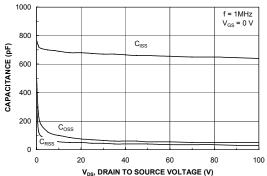
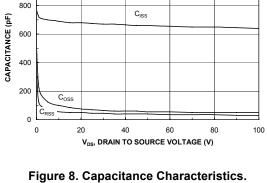
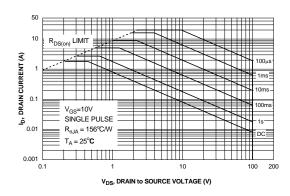


Figure 7. Gate Charge Characteristics.





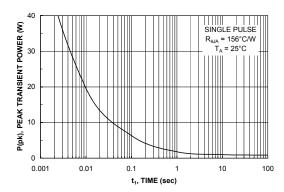


Figure 9. Maximum Safe Operating Area.



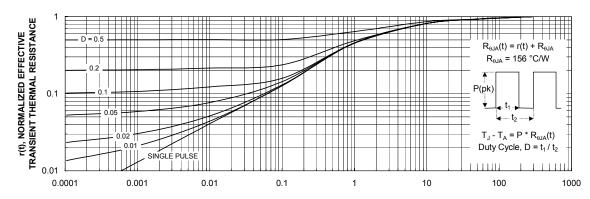


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1b. Transient thermal response will change depending on the circuit board design.





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