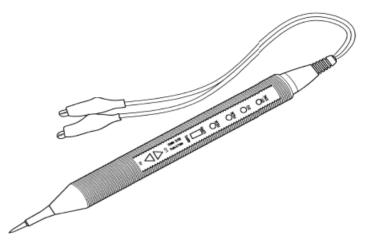
## **50 MHz Logic Probe & Pulser**







## **Specifications:**

Operating Supply Voltage Maximum Input Signal Frequency Input Impedance	: 4 V dc to 18 V dc : 50 MHz : 120 KΩ
TTL	: Logic "1" (HI LED) >3 V ±0.25 V
TTE .	Logic "0" (LO LED) <0.75 V ±25 V
CMOS	: Logic "1" (HI LED) >60% V <sub>CC</sub> ±5%
	Logic "0" (LO LED) <15% $V_{CC} \pm 5\%$ , <4% $V_{CC} \pm 5\%$ for 7 to 18 $V_{CC}$
Pulser	: Sync input impedance 1 M $\Omega$
Pulse Width	: 10 µs
Pulse Rate	: Switchable - 0.5 / 400 Hz
Minimum Detectable Pulse Width	: 10 ns
Dimensions	: 8.2 × 0.7 Inches (Diameter)
Operating Temperature	: 0°C to 50°C; 80% relative humidity
Weight	: Approximately 1.8 oz (50 g)
Output Current	: 100 mA sink / source
Square Wave Output Current	: 5 mA sink / source
Power Supply Protection	: 20 V dc (30 seconds maximum)
Sync Input Protection	: 120 V dc (30 seconds maximum)
Test Point Protection	: 35 V dc (30 seconds maximum)

## Part Number Table

Description	Part Number
Probe / pulser, Comb Logic, 50 MHz	72-500

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