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# Specification

## MC128064B6W-FPTLR



DOC.

#### DATASHEET STATEMENT

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  - 4.2: listing out definitely the tolerance.

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- **6.** Icons explanation

Midas 2006 version logo. Midas is an integrated manufacturer of flat panel display (FPD). Midas supplies TN, HTN, STN, FSTN monochrome LCD panel; COB, COG, TAB LCD module; and all kinds of LED backlight.



#### **FAST RESPONSE TIME**

This icon on the cover indicates the product is with high response speed; Otherwise not.



#### PROTECTION CIRCUIT

This icon on the cover indicates the product is with protection circuit; Otherwise not.



#### **HIGH CONTRAST**

This icon on the cover indicates the product is with high contrast; Otherwise not.



#### LONG LIFE VERSION

This icon on the cover indicates the product is long life version (over 9K hours guaranteed); Otherwise not.



#### **WIDE VIEWING SCOPE**

This icon on the cover indicates the product is with wide viewing scope; Otherwise not.



#### **Anti UV VERSION**

This icon on the cover indicates the product is against UV line. Otherwise not.



#### **RoHS COMPLIANCE**

This icon on the cover indicates the product meets ROHS requirements; Otherwise not.



#### **OPERATION TEMPERATURE RANGE**

This icon on the cover indicates the operating temperature range (X-Y).



#### **3TIMEs 100% QC EXAMINATION**

This icon on the cover indicates the product has passed Midas thrice 100% QC.
Otherwise not.



#### TWICE SELECTION OF LED MATERIALS

This icon on the cover indicates the LED had passed Midas twice strict selection which promises the product's identical color and brightness; Otherwise not.



#### Vlcm = 3.0V

This icon on the cover indicates the product can work at 3.0V exactly; otherwise not.



#### N SERIES TECHNOLOGY (2008 developed)

New structure, new craft, new technology and new materials inside both LCD module and LCD panel to improve the "RainBow"

10.	DATE	DES	CRIPTION	ITEM	PAGE	APPROVE
1	2012.03	INITI	IAL ISSUED	ALL	ALL	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1



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## **Midas LCD Part Number System**

MC COG 132033 A \* 6 W \* \* - S N T L W \* \*
1 2 3 4 5 6 7 8 9 - 10 11 12 13 14 15 16

1 = MC: Midas Components

2 = **Blank:** COB (chip on board) **COG**: chip on glass

3 = No of dots (e.g.  $240064 = 240 \times 64 \text{ dots}$ ) (e.g.  $21605 = 2 \times 16 \text{ 5mm C.H.}$ )

4 = Series

5 = Series Variant: A to Z - see addendum

6 = **3:** 3 o'clock **6:** 6 o'clock **9:** 9 o'clock **12:** 12 o'clock

7 = S: Normal (0 to + 50 deg C) W: Wide temp. (-20 to + 70 deg C) X: Extended temp (-30 + 80 Deg C)

8 = Character Set

Blank: Standard (English/Japanese)

C: Chinese Simplified (Graphic Displays only)

CB: Chinese Big 5 (Graphic Displays only)

H: Hebrew

K: European (std) (English/German/French/Greek)

L: English/Japanese (special)

M: European (English/Scandinavian)

R: Cyrillic

W: European (English/Greek)

U: European (English/Scandinavian/Icelandic)

9 = **Bezel Height** (where applicable / available)

	T	Common	Array
	Top of Bezel to Top of PCB	(via pins 1	or Edge
	011 CD	and 2)	Lit
Blank	9.5mm / not applicable	Common	Array
2	8.9 mm	Common	Array
3	7.8 mm	Separate	Array
4	7.8 mm	Common	Array
5	9.5 mm	Separate	Array
6	7  mm	Common	Array
7	7  mm	Separate	Array
8	6.4 mm	Common	Edge
9	6.4 mm	Separate	Edge
A	5.5 mm	Common	Edge
В	5.5 mm	Separate	Edge
D	6.0mm	Separate	Edge
$\mathbf{E}$	5.0mm	Separate	Edge
$\mathbf{F}$	4.7mm	Common	Edge
G	3.7mm	Separate	${EL}$

10 = **T:** TN **S:** STN **B:** STN Blue **G:** STN Grey **F:** FSTN **F2:** FFSTN

11 = **P:** Positive **N**: Negative

12 = **R:** Reflective **M:** Transmissive **T:** Transflective

13 = Backlight: Blank: Reflective L: LED

14 = Backlight Colour: Y: Yellow-Green W: White B: Blue R: Red A: Amber O: Orange G: Green RGB: R.G.B.

15 = **Driver Chip:** Blank: Standard I: I<sup>2</sup>C T: Toshiba T6963C A: Avant SAP1024B R: Raio RA 3635

16 = Voltage Variant: e.g. 3 = 3v

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#### 1. GENERAL SPECIFICATIONS

ITEM	NOMINAL DIMENSIONS / AVAILABLE OPTIONS
DISPLAY FORMAT	128 X 64 DOT MATRIX
LCD PANEL OPTIONS	FSTN (Silver-gray color)
POLARIZER OPTIONS	Positive, Transflective
BACKLIGHT OPTIONS	Array type LED backlight (Red color)
VIEWING ANGLE OPTIONS	6:00 ( Bottom )
TEMPERATURE RANGE OPTIONS	Wide temp. range ( -20°C ~ 70°C )
CONTROLLERIC	NT7107C+NT7108C
NEGATIVE IC	Built in
DISPLAY DUTY	1/64
DRIVING BIAS	1/9

#### 2. MECHANICAL SPECIFICATIONS

OVERALL SIZE	LED backlight version :		on: 78.0 x 70.0 x	78.0 x 70.0 x max 15.0		
VIEWING AREA	62.0W x 44.0H	mm	HOLE-HOLE	68.0W x 65.0H	mm	
DOT SIZE	0.40W x 0.56H	mm	DOT PITCH	0.04W x 0.04H	mm	
WEIGHT (W/O BKL)	55.0	g	WEIGHT (LED BKL)	78.0	g	

#### 3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	MIN	MAX	UNIT
POWER SUPPLY (LOGIC)	Vdd	25°C	-0.3	7.0	V
POWER SUPPLY (LCD)	V0	25°C	Vdd -19.0	Vdd +0.3	V
INPUT VOLTAGE	Vin	25°C	-0.3	Vd <mark>d +</mark> 0.3	V
OPERATING TEMPERATURE	Vopr		-20	70	°C
STORAGE TEMPERATURE	Vstg	/ /	-30	80	°C

#### 4. ELECTRONICAL CHARACTERISTIC\*

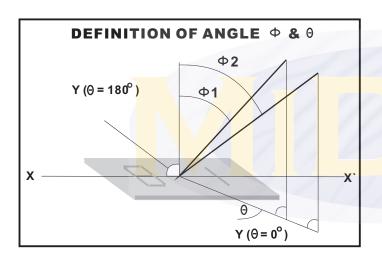
1754	CYMPOL	CONDITION	S	UNIT			
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNII	
Input voltage	Vdd	+5V	2.7	5.0	5.5	V	
Supply current	ldd	Vdd=5V		2.1		mA	
		-20°C	8.40		8.90		
Recommended LCD driving		0°C	8.10		8.55		
voltage for normal temp.	Vdd - V0	25°C	8.00	8.20	8.40	V	
Version module		50°C	7.90		8.30		
		70°C	7.75		8.10		
LED forward voltage	Vf	25°C	3.6		4.4	V	
LED forward current	If	25°C		240		mA	
LED reverse Current	lr	25°C			240	μA	
LED Peak wave length	λр	25°C If = 240mA	620		630	nm	
LED illuminance (Without LCD)	Lv	25°C If = 240mA				cd/m²	
LED life time		25°C If = 240mA	9K**			Hours	

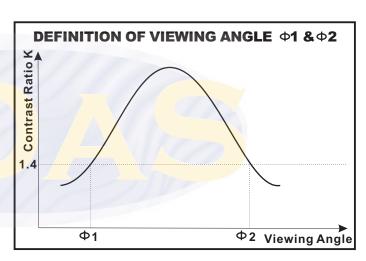
<sup>\*</sup> The above data are for reference only.

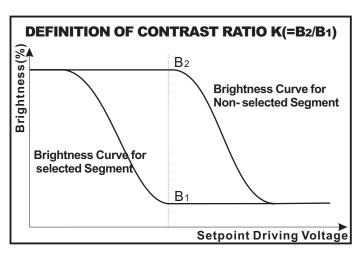
#### 5. OPTICAL CHARACTERISTIC

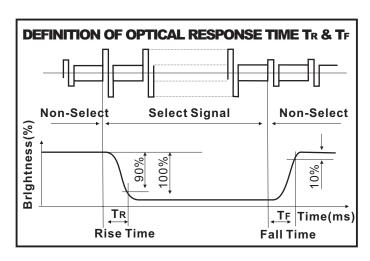
FOR TN TYPE LCD MODULE (TA=25°C, Vdd=5.0V ± 0.25V)								
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT		
VIEWING ANGLE	Ф2-Ф 1	K=4	30			deg		
VIEWING ANGLE	Θ	N=4	25			ueg		
CONTRAST RATIO	K			2				
RESPONSE TIME(RISE)	<b>T</b> R			120	150	ms		
RESPONSE TIME(FALL)	<b>T</b> F			120	150	ms		

FOR STN TYPE LCD MODULE (TA=25 °C, Vdd=5.0V ± 0.25V)								
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT		
\/  = \/  \/  \/  \	Ф2-Ф 1	17 – 4	40			doa		
VIEWING ANGLE	Θ	K=4	60			deg		
CONTRAST RATIO	K			6				
RESPONSE TIME(RISE)	<b>T</b> R			150	250	ms		
RESPONSE TIME(FALL)	<b>T</b> F			150	250	ms		









## 6. DC CHARACTERISTIC

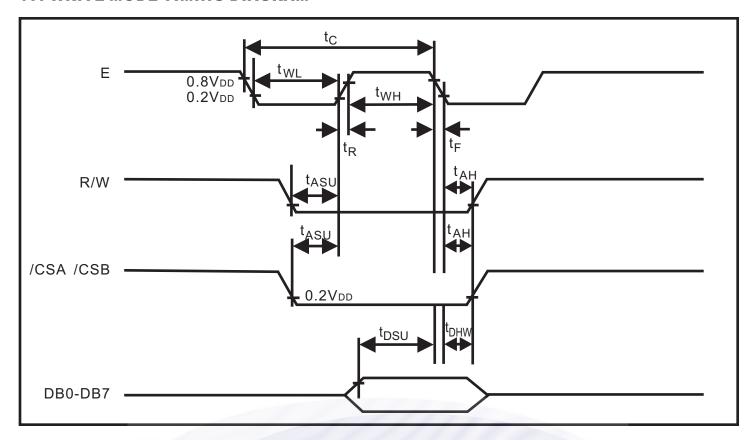
(Unless otherwise stated, VDD= +5V ± 10%, VSS=0V, Ta=25 °C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
High Level Input Voltage	VIH1		0.7VDD		VDD	V
Trigit Level input voltage	VIH2		0.7VDD		VDD	V
Low Level Input Voltage	VIL1		0		0.3VDD	V
Low Level input voitage	VIL2		0		0.8	V
High Level Output Voltage	VOH	IOH = - 200µA	2.4			٧
Low Level Output Voltage	VOL	IOL = 1.6 mA			0.4	V
Input Leakage Current	ILKG	VIN = VDD to VSS	-1.0		1.0	μ <b>Α</b>
Three-State (OFF) Input Current	ITSL	VIN = VDD to VSS	-5.0		5.0	μ <b>А</b>
	IDD1	During Display			100	μ <b>Α</b>
Operating Current	IDD2	During Access, Access Cycle = 1MHz			500	μ <b>Α</b>

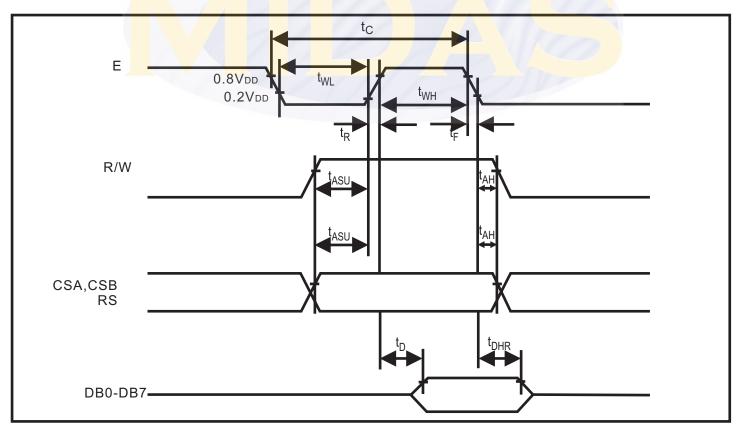
#### 7. AC CHARACTERISTIC

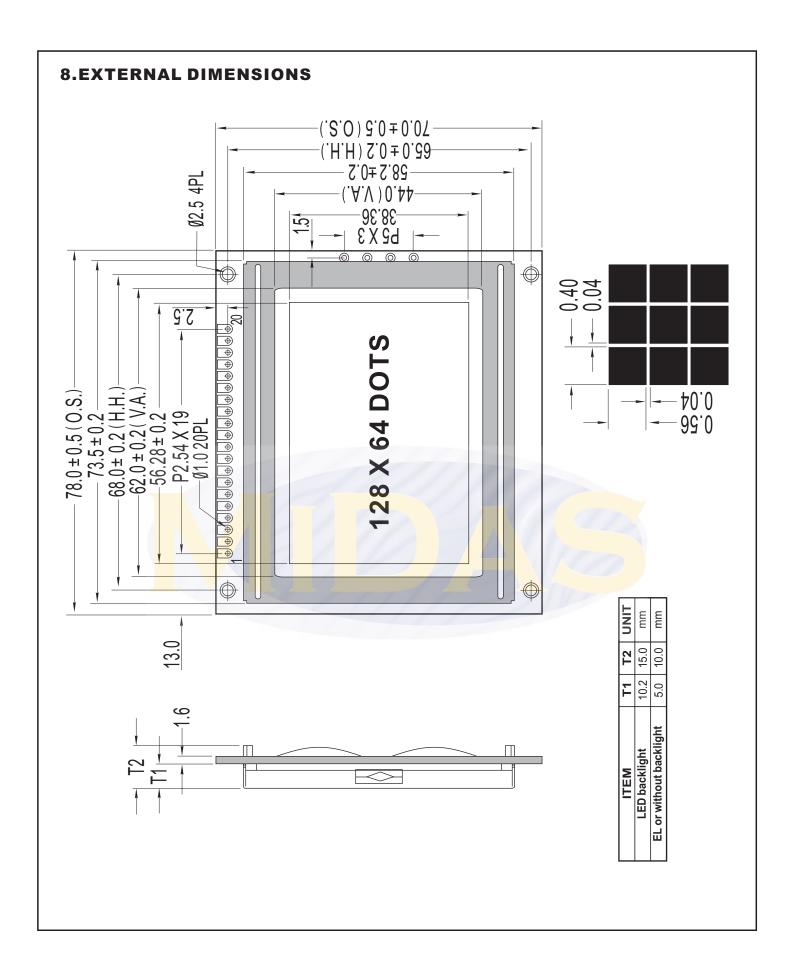
			//A		
Character <mark>ist</mark> ic	Symbol Symbol	Min	Тур	Max	Unit
E Cycl <mark>e</mark>	tc	1000			ns
E High Level Width	t wh	450		_	ns
E Low Level Width	t wL	450	_	_	ns
E Rise Time	t <sub>R</sub>	_	_	25	ns
E Fall Time	t <sub>F</sub>	_	_	25	ns
Address Setup Time	t ASU	140	_	_	ns
Address Hold Time	t ah	10	_	_	ns
Data Setup Time	t DSU	200	_	_	ns
Data Delay Time	t <sub>D</sub>	_	_	320	ns
Data Hold Time (Write)	t dhw	10	_	_	ns
Data Hold Time (Read)	t DHR	20	_	_	ns

#### 7.1 WRITE MODE TIMING DIAGRAM



#### 7.2 READ MODE TIMING DIAGRAM

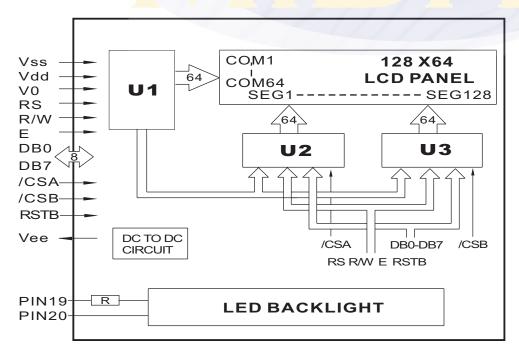




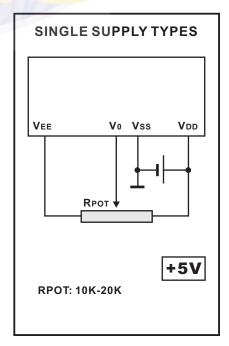
#### 9. PIN ASSIGNMENT

PIN NO.	SYMBOL	FUNCTION		REMARK
1	/CSA	When /CSA=L,/CSB=H, select U2		
2	/CSB	When /CSB=L	,/CSA=H, select U3	
3	Vss		0V	
4	Vdd	Power Supply	+5V	
5	V0		Contrast Adjust	
6	RS	Register	select signal	
7	R/W	Read	d / Write	
8	E	Chip En	able signal	
9	DB0	Data Bit 0		
10	DB1	Dat	a Bit 1	
11	DB2	Dat	a Bit 2	
12	DB3	Dat	a Bit 3	
13	DB4	Dat	a Bit 4	
14	DB5	Dat	a Bit 5	
15	DB6	Dat	a Bit 6	
16	DB7	Data Bit 7		
17	RSTB	Reset signal		
18	Vee	Negative voltage output		
19	LED+	Anode of LED Unit		5.0V
20	LED-	Cathode	of LED Unit	0V

#### 10. BLOCK DIAGRAM



#### 11. POWER SUPPLY



#### 12. FUNCTIONAL DESCRIPTION

#### **12.1 RESET**

The system can be initialized by setting the RSTB to LOW when turning the power ON or by instruction from the MPU. When the RSTB is set to LOW, the following condition occurs:

- 1. The Display is turned OFF.
- 2. The Display Start Line register is set to 0 (Z-Address 0).

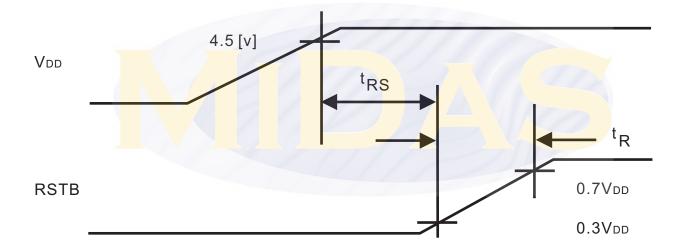
No instructions except the status read can be executed when the RSTB is LOW. This means that order to execute other instructions, the RSTB must be cleared by setting DB4 to 0 and the DB7 status read instruction.

The table below shows the power supply initial conditions.

in set to 0

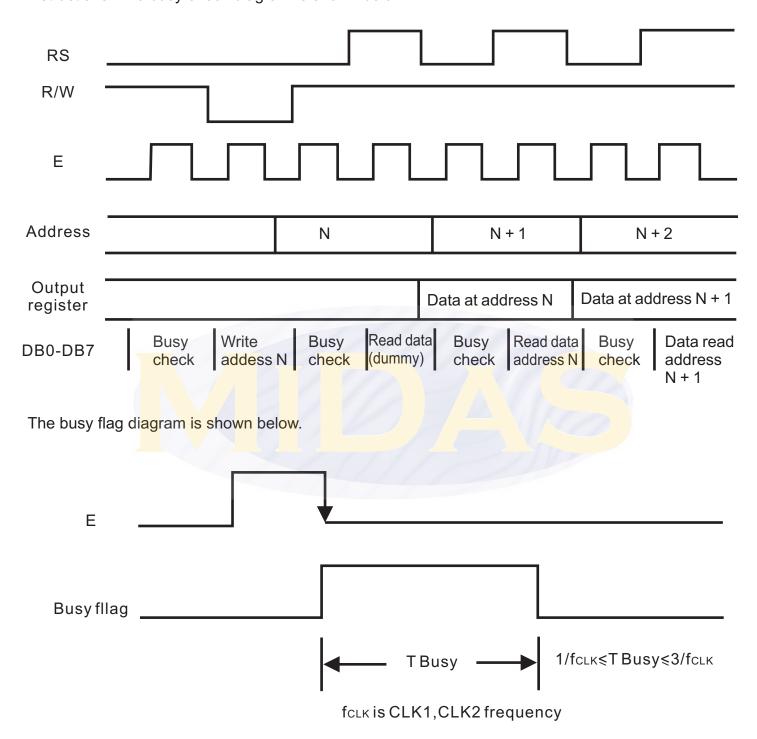
by

Parameter	Symbol	Min.	Тур.	Max.	Unit
Reset Time	tRS	1.0	-	-	uS
Rise Time	tR	-	-	200	nS

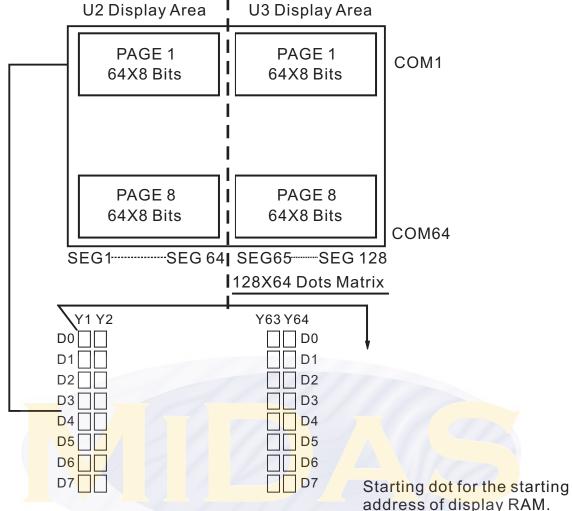


#### 12.2 BUSY FLAG

The busy flag (DB7) is used to determine whether Nt7108 is operating or not. When the busy flag is HIGH, internal operation is taking place. When the busy flag is LOW, Nt7108 can accept data or instructions. The busy check diagram is shown below.



#### 12.3 RELATION BETWEEN DISPLAY PATTERN AND DRIVERS



Each segment driver has 8 pages RAM, and each page has 64x8 bits RAM. D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.

#### 12.4 DISPLAY DATA RAM

The Display Data RAM is used to store the display data for the liquid crystal display. Write data 1 indicates an ON State of the LCDs dot matrix while the OFF State is written as 0. ADC Signal can control the Display Data RAM and the segment output. Please refer to the table below.

ADC *	Display Data
Н	Y-Address 0:S1 to Y-Address 63:S64

is

## 13. INSTRUCTION

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	L	L	L	L	Н	Н	Н	Н	Н	L/H	Controls the display on or off. Internal status and display RAM data is not affected.  L:OFF H:ON
Set address (Y address)	L	L	L	Н		Υá	addres	s (0~6	53)		Sets the Y address in the Y address counter.
Set Page (X address)	L	اــ	Η	L	I	Н	Η	Pa	ge (0-	~7)	Sets the X address at the X address register.
Display Start Line (Z address)	L		I	Ι		Disp	lay sta	rt line (	(0~63)		Indicates the display data RAM displayed at the top of the screen.
Status Read		H	BUSY		0 N / O F F	R E S E T	L		L		BUSY L:Ready H:In operation ON/OFF L:Display ON H:Display OFF RESET L:Normal H:Reset
Write Display Data	Н	L				Writ	e Data	1			Writes data (DB0:7) into display data RAM,After writing instruction,Y address is increased by 1 automatically.
Read Display Data	Н	Ι	Read Data					Reads data (DB0:7) from display data RAM to the data bus.			

#### 14. DESCRIPTION OF COMMAND

## **Display On/Off**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0,it remains in the display data RAM.

Therefore, you can make it appear by changing D=0 into D=1.

## Set Address(Y Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

## Set Page(X Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

## **Display Start Line(Z Address)**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen.

When the display duty cycle is 1/64 or others(1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

## **Status Read**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	Db1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

#### BUSY

When BUSY is 1,the Chip is executing internal operation and no instructions are accepted. When BUSY is 0,the Chip is ready to accept any instructions.

#### ON/OFF

When ON/OFF is 1,the display is off. When ON/OFF is 0,the display is on.

#### RESET

When RESET is 1,the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0,initializing has finished and the system is in the usual operation condition.

## Write Display Data

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM.

After writing instruction, Y address is increased by 1 automatically.

## **Read Display Data**

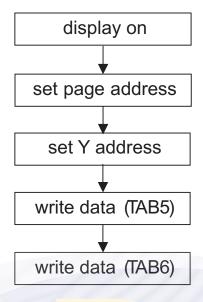
	R/W									_
1	1	D7	D6	D5	D4	D3	D2	D1	D0	

Reads data (D0-D7) from the display data RAM.

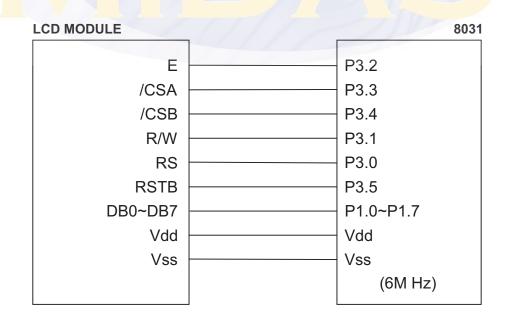
After reading instruction, Y address is increased by 1 automatically.

## 15. APPLICATION EXAMPLE

## **Application Flowchart**



## Application Circuit



## **16. PACKING DETAIL**

WITH LED BKL
30 PCS/BOX
8 BOXES/CARTON
240 PCS/CARTON
19.00 KGS/CTN(G.W.)
0.07 M³/CARTON

WITHOUT LED BKL
30 PCS/BOX
8 BOXES/CARTON
240 PCS/CARTON
17.00 KGS/CTN(G.W.)
0.07 M³/CARTON

#### NOTE

- 1. The weight is estimated for reference only.
- 2. Packing detail may be changed without notice.

