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# Specification

# MCOG027BW100016MY



#### **BOOKBINDING AREA**

DOC.

### DATASHEET STATEMENT

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  4.2: listing out definitely the tolerance.

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- 5. The sequence of the icons is random and doesn't indicate the importance grade.
- 6. Icons explanation

Midas 2006 version logo.Midas is an integrated manufacturer of flat panel display (FPD). Midas supplies TN, HTN, STN, FSTN monochrome LCD panel; COB, COG, TAB LCD module; and all kinds of LED backlight.



### FAST RESPONSE TIME

This icon on the cover indicates the product is with high response speed; Otherwise not.

	C	
	$\mathbf{\Sigma}$	

### HIGH CONTRAST

This icon on the cover indicates the product is with high contrast; Otherwise not.



### WIDE VIEWING SCOPE

This icon on the cover indicates the product is with wide viewing scope; Otherwise not.



### RoHS COMPLIANCE

This icon on the cover indicates the product meets ROHS requirements; Otherwise not.



**3TIMEs 100% QC EXAMINATION** This icon on the cover indicates the product

has passed Midas thrice 100% QC. Otherwise not.



#### VIcm = 3.0V

This icon on the cover indicates the product can work at 3.0V exactly; otherwise not.



### **PROTECTION CIRCUIT**

This icon on the cover indicates the product is with protection circuit; Otherwise not.



### LONG LIFE VERSION

This icon on the cover indicates the product is long life version (over 9K hours guaranteed); Otherwise not.



### Anti UV VERSION

This icon on the cover indicates the product is against UV line. Otherwise not.



### **OPERATION TEMPERATURE RANGE**

This icon on the cover indicates the operating temperature range (X-Y).



### TWICE SELECTION OF LED MATERIALS

This icon on the cover indicates the LED had passed Midas twice strict selection which promises the product's identical color and brightness; Otherwise not.



#### N SERIES TECHNOLOGY (2008 developed) New structure, new craft, new technology and new materials inside both LCD module and LCD panel to improve the "RainBow"

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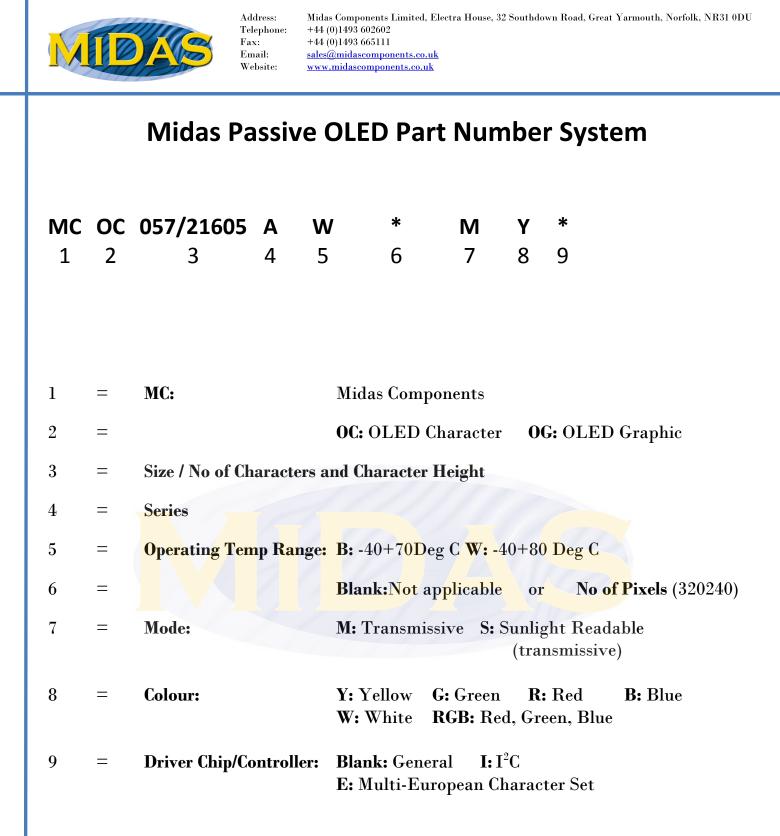
# **1. Revision History**

DATE	VERSION	REVISED PAGE NO.	Note
2010/09/24	1		First issue
2011/07/06	2		Change version

### 2. General Specification

The Features is described as follow:

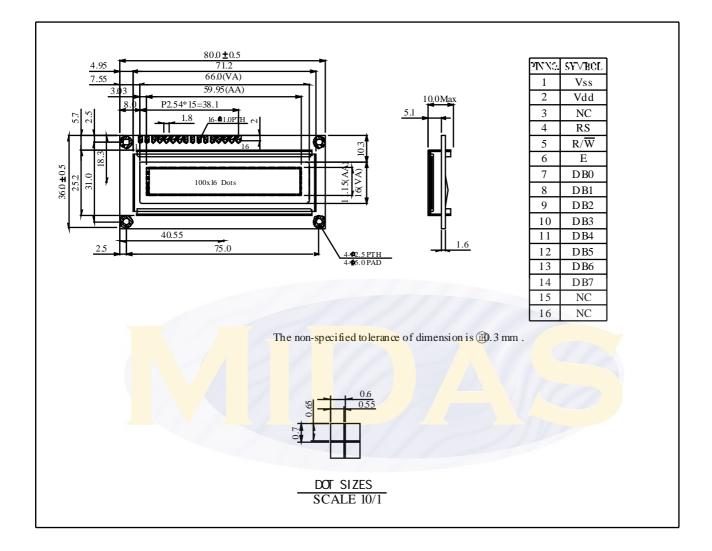
- Module dimension: 80.0 x 36.0 x 10.0 (max.) mm<sup>3</sup>
- View area: 66.0 x 16.0 mm<sup>2</sup>
- Active area: 59.95 x 11.15 mm<sup>2</sup>
- Number of dots: 100\*16
- Pixel size:  $0.55 \times 0.65 \text{ mm}^2$
- Pixel pitch: 0.60x 0.70 mm2
- Duty: 1/16
- Emitting Color: Yellow



# 4. Interface Pin Function

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC		
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(MPU $\rightarrow$ Module), L: Write(MPU $\rightarrow$ Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	D <mark>B5</mark>	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	D <mark>B7</mark>	H/L	Data bit 7
15	NC		
16	NC		





	MPU 68 Series BB0-DB7			<b>* * * *</b>		3001 ntro	0 ller IC	2	F	EG1-				002	(16)	Pixe	els OLEI	)	
	Address	s For	mat			DB7	D	36	DB5	D	B4	DE	33	DE	32	DE	31	DB0	
GXA	(Graphi	c X-a	xis A	ddres	ss	1	AD	D6 A	DD:	5 AC	DD4	AD	D3	AD	D2	AD	D1	ADD0	
GYA	(Graphi	c Y-a	xis Ao	ddres	ss	0 1		1	0		0		)	0	)	0	)	CGA0	
	1	2	3	3	4				_	97	9	8	9	9	10	)0			
CGA=0	GXA=10000000 GYA=01000000	GXA=10000001	GXA=10000010	GYA=01000000	GXA=10000011 GYA=01000000				CXA-1110000	GYA=01000000	GXA=11100001	GYA=01000000	GXA=11100010	GYA=01000000	GXA=11100011	GYA=01000000			
CGA=1	GXA=10000000 GXA=10000000 GYA=01000001 GYA=01000000	GXA=1000001	GXA=10000010 GXA=10000010		GXA=10000011 GYA=01000011				CXA=11100000	_	GXA=11100001	GYA=01000001	GXA=11100010	GYA=01000001	GXA=11100011	GYA=01000001			

# 6. Absolute Maximum Ratings

ltem	Symbol	Min	Max	Unit	Notes
Operating Temperature	T <sub>OP</sub>	-40	+80	°C	
Storage Temperature	T <sub>ST</sub>	-40	+80	°C	
Input Voltage	VI	-0.3	VDD	V	
Supply Voltage For Logic	VDD-V <sub>SS</sub>	-0.3	5.3	V	

### 7. Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltag <mark>e For Logic</mark>	VDD-VSS		3.0	5.0	5.3	V
Input High Volt.	VIH		0.9 VDD		VDD	V
Input Low Volt.	VIL	-	GND	_	0.1VDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8 VDD		VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	_	0.2 VDD	V
Supply Current	IDD	VDD=5V	—	30	_	mA
CIEx(Yellow)		x,y(CIE1931)	0.44	0.48	0.52	
CIEy(Yellow)		x,y(CIE1931)	0.46	0.50	0.54	

### 8. Optical Characteristics

ltem	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ		160			deg
view Angle	(H)φ		160			deg
Contrast Ratio	CR	Dark	2000:1		—	-
	Trise			10		μs
Response Time	T fall			10		μs
Supply Voltage For Log	jic 5V	With polarizer		125		nits
50% Check Board Brightness		150mW(5V*30mA)				Note1
Supply Voltage For Logic 3V		With polarizer		80		nits
50% Check Board Brig	htness		111			

Notes: 1.When random texts pattern is running , averagely , at any instance , about 1/2 of pixels will be on.

2. You can to use the display off mode to make long life.

### 9. OLED Lifetime

ITEM	Conditions	Тур	Remark
Operating Life Time	Ta=25℃ /Initial 50% checkboard brightness125nits	100,000 Hrs	Note

Notes:

1. Simulation pattern for operation test: interchanging with 50% checkboard. The brightness decay does not exceed 50%

- 2. You can use the display off mode to make long life.
- 3. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

## 10. Reliability

Environmenta	II Test		
Test Item	Content of Test	Test Condition	Applicable Standard
High Temperature storage	Enduranœ test applying the high storage temperature for a long time.	80℃ 240hrs	
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80℃ 240hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40℃ 240hrs	
High Temperature/ Humidity Storage	Enduranœ test applying the high temperature and high humidity storage for a long time.	60℃,90%RH 240hrs	
Temperature Cycle	Endurance test applying the low and high temperature cycle. -40°C25°C80°C 30min5min30min 1 cycle	-40℃ <mark>/80℃</mark> 100 cycles	¥,g
Mechanical <mark>Te</mark>	st		In
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	
Shock te <i>s</i> t	Constructional and mechanical enduranœ test applying the shock during transportation.	50G Half sign wave 11 msedc 3 times of each direction	
Atmospheric pressure test	Enduranœ test applying the atmospheric pressure during transportation by air.	115mbar 40hrs	
Others			
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	

### **Content of Reliability Test**

\*\*\*Supply voltage for logic system=5V. Supply voltage for OLED system =Operating voltage at 25°C

### Test and measurement conditions

- All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.
- 2. All-pixels-on is used as operation test pattern.
- 3. The degradation of Polarizer are ignored for High Temperature storage, High Temperature/Humidity Storage, Temperature Cycle

### **Evaluation criteria**

- 1. The function test is OK.
- 2. No observable defects.
- 3. Luminance: > 50% of initial value.
- 4. Current consumption: within ± 50% of initial value.

### APP ENDIX:

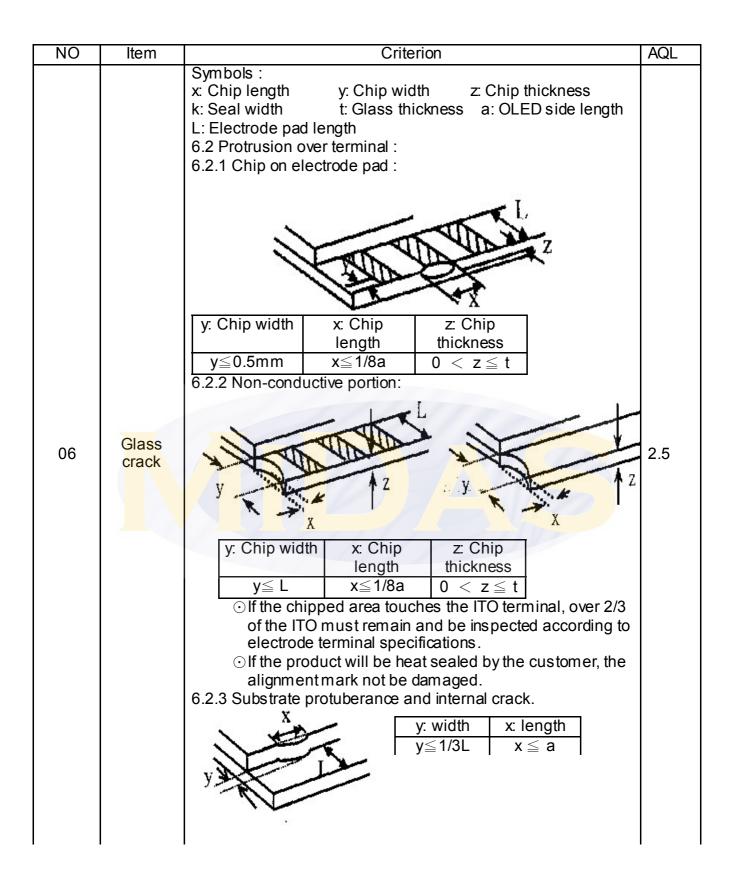
### **RESIDUE IMAGE**

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.

# 11. Inspection specification

NO	ltem	Criterion	AQL					
01	Electrical Testing	<ul> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 Viewing angle defect.</li> <li>1.7 Mixed product types.</li> <li>1.8 Contrast defect.</li> </ul>						
02	Black or bright spots on OLED (display only)	.1 Bright and black spots on display ≦0.25mm, no more than three Bright or black spots present. .2 Densely spaced: No more than two spots or lines within 3mm						
03	Black spots, bright spots, contaminatio n (non-display)	<ul> <li>3.1 Round type : As follow ing drawing Φ=( x + y ) / 2</li> <li>3.2 Line type : (As follow ing drawing)</li> </ul>	2.5					
		$\begin{array}{c c c c c c c c c c c c c c c c c c c $	2.5					
04	Polarizer bubbles	$ \begin{array}{ c c c c c } \mbox{If bubbles are visible, judge} \\ \mbox{using black spot} \\ \mbox{specifications, not easy to} \\ \mbox{find, must check in specify} \\ \mbox{direction.} \end{array} \begin{array}{ c c c } \mbox{Size } \Phi & \mbox{Acceptable Q} \\ \mbox{TY} \\ \mbox{\Phi \leq 0.20} & \mbox{Accept no} \\ \mbox{dense} \\ \mbox{0.20 < \Phi \leq 0.50} & \mbox{3} \\ \mbox{0.50 < \Phi \leq 1.00} & \mbox{2} \\ \mbox{1.00 < \Phi} & \mbox{0} \\ \mbox{Total Q TY} & \mbox{3} \\ \end{array} $	2.5					

NO	Item	Criterion	AQL
05	Scratche s	Follow NO.3 black spots, bright spots, contamination	
06	Chipped glass	Symbols Define: x Chip length k: Seal width L: Electrode pad length:z. Chip thickness a: OLED side length L: Electrode pad length:6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels: $\overrightarrow{a}$	2.5
		$\odot$ If there are 2 or more chips, x is the total length of each chip.	



NO	Item	Criterion	AQL		
07	Cracked glass	The OLED with extensive crack is not acceptable.	2.5		
08	Bezel	<ul> <li>8.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li> <li>8.2 Bezel must comply with job specifications.</li> </ul>			
9		<ul> <li>9.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>9.2 COB seal surface may not have pinholes through to the IC.</li> <li>9.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>9.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</li> <li>9.5 No oxidation or contamination PCB terminals.</li> <li>9.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</li> <li>9.7 The jumper on the PCB should conform to the product characteristic chart.</li> <li>9.8 If solder gets on bezel tab pads, zebra pad or screw hold pad, make sure it is smoothed down.</li> <li>9.9 The Scraping testing standard for Copper Coating of PCB</li> </ul>	<ul> <li>2.5</li> <li>2.5</li> <li>0.65</li> <li>2.5</li> <li>0.65</li> <li>0.65</li> <li>2.5</li> <li>2.5</li> <li>2.5</li> </ul>		
10	Soldering	<ul> <li>10.1 No un-melted solder paste may be present on the PCB.</li> <li>10.2 No cold solder joints, missing solder connections, oxidation or icide.</li> <li>10.3 No residue or solder balls on PCB.</li> <li>10.4 No short circuits in components on PCB.</li> </ul>	2.5 2.5 2.5 0.65		

11In the interface Pin (OLB) of TCP.11.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.0.6511.2 No cracks on interface pin (OLB) of TCP.11.3 No contamination, solder residue or solder balls on product.2.511.4 The IC on the TCP may not be damaged, circuits.11.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.2.511.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.2.511.7 Sealant on top of the ITO circuit has not hardened.0.6511.9 OLED pin loose or missing pins.0.6511.10 Product packaging must the same as specified on packaging specification sheet.0.6511.11 Product dimension and structure must conform to product specification sheet.0.65	NO	Item	Criterion	AQL
	11		<ul> <li>interface Pin (OLB) of TCP.</li> <li>11.2 No cracks on interface pin (OLB) of TCP.</li> <li>11.3 No contamination, solder residue or solder balls on product.</li> <li>11.4 The IC on the TCP may not be damaged, circuits.</li> <li>11.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.</li> <li>11.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.</li> <li>11.7 Sealant on top of the ITO circuit has not hardened.</li> <li>11.8 Pin type must match type in specification sheet.</li> <li>11.9 OLED pin loose or missing pins.</li> <li>11.10 Product packaging must the same as specified on packaging specification sheet.</li> <li>11.11 Product dimension and structure must conform to</li> </ul>	0.65 2.5 2.5 2.5 2.5 2.5 2.5 0.65 0.65 0.65



Check Item	Classification	Criteria
No Display	Major	
Missing Line	Major	2 % 2 % 4 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 2 % 3 %       2 % 2 %       2 % 2 %       3 % 2 %         2 % 3 % 4 %       2 % 3 %       2 % 4 %       3 % 2 %         2 % 3 % 4 %       2 % 4 %       2 % 4 %       3 % 2 %         2 % 3 % 4 %       2 % 4 %       2 % 4 %       3 % 2 %         2 % 4 %       3 % 4 %       2 % 4 %       3 % 2 %         2 % 4 %       3 % 4 %       3 % 4 %       3 % 4 %         2 % 4 %       3 % 4 %       3 % 4 %       3 % 4 %         3 % 4 %       3 % 4 %       3 % 4 %       3 % 4 %         3 % 5 %       3 % 4 %       3 % 4 %       3 % 4 %         3 % 5 %       3 % 4 %       3 % 4 %       3 % 4 %
Pixel Short	Major	
Darker Short	Major	1     1
Wrong Display	Major	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
Un-unifom B/A x 100% < 70% A/C x 100% < 70%	Major	A Normal B Dark Pixel C Light Pixel

### 12. Precautions in use of Modules

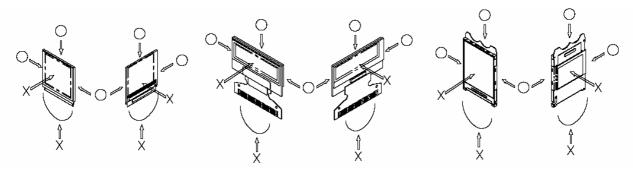
- 1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- 2. Don't make extra holes on the printed circuit board, modify its shape or change the components of OLED module.
- 3. Don't disassemble the OLED module.
- 4. Don't operate it above the absolute maximum rating.
- 5. Don't drop, bend or twist OLED module.
- 6. Soldering: only to the I/O terminals.
- 7. Storage: please storage in anti-static electricity container and clean environment.
- 8. Midas have the right to change the passive components (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- 9. Midas have the right to change the PCB Rev.

### 12.1 Handling Precautions

- (1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
- (2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- (3) If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
- (4) The polarizer covering the surface of the OLED display module is soft and easily scratched. Please be careful when handling the OLED display module.
- (5) When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage of by using following adhesion tape.
- \* Scotch Mending Tape No. 810 or an equivalent

Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent Also, pay attention that the following liquid and solvent may spoil the polarizer:

- \* Water
- \* Ketone
- \* Aromatic Solvents
- (6) Hold OLED display module very carefully when placing OLED display module into the System housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.



- (7) Do not apply stress to the LSI chips and the surrounding molded sections.
- (8) Do not disassemble nor modify the OLED display module.
- (9) Do not apply input signals while the logic power is off.
- (10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
- \* Be sure to make human body grounding when handling OLED display modules.
- \* Be sure to ground tools to use or assembly such as soldering irons.
- \* To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
- \* Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.
- (11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5.
- (12) If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

### **12.2 Storage Precautions**

- (1) When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. And, also, avoiding high temperature and high humidity environment or low temperature (less than 0°C) environments.(We recommend you to store these modules in the packaged state when they were shipped from Midas Components Ltd. At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
- (2) If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.

### **12.3 Designing Precautions**

- (1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen.
- (2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specifications and, at the same time, to make the signal line cable as short as possible.
- (3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD). (Recommend value: 0.5A)

(4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.

- (5) As for EMI, take necessary measures on the equipment side basically.
- (6) When fastening the OLED display module, fasten the external plastic housing section.
- (7) If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module. Connection (contact) to any other potential than the above may lead to rupture of the IC.

