OM11040

IAR LPC1343 Evaluation Board

Demo board description

The IAR LPC1343 Evaluation Board allows you to quickly and easily evaluate the LPC13xx family of microcontrollers. The microcontroller, board, and accompanying features make it a great starting point for your next Cortex-M3 project.

Populated with the LPC1343 to demonstrate LPC13xx features, the LPC1343 board allows the evaluation of the robust USB performance of the LPC1343.

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Features

- Debug through J-Link-OB, a debug interface mounted on the board
- Standard JTAG connector (with SWD only) and small SWD connector
- Power up the board through USB, J-Link (USB cable included) or external power adapter (not included)
- USB connector and EXT connector
- Small LCD
- Two user buttons and a Reset button
- Two user LEDs and a Power LED
- Analog trim wheel
- Buzzer
- UART

Descriptive summary

IAR LPC1343 Evaluation Board

IAR's LPC1343 Evaluation Board lets you get up-and-running quickly in evaluating the superior performance and capability of the LPC1343. The LPC1343 is developed with a highly flexible USB architecture featuring a high level of integration and low power consumption. The LPC1343 includes on-chip USB drivers for mass storage class (MSC) and human interface device (HID).