

76mm (3.0INCH) SINGLE DIGIT DISPLAY

Part Number: SC30-21YWA Yellow

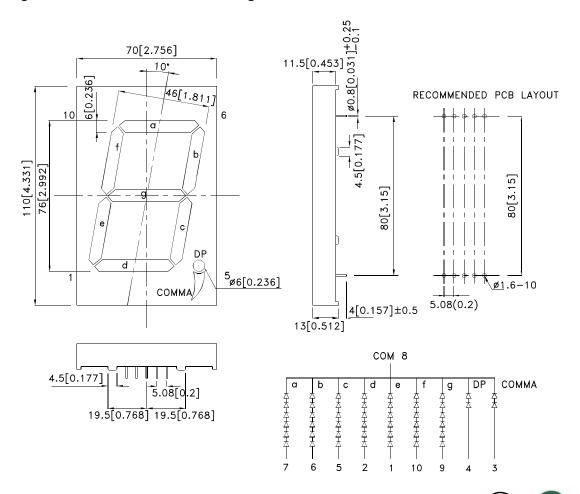
Features

- 3.0 inch digit height.
- Low current operation.
- Excellent character appearance.
- Easy mounting on P.C. boards or sockets.
- · Categorized for luminous intensity.
- Mechanically rugged.
- Standard : gray face, white segment.
- RoHS compliant.

Description

The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode.

Package Dimensions& Internal Circuit Diagram





1. All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01")unless otherwise noted.

2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SPEC NO: DSAM8239 REV NO: V.1A DATE: JAN/12/2013
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: C.H.HAN

PAGE: 1 OF 7

ERP: 1301003367

Selection Guide

Part No.	Dice	Lens Type	lv (mcd) [1] @ 10mA		Description
			Min.	Тур.	2000., p
SC30-21YWA	Yellow (GaAsP/GaP)	White Diffused	14	30	Common Cathode , Rt. Hand Decimal.
			*5.6	*12	

- Notes:
 1. Luminous intensity/ luminous Flux: +/-15%.

 * Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Yellow	590		nm	IF=20mA
λD [1]	Dominant Wavelength	Yellow	588		nm	IF=20mA
Δλ1/2	Spectral Line Half-width	Yellow	35		nm	IF=20mA
С	Capacitance	Yellow	20		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage Per segment or(Dp and Comma)	Yellow	12.6 (4.2)	15.0 (5.0)	V	IF=20mA
lR	Reverse Current Per segment or(Dp and Comma)	Yellow		10 (10)	uA	V _R = 5V

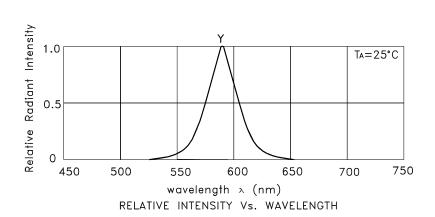
- Navelength: +/-1nm.
 Forward Voltage: +/-0.1V.
 Wavelength value is traceable to the CIE127-2007 compliant national standards.

Absolute Maximum Ratings at TA=25°C

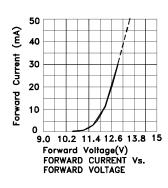
Parameter	Yellow	Units
Power dissipation Per segment or(Dp and Comma)	450(150)	mW
DC Forward Current Per segment or(Dp and Comma)	30(30)	mA
Peak Forward Current [1] Per segment or(Dp and Comma)	140(140)	mA
Reverse Voltage Per segment or(Dp and Comma)	5(5)	V
Operating / Storage Temperature	orage Temperature -40°C To +85°C	
ead Solder Temperature[2] 260°C For 3-5 Seconds		

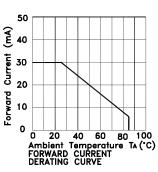
- Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. 2mm below package base.

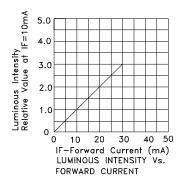
SPEC NO: DSAM8239 **REV NO: V.1A** DATE: JAN/12/2013 PAGE: 2 OF 7 APPROVED: WYNEC **CHECKED:** Joe Lee DRAWN: C.H.HAN ERP: 1301003367

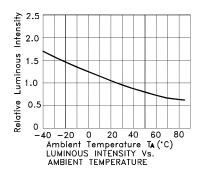


Yellow SC30-21YWA

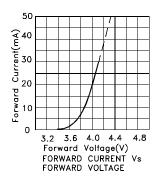


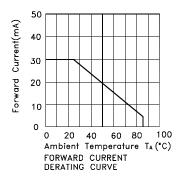


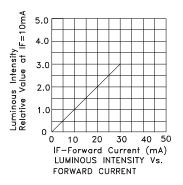


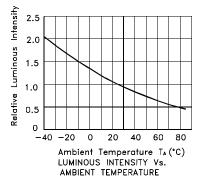


SPEC NO: DSAM8239 APPROVED: WYNEC REV NO: V.1A CHECKED: Joe Lee DATE: JAN/12/2013 DRAWN: C.H.HAN PAGE: 3 OF 7 ERP: 1301003367

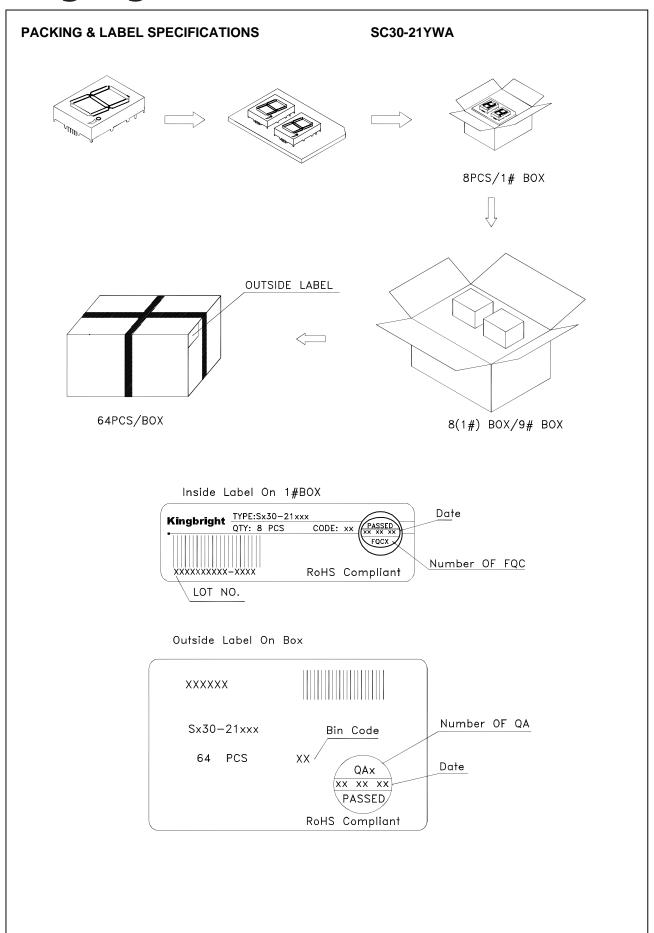








SPEC NO: DSAM8239 REV NO: V.1A DATE: JAN/12/2013 PAGE: 4 OF 7
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: C.H.HAN ERP: 1301003367

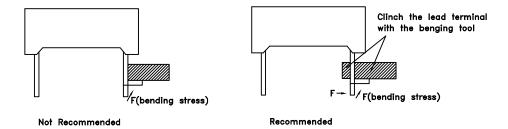


SPEC NO: DSAM8239 APPROVED: WYNEC REV NO: V.1A CHECKED: Joe Lee DATE: JAN/12/2013 DRAWN: C.H.HAN PAGE: 5 OF 7 ERP: 1301003367

THROUGH HOLE DISPLAY MOUNTING METHOD

Lead Forming

Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

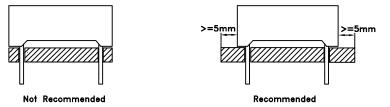


Installation

- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



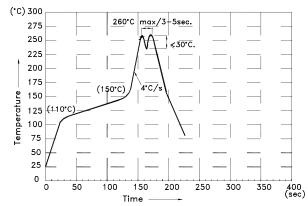
3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.



SPEC NO: DSAM8239 APPROVED: WYNEC REV NO: V.1A CHECKED: Joe Lee DATE: JAN/12/2013 DRAWN: C.H.HAN PAGE: 6 OF 7 ERP: 1301003367

DISPLAY SOLDERING CONDITIONS

Wave Soldering Profile For Lead-free Through-hole LED.



NOTES:

- 1.Recommend the wave temperature 245°C~260°C.The maximum soldering temperature should be less than 260°C.
- 2.Do not apply stress on epoxy resins when temperature is over 85°C.
- 3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- 4.During wave soldering , the PCB top-surface temperature should be kept below 105°C
- 5.No more than once.

Soldering General Notes:

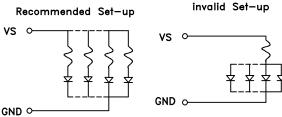
- 1. Through—hole displays are incompatible with reflow soldering.
- 2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

- 1.Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts .And the devices should not be washed for more than one minute.

CIRCUIT DESIGN NOTES

- 1.Protective current—limiting resistors may be necessary to operate the Displays.
- 2.LEDs mounted in parallel should each be placed in series with its own current—limiting resistor.



Detailed application notes are listed on our website. http://www.kingbright.com/application notes

SPEC NO: DSAM8239 REV NO: V.1A DATE: JAN/12/2013 PAGE: 7 OF 7
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: C.H.HAN ERP: 1301003367