

### Features:

- · High voltage, high speed transistor for horizontal deflection output stages of TV and CTV circuits
- Collector-Emitter Sustaining Voltage V<sub>CEV</sub> = 400V (Min.)
- Low Saturation Voltage V<sub>CE(sat)</sub> = 1V (Max.) at I<sub>C</sub> = 5A
   Fast Switching Speed: t<sub>f</sub> = 0.75µs (Max.)

### **Maximum Ratings**

Characteristic	Symbol	BU406	Unit	
Collector-Emitter Voltage	V <sub>CEO</sub>	200		
Collector-Emitter Voltage	V <sub>CEV</sub>	- 400 V		
Collector-Base Voltage	V <sub>CBO</sub>			
Emitter-Base Voltage	V <sub>EBO</sub>			
Collector Current-Continuous -Peak	I <sub>C</sub>	7 10	А	
Base Current-Continuous	I <sub>B</sub>	4		
Total Power Dissipation at T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	60 0.48	W W/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C	

### **Thermal Characteristics**

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	$R_{\theta jc}$	2.08	°C/W



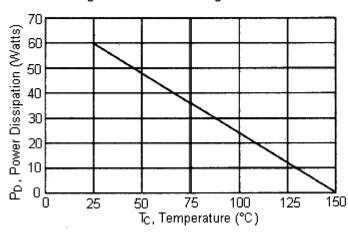


### Electrical Characteristics ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min.	Max.	Unit
OFF Characteristics	·			-
Collector-Emitter Sustaining Voltage (1) $I_C = 100 \text{mA}, I_B = 0$	V <sub>CEO(sus)</sub>	200	-	V
Collector Cut off Current V <sub>CE</sub> = 400V, V <sub>BE</sub> = 0	I <sub>CES</sub>	-	5	mA
Emitter Cut off Current V <sub>EB</sub> = 6V, I <sub>C</sub> = 0	I <sub>EBO</sub>	-	1	μA
ON Characteristics (1)				,
DC Current Gain $I_C = 2A, V_{CE} = 5V$	h <sub>FE</sub>	30 (Typ.)	-	-
Collector-Emitter Saturation Voltage $I_C = 5A$ , $I_B = 0.5A$	V <sub>CE(sat)</sub>	-	1	V
Base-Emitter Saturation Voltage $I_C = 5A$ , $I_B = 0.5A$	V <sub>BE(sat)</sub>	-	1.2	
Dynamic Characteristics	•			
Current Gain-Bandwidth Product $I_C = 0.5A, V_{CE} = 10V, f = 1MHz$	f <sub>T</sub>	10	-	MHz
Output Capacitance $V_{CE} = 10V$ , $I_{E} = 0$ , $f = 1MHz$	C <sub>ob</sub>	80 (Typ.)	-	pF
Switching Characteristics				
Fall Time $V_{CC} = 40V$ , $I_{C} = 5A$ , $I_{B1} = -I_{B2} = 0.6A$ , $L = 150\mu H$	t <sub>f</sub>	-	0.75	μs

<sup>(1)</sup> Pulse Test: Pulse Width = 300µs, Duty Cycle ≤2%

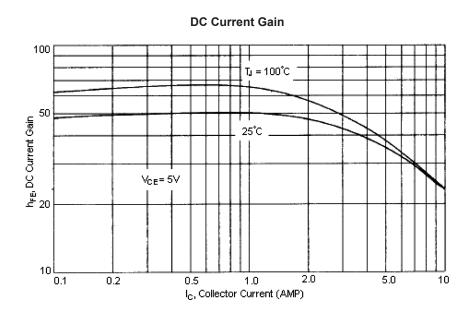
Figure - 1 Power Derating



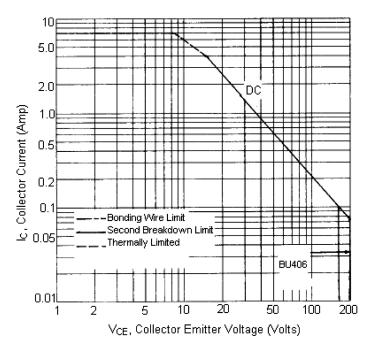
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### Active-Region Safe Operating Area (SOA)

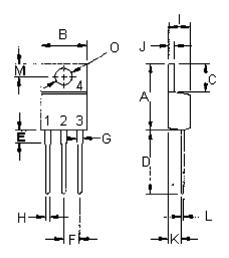


There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate  $I_{C^{-}}V_{CE}$  limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of SOA curve is based on  $T_{J(PK)}$  = 150°C;  $T_C$  is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(PK)} \leq$ 150°C. At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.







#### Pin Configuration:

- 1. Base
- 2. Collector
- 3. Emitter
- 4. Collector(Case)

Dimensions	Min.	Max.
Α	14.68	15.31
В	9.78	10.42
С	5.01	6.52
D	13.06	14.62
E	3.57	4.07
F	2.42	3.66
G	1.12	1.36
Н	0.72	0.96
1	4.22	4.98
J	1.14	1.38
K	2.2	2.97
L	0.33	0.55
M	2.48	2.98
0	3.7	3.9

Dimensions: Millimetres

### **Part Number Table**

Description	Part Number	
Transistor, NPN, TO-220	BU406	

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