### Transistor, PNP TO-3

# multicomp



#### **Description:**

Complementary silicon power transistors are designed for general-purpose switching and amplifier applications.

#### Features:

- DC current gain h<sub>FE</sub> = 20 70 at I<sub>C</sub> = 4A DC
- Collector-emitter saturation voltage-V<sub>CE</sub> (sat) = 1.1V DC (max.) at I<sub>C</sub> = 4A DC
- Excellent safe operating area
- Pb-free packages

#### **Maximum Ratings**

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V <sub>CEO</sub>	60	60 70 100 7	
Collector-Emitter Voltage	V <sub>CER</sub>	70		
Collector-Base Voltage	V <sub>CB</sub>	100		
Emitter-Base Voltage	V <sub>EB</sub>	7		
Collector Current-Continuous	Ι <sub>C</sub>	15	A DC	
Base Current	I <sub>B</sub>	7		
Total Power Dissipation at T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	115 0.657	W W/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>Stg</sub>	-65 to +200	С°	

#### **Thermal Characteristics**

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	R <sub>ejc</sub>	1.52	°C/W

Max. ratings are those values beyond which device damage can occur. Max. ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

www.element14.com www.farnell.com www.newark.com

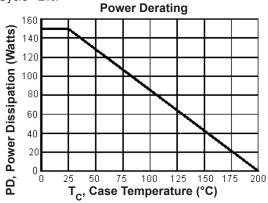




Electrical Characteristics (T <sub>c</sub> = 25°C unless otherwise noted)				
Characteristic	Symbol	Min.	Max.	Unit
Off Characteristics*	·			·
Collector-Emitter Sustaining Voltage (Note 1) ( $I_C = 200$ mA DC, $I_B = 0$ )	V <sub>EO (sus)</sub>	60	-	V DC
Collector-Emitter Sustaining Voltage (Note 1) ( $I_C = 200$ mA DC, $R_{BE} = 100\Omega$ )	V <sub>CER (sus)</sub>	70	-	
Collector Cut off Current ( $V_{CE} = 30V DC$ , $I_B = 0$ )	I <sub>CEO</sub>	-	0.7	
Collector Cut off Current ( $V_{CE}$ = 100V DC $V_{BE (off)}$ = 1.5V DC) ( $V_{CE}$ = 100V DC $V_{BE (off)}$ = 1.5V DC, $T_C$ = 150°C)	I <sub>CEX</sub>	-	1 5	mA DC
Emitter Cut off Current ( $V_{BE}$ = 7V DC, $I_C$ = 0)	I <sub>EBO</sub>	-	5	
On Characteristic* (Note 1)				
DC Current Gain ( $I_C = 4A DC$ , $V_{CE} = 4mA DC$ ) ( $I_C = 10A DC$ , $V_{CE} = 4V DC$ )	h <sub>FE</sub>	20 5	70 -	-
Collector-Emitter Saturation Voltage ( $I_C = 4A DC$ , $I_B = 400A DC$ ) ( $I_C = 10A DC$ , $I_B = 3.3A DC$ )	V <sub>CE (sat)</sub>	-	1.1 3	V DC
Base-Emitter On Voltage ( $I_C$ = 4A DC, $V_{CE}$ = 4V DC)	V <sub>BE (on)</sub>	-	1.5	
Second Breakdown	·			·
Second Breakdown Collector Current with Base Forward Biased $(V_{CE} = 40V DC, t = 1s, Non Repetitive)$	I <sub>S/b</sub>	2.87	-	A DC
Dynamic Characteristics				
Current-Gain - Bandwidth Product ( $I_C = 0.5A DC$ , $V_{CE} = 10V DC$ , f = 1MHz)	f <sub>T</sub>	2.5	-	MHz
*Small-Signal Current Gain (I <sub>C</sub> = 1A DC, V <sub>CE</sub> = 4V DC, f = 1kHz)	h <sub>fe</sub>	15	120	pF
*Small-Signal Current Gain Cut off Frequency $(V_{CE} = 4V DC, I_C = 1A DC, f = 1kHz)$	f <sub>hfe</sub>	10	-	kHz

\*Indicates Within JEDEC Registration. (MJ2955).

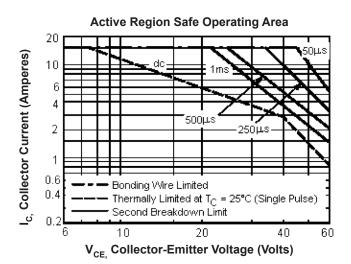
1. Pulse Test : Pulse Width = 300µs, Duty Cycle ≤2%.



www.element14.com www.farnell.com www.newark.com

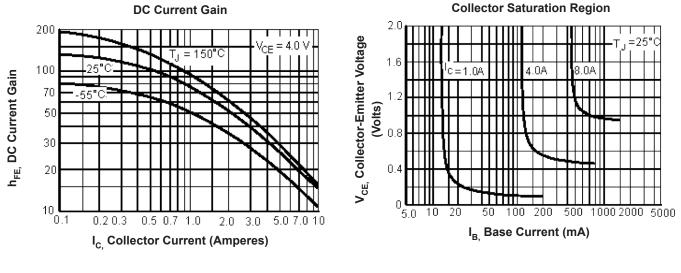




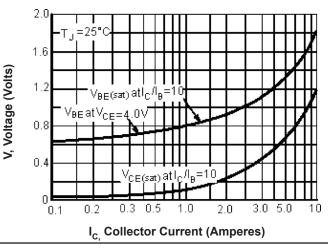


There are two limitation on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data is based on  $T_C = 25^{\circ}C$ ;  $T_{J(pk)}$  is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated for temperature according.



"On" Voltage



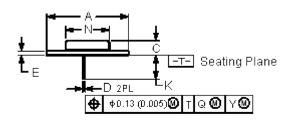
www.element14.com www.farnell.com www.newark.com

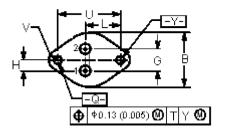


18/03/13 V1.0

## multicomp

### Dimensions





#### Pin Configuration:

Pin 1. Base

2. Emitter

Collector (Case)

Dimensions	Min.	Max.	
A	1.55 (39.37) Reference		
В	-	1.05 (26.67)	
С	0.25 (6.35)	0.335 (8.51)	
D	0.038 (0.97)	0.043 (1.09)	
E	0.055 (1.4)	0.07 (1.77)	
G	0.43 (10.92) BSC		
н	0.215 (5.46) BSC		
К	0.44 (11.18)	0.48 (12.19)	
L	0.665 (16.89) BSC		
N	-	0.83 (21.08)	
Q	0.151 (3.84)	0.165 (4.19)	
U	1.187 (30.15) BSC		
V	0.131 (3.33)	0.188 (4.77)	

Dimensions : Inches (Millimetres)

#### Part Number Table

Description	Part Number	
Transistor, PNP, TO-3	MJ2955	

Important Notice : This data sheet and its contents (the "Information") belong to the members of the Premier Farnell group of companies (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheet should check for themselves the Information and the suitability of the products for their purpose and not make any assumptions based on information included or omitted. Liability for loss or damage resulting from any reliance on the Information or use of it (including liability resulting from negligence or where the Group was aware of the possibility of such loss or damage arising) is excluded. This will not operate to limit or restrict the Group's liability for death or personal injury resulting from its negligence. Multicomp is the registered trademark of the Group. © Premier Farnell plc 2012.

www.element14.com www.farnell.com www.newark.com

