



Description:

Silicon power transistors.

The MJ15023 power transistors designed for high power audio, disk head positioners and other linear applications.

Features:

- High safe operating area (100% tested) 2A at 80V
- High DC current gain = h_{FE} = 15 (min.) at I_C = 8A DC
- · Pb-free packages

Maximum Ratings

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V _{CEO}	200	200 350 5 400	
Collector-Base Voltage	V _{CBO}	350		
Emitter-Base Voltage	V _{EBO}	5		
Collector-Emitter Voltage	I _{CEX}	400		
Collector Current - Continuous - Peak (Note 1)	I _C	16 30 A DC		
Base Current-Continuous	I _B	5		
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	250 1.43	W W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-65 to +200	°C	

Thermal Characteristics

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	$R_{_{\theta JC}}$	0.7	°C/W

Max. ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Pulse Test: Pulse Width = 5ms, Duty Cycle ≤10%





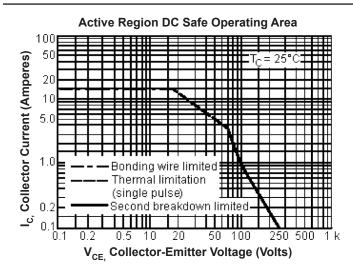
Electrical Characteristics (T_c = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Off Characteristics				
Collector-Emitter Sustaining Voltage (Note 2) $(I_C = 100 \text{mA DC}, I_B = 0)$	V _{CEO (sus)}	200	-	-
Collector Cut off Current (V _{CE} = 200V DC, V _{BE (off)} = 1.5V DC)	I _{CEX}	-	250	
Collector Cut off Current $(V_{CE} = 150V DC, I_B = 0)$	I _{CEO}	-	μA DC 500	μA DC
Emitter Cut off Current $(V_{CE} = 5V DC, I_B = 0)$	I _{EBO}	-		
Second Breakdown				
Second Breakdown Collector Current with Base Forward Biased (V_{CE} = 50V DC, t = 0.5s (Non-repetitive) (V_{CE} = 80V DC, t = 0.5s (Non-repetitive)	I _{S/b}	5 2	-	A DC
On Characteristic				
DC Current Gain (I_C = 8A DC, V_{CE} = 4V DC) (I_C = 16A DC, V_{CE} = 4V DC)	h _{FE}	15 5	60 -	-
Collector-Emitter Saturation Voltage ($I_C = 8A DC$, $I_B = 0.8A DC$) ($I_C = 16A DC$, $I_B = 3.2A DC$)	V _{CE (sat)}	-	1.4 4	V DC
Base-Emitter On Voltage $(I_C = 8A DC, V_{CE} = 4V DC)$	V _{BE (on)}	-	2.2	
Dynamic Characteristics				
Current-Gain Bandwidth Product ($I_C = 1A DC$, $V_{CE} = 10V DC$, $f_{test} = 1MHz$)	f _T	4	-	MHz
Output Capacitance $(V_{CB} = 10V DC, I_{E} = 0, f_{test} = 1MHz)$	C _{OB}	-	600	pF

^{1.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2%

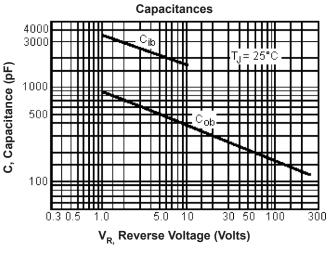


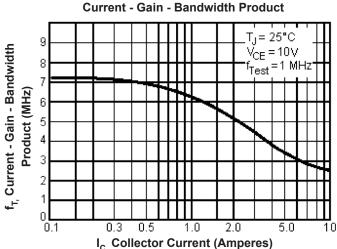


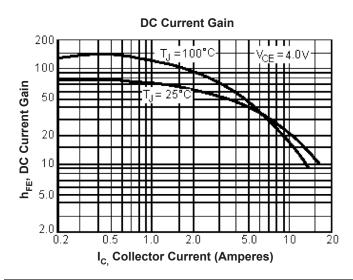


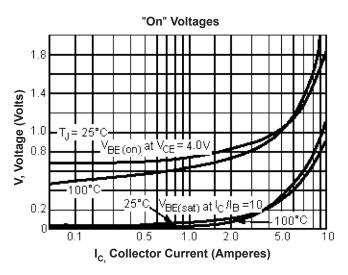
There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $\rm I_C$ - $\rm V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data is based on $T_{J(pk)}$ = 200°C; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.







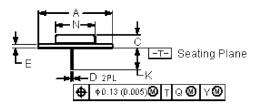


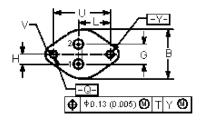
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Dimensions





Pin Configuration

Pin 1. Base
2. Emitter
Collector (Case)

Dimensions	Min.	Max.	
А	1.55 (39.37) Reference		
В	-	1.05 (26.67)	
С	0.25 (6.35)	0.335 (8.51)	
D	0.038 (0.97)	0.043 (1.09)	
E	0.055 (1.4)	0.07 (1.77)	
G	0.43 (10.92) BSC		
Н	0.215 (5.46) BSC		
K	0.44 (11.18)	0.48 (12.19)	
L	0.665 (16.89) BSC		
N	-	0.83 (21.08)	
Q	0.151 (3.84)	0.165 (4.19)	
U	1.187 (30.15) BSC		
V	0.131 (3.33)	0.188 (4.77)	

Dimensions: Inches (Millimetres)

Part Number Table

Description	Part Number	
Transistor, PNP, TO-3	MJ15023	

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