

RoHS Compliant



### **Description:**

WTC middle and high voltage series MLCC is designed by a special internal electrode pattern, which can reduce voltage concentrations by distributing voltage gradients throughout the entire capacitor. This special design also affords increased capacitance values in a given case size and voltage rating. WTC capacitor arrays are developed to offer designers the opportunity to lower placement costs increase assembly line output through lower component count per board.

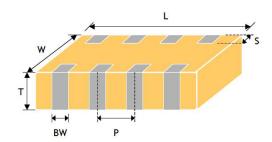
### Features:

- · High density mounting due to mounting space saving.
- Mounting cost saving.
- · Increased throughput.

### **Applications:**

- · For use as a bypass for digital and analog signal line noise
- · Computer motherboards and peripherals.
- · The other common electronic circuits.

#### **External Dimensions:**



The outline of MLCC

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol		S (mm)	BW (mm)	P (mm)
0508 (1220)	2 ±0.15	1.25 ±0.15	0.85 ±0.1	Т	0.2 ±0.1	0.25 ±0.1	0.5 ±0.1
0612 (1632)	3.2 ±0.15	1.6 ±0.15	0.8 ±0.1	В	0.3 ±0.2	0.4 ±0.15	0.8 ±0.15

Reflow soldering process only.





### **General Electrical Data:**

Dielectric	NI	P0	X7	'R	
Size	4x0402	4x0603	4x0402	4x0603	
Capacitance*	10pF to 270pF	10pF to 470pF	1,000pF to 100nF	180pF to 100nF	
Capacitance tolerance**	J (±5%),	K (±10%)	K (±10%),	M (±20%)	
Rated voltage (WVDC)	50V	50V 25, 50V		16V, 25V, 50V	
Q/Tan δ*		Q≥400+20C =: Q≥1000	Ur=50V, ≤2.5% Ur=25V&16V, ≤3.5% Ur=10V, ≤5.0%		
Insulation resistance at Ur	≥1	0G	≥10G or RxC≥500x	F whichever is less	
Operating temperature		-55°C to	+125°C		
Capacitance characteristic	±30	ppm			
Termination	Ni/Sn (lead-free termination)				

<sup>\*</sup> Measured at 30~70% related humidity.

NP0: Apply 1.0±0.2Vrms, 1.0MHz±10% at the conditions of 25°C ambient temperature.

### **Packaging Dimension And Quantity:**

Size	Thickness/Symbol		Paper Tape		
Size	(mm)		7" reel	13" reel	
4 x 0402	0.85 ±0.1	Т	4k	-	
4 x 0603	0.8 ±0.1	В	4k	-	

Unit: pieces

### Capacitance Range:

Size 4 x 0402			2		4 x 0603						
	Dielectric	NP0		X	7R		NP0			X7R	
R	ated Voltage (VDC)	50	10	16	25	50	25	50	16	25	50
	10pF (100)	Т					В	В			
	15pF (150)	Т					В	В			
"	22pF (220)	Т					В	В			
Capacitance	33pF (330)	Т					В	В			
acit	47pF (470)	Т					В	В			
Sap	68pF (680)	Т					В	В			
~	100pF (101)	Т					В	В			
	150pF (151)	Т	·				В	В			
	180pF (181)	Т					В	В		В	В

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X7R: Apply 1.0±0.2Vrms, 1.0kHz±10%, at the conditions of 25°C ambient temperature.

<sup>\*\*</sup> Preconditioning for Class II MLCC: Perform a heat treatment at 150±10°C for 1 hour, then leave in ambient condition for 24±2 hours before measurement.



### **Capacitance Range:**

	Size			4 x 0402	2		4 x 0603				
	Dielectric		X7R				NPO >			X7R	
R	ated Voltage (VDC)	50	10	16	25	50	25	50	16	25	50
	220pF (221)	Т					В	В		В	В
	270pF (271)	Т					В	В		В	В
	330pF (331)						В	В		В	В
	470pF (471)						В	В		В	В
	6,80pF (681)									В	В
	1,000pF (102)		T	Т	Т	Т				В	В
	1,500pF (152)		T	Т	T	Т				В	В
e e	2,200pF (222)		Т	Т	Т	Т				В	В
itan	3,300pF (332)		T	Т	Т	Т				В	В
Capacitance	4,700pF (472)		T	Т	T	Т				В	В
ပြီ	6,800pF (682)		T	Т	T	Т				В	В
	0.010µF (103)		T	Т	Т	Т				В	В
	0.015µF (153)		T	Т	Т				В	В	В
	0.022µF (223)		T	Т	Т				В	В	В
	0.033µF (333)		Т	Т	Т				В		
	0.047µF (473)		Т	Т	Т				В		
	0.068µF (683)		T	Т	Т				В		
	0.10µF (104)		Т	Т	Т				В		

<sup>1.</sup> The letter in cell is expressed the symbol of product thickness.

## **Reliability Test Conditions and Requirements:**

No	Item	Test Condition	Requirements
1	Visual and Mechanical	-	* No remarkable defect.  * Dimensions to conform to individual specification sheet.
2	Capacitance	Class I: NP0	* Shall not exceed the limits given in the detailed spec.
3	Q/ D.F. (Dissipation Factor)	1 ±0.2Vrms, 1MHz ±10% Class II: (X7R) 1 ±0.2Vrms, 1kHz ±10%	NP0: Cap≥30pF, Q≥1000; Cap<30pF, Q≥400+20C X7R: Ur=50V, ≤2.5%; Ur=25V&16V, ≤3.5%; Ur=10V, ≤5.0%
4	Dielectric Strength	* To apply voltage (≤100V) 250%.  * Duration: 1 to 5 sec.  * Charge and discharge current less than 50mA.	* No evidence of damage or flash over during test.
5	Insulation Resistance	To apply rated voltage for max. 120 sec.	≥10GΩ or RxC≥500-F whichever is smaller.







### **Reliability Test Conditions and Requirements:**

No	Item		Test Conditio	n			Requirements	
		With no e	electrical load.					
	Tomporatura	T.C.	•			T.C.	Capacitance Change	
6	Temperature Coefficient	I NEO	Temperatu			NP0	Within ±30ppm/°C	
		NP0				X7R	Within ±15%	
		X7R		25 C				
7	Adhesive Strength of Termination	5N (≤06	zing force: 03) and 10N (>0603 :: 10 ±1 sec.	)		No remarkable of terminations.	damage or removal of the	
8	Vibration Resistance	Total amp Test time	frequency: 10~55 H plitude: 1.5mm :: 6 hrs. (Two hrs eac perpendicular direct	ch in three	e	No remarkable o Cap change and	damage. d Q/D.F.: To meet initial spec.	
9	Solderability		emperature: 235 ±5°0 ime: 2 ±0.5 sec.	C		95% min. covera	age of all metalized area.	
10	Bending Test	The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1mm per second until the deflection becomes 1mm and then the pressure shall be maintained for 5 ±1 sec. Measurement to be made after keeping at room temp. for 24 ±2 hrs.			il the sec.	No remarkable damage. Cap change NP0: within ±5.0% or ±0.5pF whichever is larger. X7R: within ±12.5% (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test.)		
11	Resistance to Soldering Heat	Solder temperature: 260 ±5°C Dipping time: 10 ±1 sec Preheating: 120 to 150°C for 1 minute before immerse the capacitor in a eutectic solder. Before initial measurement (Class II only): Perform 150+0/-10°C for 1 hr and then set for 48 ±4hrs at room temp. Measurement to be made after keeping at room temp. for 24 ±2 hrs. (Class I)				larger. X7R: within ±7.5 Q/D.F., I.R. and requirements.	5% or ±0.25pF whichever is	
		Conduct	hrs. (Class II). the five cycles accor tures and time.	ding to th	ne			
		Step	Temp. (°C)	Time (	(Min.)			
		1 Min. operating temp +0/-3		30±	±3	No remarkable o	damage.	
	_	2	Room temp.	2~3		Cap change: NP0: within +2.5	5% or ±0.25pF whichever is	
12	Temperature Cycle	3	Max. operating temp +3/-0	. 30=	±3	larger. X7R: within ±7.5	5%	
		4 Room temp. 2~3		<i>-</i> 3		dielectric strength: To meet initial		
		Before initial measurement (Class II only): Perform 150+0/-10°C for 1hour and then set for 24±2 hrs at room temp. Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).			en set ent o. for	requirements.		

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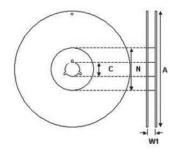


### **Reliability Test Conditions and Requirements:**

No	Item	Test Condition	Requirements
13	Humidity (Damp Heat) Steady State	Test temp.: 40±2°C Humidity: 90~95% RH Test time: 500+24/-0hrs. Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).	No remarkable damage. Cap change: NP0: within $\pm 5.0\%$ or $\pm 0.5$ pF whichever is larger. X7R: within $\pm 12.5\%$ Q/D.F. value: NP0: Cap $\geq 30$ pF, Q $\geq 350$ ; $10$ pF $\leq$ Cap $\leq 30$ pF, Q $\geq 275+2.5$ C Cap $\leq 10$ pF; Q $\geq 200+10$ C X7R: Ur= $50$ V, $\leq 3\%$ ; Ur= $25$ V& $16$ V, $\leq 5\%$ ; Ur= $10$ V, $\leq 7.5\%$ I.R.: $\geq 1$ G $\Omega$ or RxC $\geq 50$ $\Omega$ -F whichever is smaller.
14	Humidity (Damp Heat) Load	Test temp.: 40±2°C Humidity: 90~95%RH Test time: 500+24/-0 hrs. To apply voltage: rated voltage. Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).	No remarkable damage. Cap change: NP0: within ±7.5% or ±0.75pF whichever is larger. X7R: within ±12.5% Q/D.F. value: NP0: Cap≥30pF, Q≥200; Cap<30pF, Q≥100+10/3C X7R: Ur=50V, ≤3%; Ur=25V&16V, ≤5%; Ur=10V, ≤7.5% I.R.: ≥500MΩor RxC≥25Ω-F whichever is smaller.
15	High Temperature Load (Endurance)	Test temp.: NP0, X7R: 125±3°C To apply voltage: 200% of rated voltage. Test time: 1000+24/-0 hrs. Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).	No remarkable damage. Cap change: NP0: within $\pm 3.0\%$ or $\pm 0.3$ pF whichever is larger. X7R: within $\pm 12.5\%$ Q/D.F. value: NP0: Cap $\geq 30$ pF, Q $\geq 350$ 10pF $\leq$ Cap $\leq 30$ pF, Q $\geq 275+2.5$ C Cap $\leq 10$ pF, Q $\geq 200+10$ C X7R: Ur= $\leq 10$ 0, $\leq 3\%$ ; Ur= $\leq 10$ 0, $\leq 10$ 0 or RxC $\leq 10$ 0. Whichever is smaller.

### **Appendixes**

#### **Reel Dimensions**



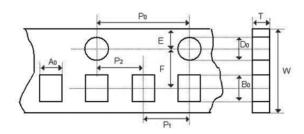
Size	4x0402, 4x0603
Reel size	7"
С	13 +0.5/-0.2
<b>W</b> 1	8.4 +1.5/-0
Α	178 ±0.1
N	60 +1/-0

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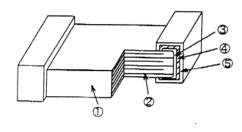
#### **Tape Dimensions**



The dimension of paper tape

Size	4x0402	4x0603
Thickness	Т	В
<b>A</b> 0	1.5 ±0.1	2 ±0.1
Bo	2.3 ±0.1	3.5 ±0.1
Т	0.95 ±0.05	0.95 ±0.05
<b>K</b> <sub>0</sub>	-	-
W	8 ±0.1	8 ±0.1
Po	4 ±0.1	4 ±0.1
10xPo	40 ±0.1	40 ±0.1
<b>P</b> 1	4 ±0.1	4 ±0.1
P <sub>2</sub>	2 ±0.05	2 ±0.05
Do	1.55 ±0.05	1.5 ±0.05
<b>D</b> 1	-	-
E	1.75 ±0.05	1.75 ±0.1
F	3.5 ±0.05	3.5 ±0.05

### **Constructions:**



No.	Naı	me	NP0 & X7R,	
1	Ceramic	material	BaTiO <sub>3</sub> based	
2	Inner electrode		Ni	
3		Inner layer	Cu	
4	Termination	Middle layer	Ni	
5		Outer layer	Sn (Matt)	

#### Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70%. related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

#### Cautions:

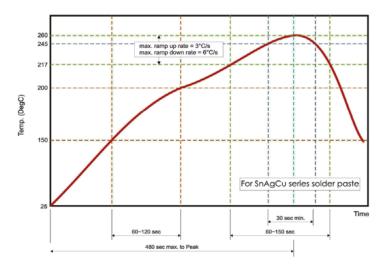
- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.



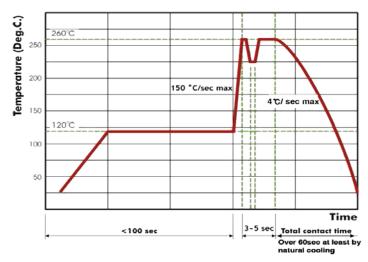


### **Recommended Soldering Conditions:**

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of  $N_2$  within oven are recommended.



Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.



Recommended wave soldering profile for SMT process with SnAgCu series solder.





#### **Part Number Table**

Description	Partnumber
Capacitor, RF, 0.1UF, 10V, X7R, 4x0402	MC000249
Capacitor, RF, 0.01UF, 50V, X7R, 4x0603	MC000250
Capacitor, RF, 0.01UF, 50V, X7R, 4x0603	MC000251
Capacitor, RF, 0.01UF, 50V, X7R, 4x0603	MC000252
Capacitor, RF, 100PF, 50V, NP0, 4x0603	MC000253
Capacitor, RF, 150PF, 50V, NP0, 4x0603	MC000254
Capacitor, RF, 180PF, 50V, NP0, 4x0603	MC000255
Capacitor, RF, 220PF, 50V, NP0, 4x0603	MC000256
Capacitor, RF, 33PF, 50V, NP0, 4x0603	MC000257
Capacitor, RF, 330PF, 50V, NP0, 4x0603	MC000258
Capacitor, RF, 100PF, 50V, NP0, 4x0603	MC000259
Capacitor, RF, 220PF, 50V, NP0, 4x0603	MC000260
Capacitor, RF, 47PF, 50V, NP0, 4x0603	MC000261
Capacitor, RF, 470PF, 50V, NP0, 4x0603	MC000262
Capacitor, RF, 470PF, 50V, NP0, 4x0603	MC000263
Capacitor, RF, 0.01UF, 25V, X7R, 4x0603	MC000264
Capacitor, RF, 0.047UF, 16V, X7R, 4x0603	MC000265
Capacitor, RF, 1000PF, 50V, X7R, 4x0603	MC000266
Capacitor, RF, 1000PF, 50V, X7R, 4x0603	MC000267
Capacitor, RF, 0.022UF, 50V, X7R, 4x0603	MC000268

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