



PRODUCT BULLETIN
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ISSUE DATE: 19-Jun-2012
NOTIFICATION: 15222
TITLE: PIONEER 0 AND PIONEER 1 ERRATA UPDATE
EFFECTIVE DATE: 20-Jun-2012

DEVICE(S)

MPN
MK10DN128VFM5
MK10DN128VFT5
MK10DN128VLF5
MK10DN128VLH5
MK10DN128VMP5
MK10DN32VFM5
MK10DN32VFT5
MK10DN32VLF5
MK10DN32VLH5
MK10DN32VMP5
MK10DN64VFM5
MK10DN64VFT5
MK10DN64VLF5
MK10DN64VLH5
MK10DN64VMP5
MK10DX128VFM5
MK10DX128VFT5
MK10DX128VLF5
MK10DX128VLH5
MK10DX128VLH7
MK10DX128VLL7
MK10DX128VMB7
MK10DX128VMP5
MK10DX256VLH7
MK10DX256VLK7
MK10DX256VLK7R
MK10DX256VLL7
MK10DX256VMB7

MK10DX256VML7
MK10DX32VFM5
MK10DX32VFT5
MK10DX32VLF5
MK10DX32VLH5
MK10DX32VMP5
MK10DX64VFM5
MK10DX64VFT5
MK10DX64VLF5
MK10DX64VLH5
MK10DX64VLH7
MK10DX64VLK7
MK10DX64VLK7R
MK10DX64VMB7
MK10DX64VMP5
MK20DN128VFM5
MK20DN128VFT5
MK20DN128VLF5
MK20DN128VLH5
MK20DN128VMP5
MK20DN32VFM5
MK20DN32VFT5
MK20DN32VLF5
MK20DN32VLH5
MK20DN32VMP5
MK20DN64VFM5
MK20DN64VFT5
MK20DN64VLF5
MK20DN64VLH5
MK20DN64VMP5
MK20DX128VFM5
MK20DX128VFT5
MK20DX128VLF5
MK20DX128VLH5
MK20DX128VLH7
MK20DX128VLK7
MK20DX128VLL7
MK20DX128VMP5
MK20DX256VLH7

MK20DX256VLK7
MK20DX256VLL7
MK20DX256VMB7
MK20DX256VML7
MK20DX32VFM5
MK20DX32VFT5
MK20DX32VLF5
MK20DX32VLH5
MK20DX32VMP5
MK20DX64VFM5
MK20DX64VFT5
MK20DX64VLF5
MK20DX64VLH5
MK20DX64VLH7
MK20DX64VMB7
MK20DX64VMP5
MK30DX128VLH7
MK30DX128VMB7
MK30DX256VLH7
MK30DX256VLK7
MK30DX256VLL7
MK30DX256VMB7
MK30DX64VLH7
MK30DX64VMB7
MK40DX128VLH7
MK40DX128VLK7
MK40DX128VLL7
MK40DX128VMB7
MK40DX256VLH7
MK40DX256VLK7
MK40DX256VLL7
MK40DX256VML7
MK40DX64VLH7
MK40DX64VMB7
MK50DX128CLH7
MK50DX128CLL7
MK50DX256CLK7
MK50DX256CLL7
MK51DX128CLH7

MK51DX256CLK7
MK51DX256CLL7
MK51DX256CML7

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The Kinetis 50 MHz devices (1N86B mask set) errata documentation has been updated to include additional errata that have been identified. The updated version has been added to the freescale.com website at: http://cache.freescale.com/files/microcontrollers/doc/errata/KINETIS50MHZ_1N86B.pdf?fpsp=1&WT_TYPE=Errata&WT_VENDOR=FREESCALE&WT_FILE_FORMAT=pdf&WT_ASSET=Documentation

The Kinetis 72 MHz devices (1N36B mask set) errata documentation has been updated to include additional errata that have been identified. The updated version has been added to the freescale.com website at: http://cache.freescale.com/files/microcontrollers/doc/errata/KINETIS72MHZ_1N36B.pdf?fpsp=1&WT_TYPE=Errata&WT_VENDOR=FREESCALE&WT_FILE_FORMAT=pdf&WT_ASSET=Documentation

e4588 DMAMUX: When using PIT with "always enabled" request, DMA request does not deassert correctly

e4590 MCG: Transitioning from VLPS to VLPR low power modes while in BLPI clock mode is not supported.

e4949 Reset and Boot: Device may not exit the power on reset (POR) event correctly with fast ramp-up slew rates.

e4945 UART: ISO-7816 T=1 mode receive data format with a single stop bit is not supported

REASON FOR CHANGE

The errata documentation for the kinetis 50MHz and 72 MHz devices has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

There have been no changes made to the current production device. The errata describe existing conditions identified on current production devices. There are potential software and/or hardware implications to customers.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 08-Jun-2012

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15222_KINETIS50MHZ_1N86B_Rev._10_MAY_2012.pdf](#)

[15222_KINETIS72MHZ_1N36B_Rev._11_MAY_2012.pdf](#)
