



PRODUCT BULLETIN

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ISSUE DATE: 18-Mar-2013
NOTIFICATION: 15539
TITLE: i.MX 6Dual/6Quad Errata Update
EFFECTIVE DATE: 19-Mar-2013

DEVICE(S)

MPN
MCIMX6D4AVT08AC
MCIMX6D4AVT08ACR
MCIMX6D4AVT10AC
MCIMX6D4AVT10ACR
MCIMX6D5EYM10AC
MCIMX6D5EYM10CC
MCIMX6D5EYM12AC
MCIMX6D6AVT08AC
MCIMX6D6AVT08ACR
MCIMX6D6AVT10AC
MCIMX6D6AVT10ACR
MCIMX6D7CVT08AC
MCIMX6Q4AVT08AC
MCIMX6Q4AVT08ACR
MCIMX6Q4AVT10AC
MCIMX6Q4AVT10ACR
MCIMX6Q5EYM10AC
MCIMX6Q5EYM12AC
MCIMX6Q6AVT08AC
MCIMX6Q6AVT08ACR
MCIMX6Q6AVT10AC
MCIMX6Q6AVT10ACR
MCIMX6Q7CVT08AC

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The errata document has been updated for the i.MX 6Dual/6Quad product lines.

The following changes were made to the document:

- Added ERR006223 CCM: Failure to resume from Wait/Stop mode with power gating
- Added ERR006259 ARM: Debug/trace functions (PMU, PTM and ETB) are disabled with absence of JTAG_TCK clock after POR
- Added ERR006281 USB: Incorrect DP/DN state when only VBUS is applied
- Deleted ERR005384 (not relevant to this Freescale implementation of the ARM core)
- Updated ERR004367 SNVS: SNVS_LP resets to the power OFF state

The document is on the external web at:

http://cache.freescale.com/files/32bit/doc/errata/IMX6DQCE.pdf?fpsp=1&WT_TYPE=Errata&WT_VENDOR=FREESCALE&WT_FILE_FORMAT=pdf&WT_ASSET=Documentation

REASON FOR CHANGE

To add workarounds and information on possible projected impact of respective features

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes to form, fit, function or reliability

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 15-Feb-2013

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15539_IMX6DQCE_Rev1_1.pdf](#)
