PRODUCT BULLETIN
Generic Copy

ISSUE DATE:	06-Mar-2013
NOTIFICATION:	15536
TITLE:	i.MX 6SoloLite Errata Update
EFFECTIVE DATE:	07-Mar-2013

DEVICE(S)

MPN
MCIMX6L2DVN10AA
MCIMX6L2EVN10AA
MCIMX6L3DVN10AA
MCIMX6L3EVN10AA
MCIMX6L7DVN10AA
MCIMX6L7DVN10SA
MCIMX6L8DVN10AA
MCIMX6L8DVN10SA

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The errata document has been updated for the i.MX 6SoloLite product line.

The following changes were made to the document:
- Added ERR006223 CCM: Failure to resume from Wait/Stop mode with power gating
- Added ERR006259 ARM: Debug/trace functions (PMU, PTM and ETB) are disabled with absence of JTAG_TCK clock after POR
- Added ERR006281 USB: Incorrect DP/DN state when only VBUS is applied
- Added ERR006287 eLCDIF/EPDC/PXP/SPDC: Display domain modules register read may return value for the prior read access after resuming from power gating

The document is located on the external web at:

http://cache.freescale.com/files/32bit/doc/errata/IMX6SLCE.pdf?
fpsp=1&WT_TYPE=Errata&WT_VENDOR=FREESCALE&WT_FILE_FORMAT=pdf&WT_ASSET=Documentation

REASON FOR CHANGE

To add workarounds and information on possible projected impact of respective features

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes to form, fit, function or reliability

NOTE:
THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 15-Feb-2013

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

   N/A

RELIABILITY DATA SUMMARY:

   N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

   N/A

CHANGED PART IDENTIFICATION:

   N/A

ATTACHMENT(S):
External attachment(s) FOR this notification can be viewed AT:
[15536_IMX6SLCE_Rev1_1.pdf](#)