



CUSTOMER ALERT UPDATE

Generic Copy

This notice is Freescale Confidential Proprietary and is only intended for the customer listed on this notification.

ISSUE DATE: 27-Nov-2012
NOTIFICATION: 15444A
TITLE: I.MX27 ERRATA
EFFECTIVE DATE: 28-Nov-2012

DEVICE(S)

MPN
MCIMX27LMJP4A
MCIMX27LMOP4A
MCIMX27LMOP4AR2
MCIMX27LMOP4B
MCIMX27LVOP4A
MCIMX27LVOP4AR2
MCIMX27MJP4A
MCIMX27MJP4AR2
MCIMX27MOP4A
MCIMX27MOP4AR2
MCIMX27VJP4A
MCIMX27VOP4A
MCIMX27VOP4AR2

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DESCRIPTION

This Update notice is to clarify the affected date codes for the latest errata rev 0.6 for #13 and #14.

The i.MX27 errata documentation has been updated to include additional errata that have been identified.

The updated errata documentation is attached to this notice and can be found at:

www.freescale.com/imx27

File name: [MCIMX27CE_Rev0.6](#)

The errata updates are as follows:

1. Any read access from the Clock Controller Module SPCTL0 register will clear the contents of the register.
2. In 4 bpp, 8 bpp, 18 bpp, and TFT modes, LSCLK is missed in these conditions:
 - One clock cycle before every OE_ACD assert timing
 - Every VSYNC rising timing
 - Every VSYNC falling timing
3. While making an enhancement to the PLL circuit to improve performance, a change was incorporated that requires a software update. Software needs to initialize ARMSRC and ARMDIV bit fields in the CSCR register prior to PLL restart to prevent dead-lock.
4. MPLL reference clock source change following a warm reset.

AFFECTED DATE CODES

Workweek 1230 and newer for MCIMX27LMOP4A/R2, MCIMX27MJP4A, MCIMX27MOP4A/R2.
Workweek 1231 and newer for MCIMX27VOP4A/R2, MCIMX27VJP4A.
Workweek 1141 and newer for MCIMX27LVOP4A/R2.

SUMMARY OF EVALUATIONS/FINDINGS

Freescale has made a minor silicon change in the product which may cause unexpected customer system issues in some cases. A product alert is being issued for customer notification of errata update. The errata #13 and #14 were added and may require customer software update.

FREESCALE ACTIONS

Freescale has confirmed the errata workarounds.

CUSTOMER ACTIONS

Customer should confirm date codes in question and confirm their respective systems boot properly. If systems are operating normally, no action is needed.

If a customer has confirmed system boot issues with date codes in question, please follow the errata workaround for proper fix.

IMPORTANT NOTE: IT IS YOUR RESPONSIBILITY TO NOTIFY ANYONE WHO HAS PROCURED AFFECTED PRODUCT DIRECTLY OR INDIRECTLY FROM YOU.

To request further data or inquire about the notification, please enter a [Service Request](#).

For sample inquiries - please go to www.freescale.com

RELATED NOTIFICATION(S):

15444- i.MX27 Errata

TO VIEW the GENERIC copy, click on the notification number above.

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15444A_MCIMX27CE_Rev0_6.pdf](#)
