



PRODUCT BULLETIN

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ISSUE DATE: 12-Nov-2012
NOTIFICATION: 15422
TITLE: I.MX21 ERRATA UPDATE
EFFECTIVE DATE: 13-Nov-2012

DEVICE(S)

MPN
MC9328MX21CJM
MC9328MX21CJMR2
MC9328MX21CVK
MC9328MX21CVM
MC9328MX21CVMR2
MC9328MX21DVK
MC9328MX21DVM
MC9328MX21DVMR2
MC9328MX21SCVK
MC9328MX21SCVKR2
MC9328MX21SCVM
MC9328MX21SVK
MC9328MX21SVM
MC9328MX21VK
MC9328MX21VM
MC9328MX21VMR2
MC94MX21DVKN3
MC94MX21DVKN3R2

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The i.MX21 errata documentation has been updated to include additional errata that have been identified.

The documentation can be found at http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=i.MX21&nodeId=018rH3ZrDR66CA&fjsp=1&tab=Documentation_Tab

File names:

MC9328MX21SCE_Rev4

MC9328MX21SCE_Rev6

In 4 bpp, 8 bpp, 18 bpp, and TFT modes, LSCLK is missed in these conditions:

- One clock cycle before every OE_ACD assert timing
- Every VSYNC rising timing
- Every VSYNC falling timing

REASON FOR CHANGE

The errata documentation for i.MX21 has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes have been made to the current production device. The errata describe existing conditions identified on current production devices. There are potential hardware and/or software implications to customers.

NOTE:
THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 30-Oct-2012

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15422_MC9328MX21SCE_Rev4.pdf](#)

[15422_MC9328MX21CE_Rev6.pdf](#)
