

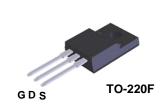
FQPF20N06L N-Channel QFET[®] MOSFET 60 V, 15.7 A, 52 mΩ

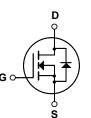
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 15.7 A, 60 V, $R_{DS(on)}$ =52 m Ω (Max.) @V_{GS}=10 V, I_D=7.85 A
- Low Gate Charge (Typ. 9.5 nC)
- Low Crss (Typ. 35 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_c = 25°C unless otherwise noted

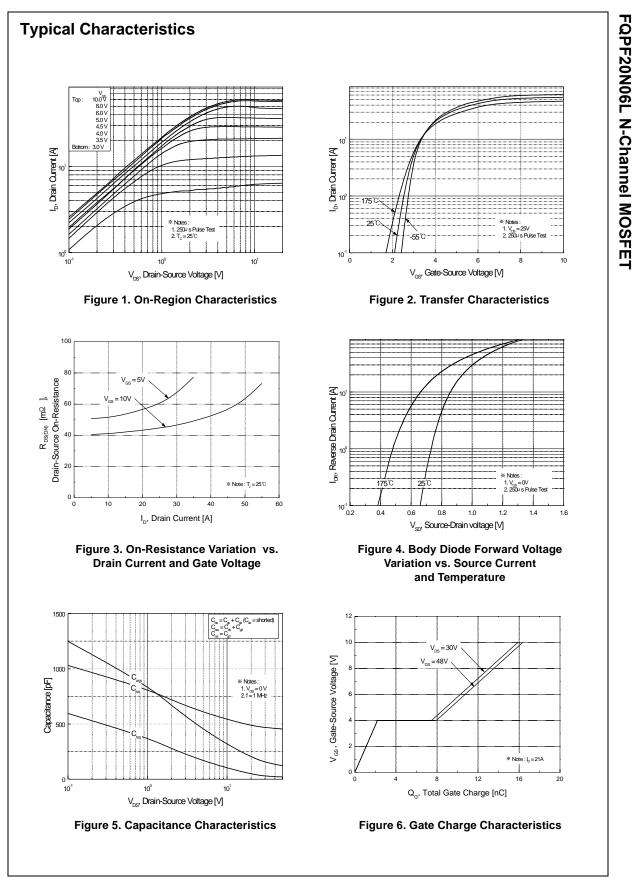
Symbol	Parameter		FQPF20N06L	Unit
V _{DSS}	Drain-Source Voltage		60	V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}$	C)	15.7	А
	- Continuous (T _C = 100)°C)	11.1	А
I _{DM}	Drain Current - Pulsed	(Note 1)	62.8	Α
V _{GSS}	Gate-Source Voltage		± 20	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	170	mJ
I _{AR}	Avalanche Current	(Note 1)	15.7	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	3.0	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns
PD	Power Dissipation $(T_C = 25^{\circ}C)$		30	W
	- Derate above 25°C		0.2	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
Τ _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

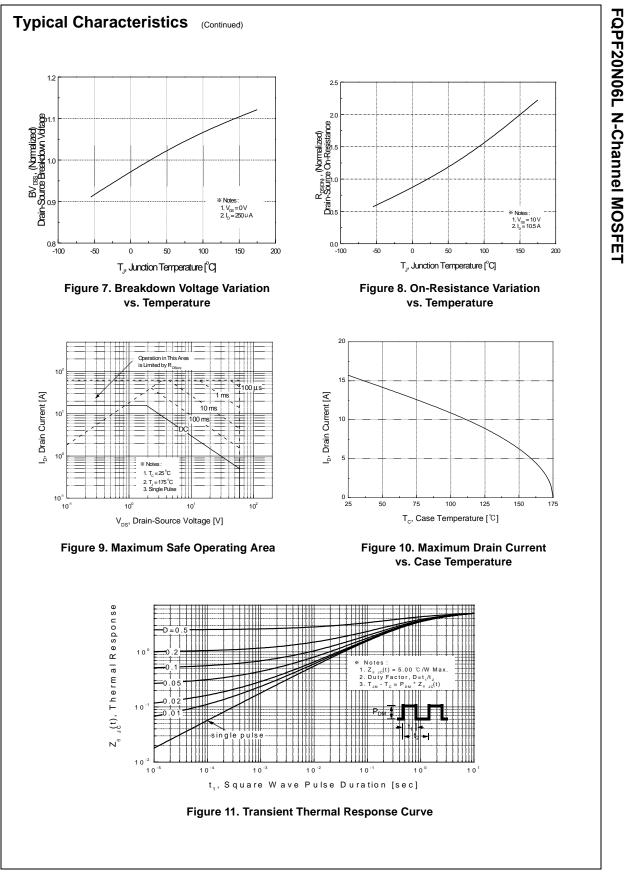
Symbol	Parameter	Тур	Max	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case		5.00	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient		62.5	°C/W

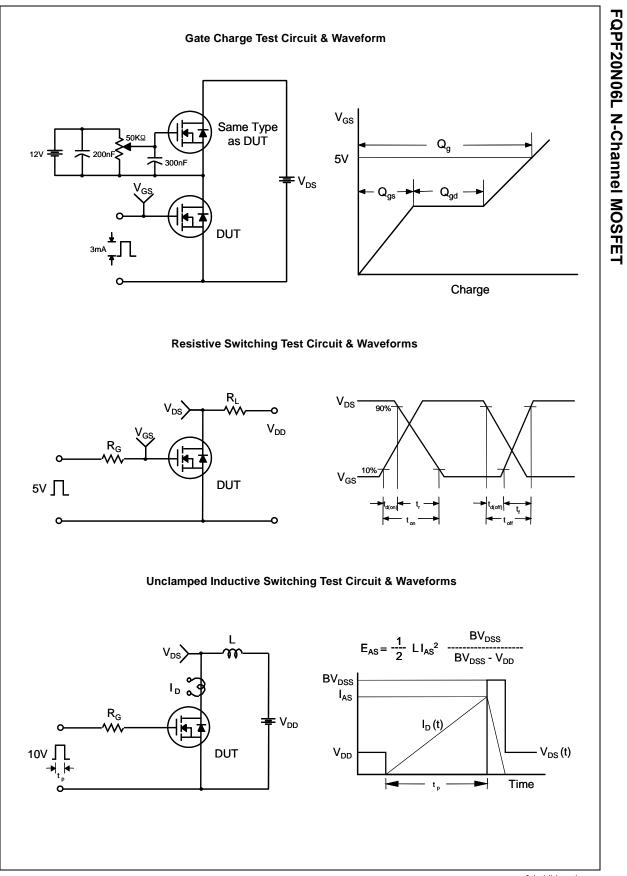
March 2013

	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	60			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu$ A, Referenced to 25°C		0.06		V/°C
I _{DSS}	$V_{DS} = 60 \text{ V}, V_{CS} = 0 \text{ V}$				1	μA
	Zero Gate Voltage Drain Current	V _{DS} = 48 V, T _C = 150°C			10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		2.5	V
R _{DS(on)}	Static Drain-Source	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 7.85 \text{ A}$		0.042	0.055	~
20(01)	On-Resistance	V _{GS} =5V, I _D =7.85A		0.055	0.07	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 25 \text{ V}, I_D = 7.85 \text{ A}$ (Note 4)		9		S
D						
C _{iss}	ic Characteristics Input Capacitance			480	630	pF
C _{oss}	Output Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		175	230	pF
C _{rss}	Reverse Transfer Capacitance	1 = 1.0 10112		35	45	pF
	ng Characteristics					-
t _{d(on)}	Turn-On Delay Time	V _{DD} = 30 V, I _D = 10.5 A,		10	30	ns
t _r	Turn-On Rise Time	R _G = 25 Ω		165	340	ns
t _{d(off)}	Turn-Off Delay Time	-		35	80	ns
t _f	Turn-Off Fall Time	(Note 4, 5)		70	150	ns
Q _g	Total Gate Charge	V _{DS} = 48 V, I _D = 21 A,		9.5	13	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = 5 V$		2.5		nC
Q _{gd}	Gate-Drain Charge	(Note 4, 5)		5.5		nC
Drain-S	ource Diode Characteristics ar	nd Maximum Ratings				
I _S	Maximum Continuous Drain-Source Dic	ode Forward Current			15.7	Α
I _{SM}	Maximum Pulsed Drain-Source Diode F				62.8	Α
V _{SD}	Drain-Source Diode Forward Voltage				1.5	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 V, I_{S} = 21 A,$		54		ns
Q _{rr}	Reverse Recovery Charge	$dI_{F} / dt = 100 \text{ A}/\mu \text{s}$ (Note 4)		75		nC



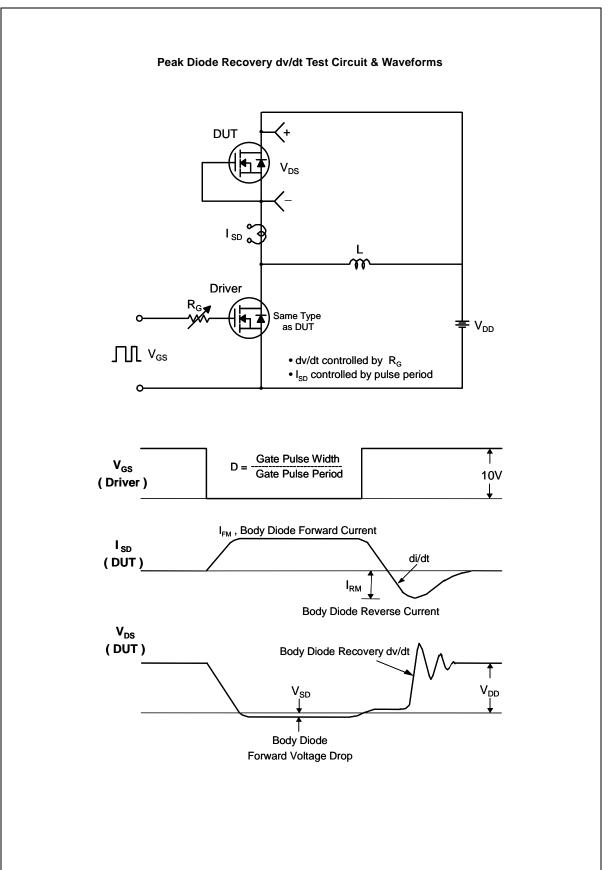
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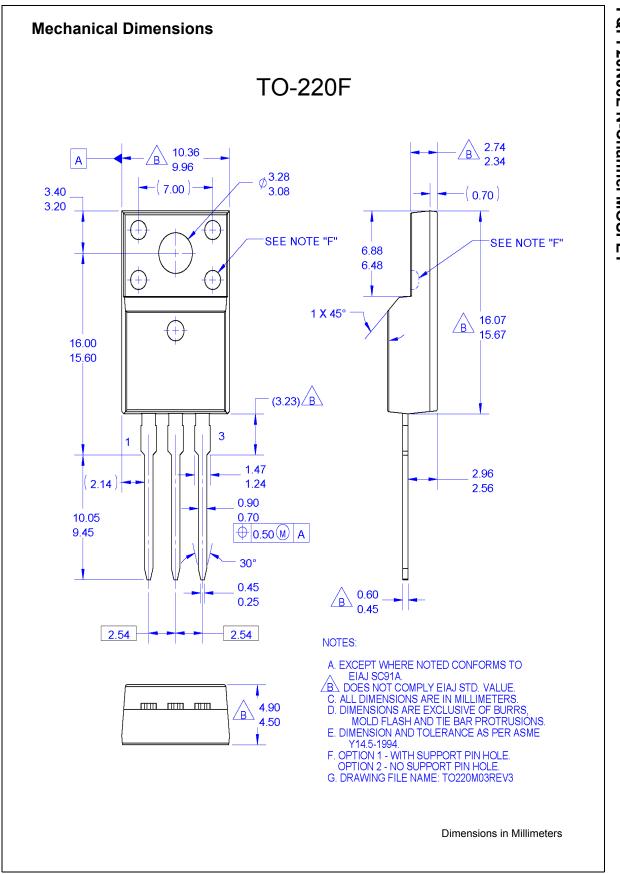




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FQPF20N06L N-Channel MOSFET



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