

FDP39N20 / FDPF39N20 N-Channel UniFETTM MOSFET 200 V, 39 A, 66 mΩ

Features

- $R_{DS(on)}$ = 66 m Ω (Max.) @ V_{GS} = 10 V, I_D = 19.5 A
- Low Gate Charge (Typ.38 nC)
- Low C_{rss} (Typ. 57 pF)
- 100% Avalanche Tested

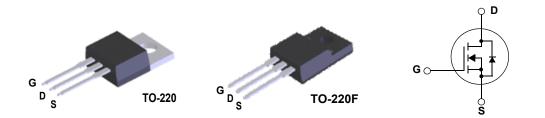
Applications

- PDP TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor[®], s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

March 2013



Absolute Maximum Ratings

Symbol	Parameter		FDP39N20 FDPF39N20		Unit	
V _{DSS}	Drain-Source Voltage		200		V	
ID	Drain Current	- Continuous (T _C = 25°C) - Continuous (T _C = 100°C)		39 39 * 23.4 23.4 *		A A
I _{DM}	Drain Current	- Pulsed	(Note 1)	156	156 *	А
V _{GSS}	Gate-Source voltage		±30		V	
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	860		mJ
I _{AR}	Avalanche Current		(Note 1)	39		А
E _{AR}	Repetitive Avalanche Energy		(Note 1)	25.1		mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.5		V/ns
P _D	Power Dissipation	(T _C = 25°C) - Derate above 25°C		251 2.0	37 0.29	W W/°C
T _{J,} T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C	
Τ _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		,	300		°C

* Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FDP39N20	FDPF39N20	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.5	3.4	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5	-	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP39N20	FDP39N20	TO-220	-	-	50
FDPF39N20	FDPF39N20	TO-220F	-	-	50

Electrical Characteristics T_c = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max	Unit
Off Charac	teristics					
BV _{DSS}	Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = 250 \mu A$		200			V
ΔΒV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$, Referenced to 25°C		0.2		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 200V, V_{GS} = 0V$ $V_{DS} = 160V, T_{C} = 125^{\circ}C$			1 10	μΑ μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V			-100	nA
On Charac	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 19.5A		0.056	0.066	Ω
9 _{FS}	Forward Transconductance	vard Transconductance $V_{DS} = 40V, I_D = 19.5A$		28.5		S
Dynamic C	haracteristics					
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V,		1640	2130	pF
C _{oss}	Output Capacitance	f = 1.0MHz		400	520	pF
C _{rss}	Reverse Transfer Capacitance			57	85	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 100V, I _D = 39A		30	70	ns
t _r	Turn-On Rise Time	$R_G = 25\Omega$ (Note 4)		160	330	ns
t _{d(off)}	Turn-Off Delay Time			150	310	ns
t _f	Turn-Off Fall Time			150	310	ns
Qg	Total Gate Charge	V _{DS} = 160V, I _D = 39A		38	49	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10V		11		nC
Q _{gd}	Gate-Drain Charge	(Note 4)		16.5		nC
Drain-Sour	rce Diode Characteristics and Maximur	n Ratings		I.		
I _S	S Maximum Continuous Drain-Source Diode Forward Current				39	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				156	А
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 39A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 39A		152		ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt =100A/μs		1.1		μC

NOTES:

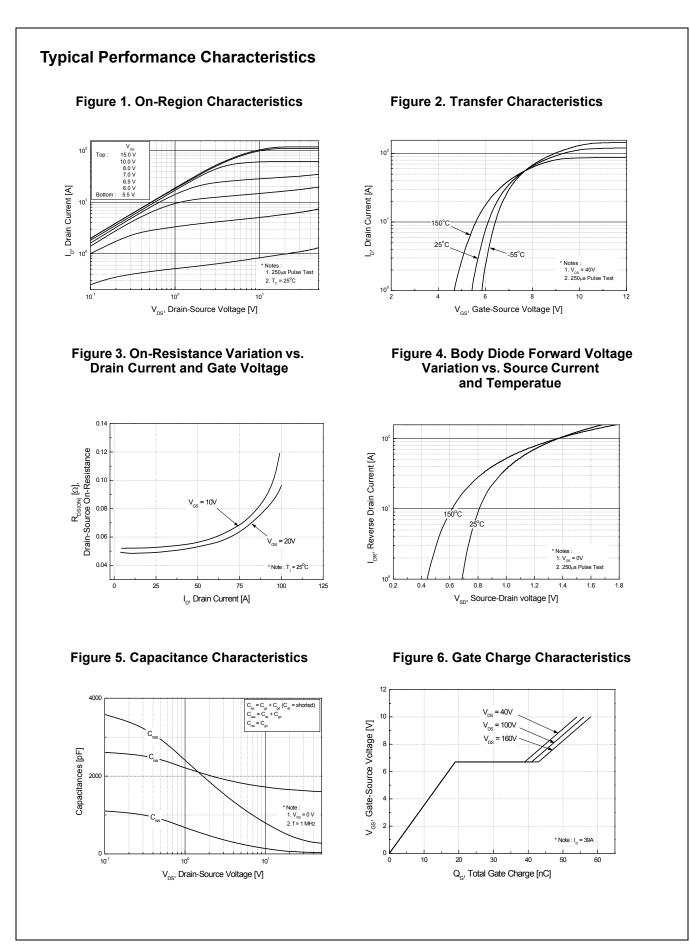
1. Repetitive Rating: Pulse width limited by maximum junction temperature

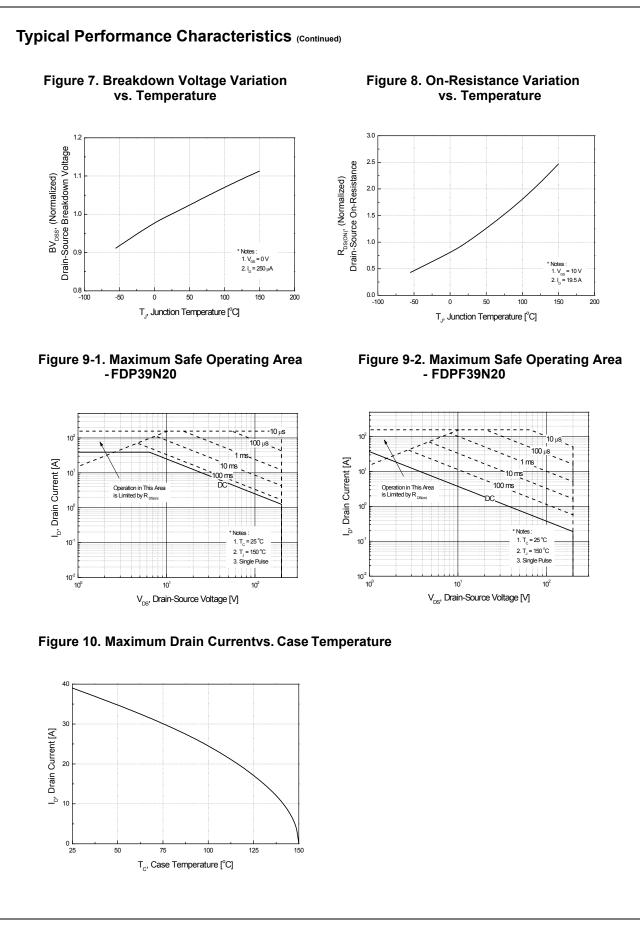
2. L = 0.85mH, I_{AS} = 39A, V_DD = 50V, R_G = 25 Ω , Starting T_J = 25°C

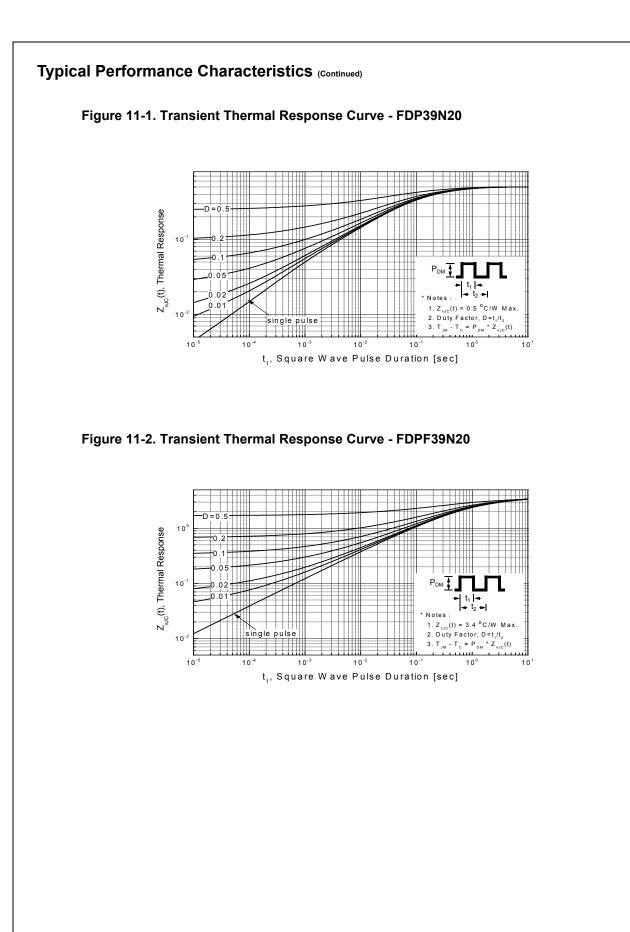
3. I_{SD} \leq 39A, di/dt \leq 200A/µs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C

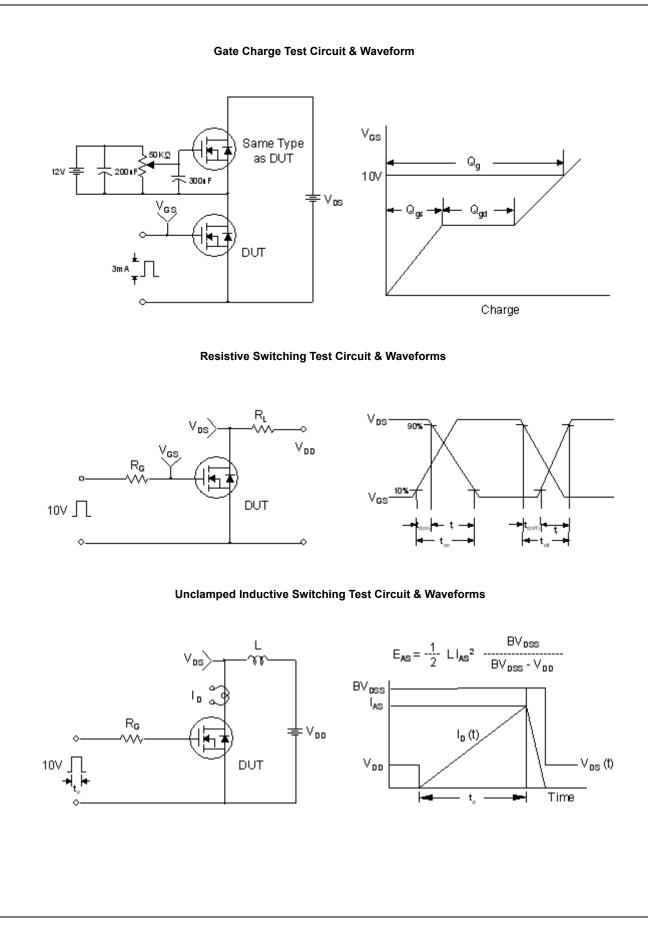
4. Essentially Independent of Operating Temperature Typical Characteristics





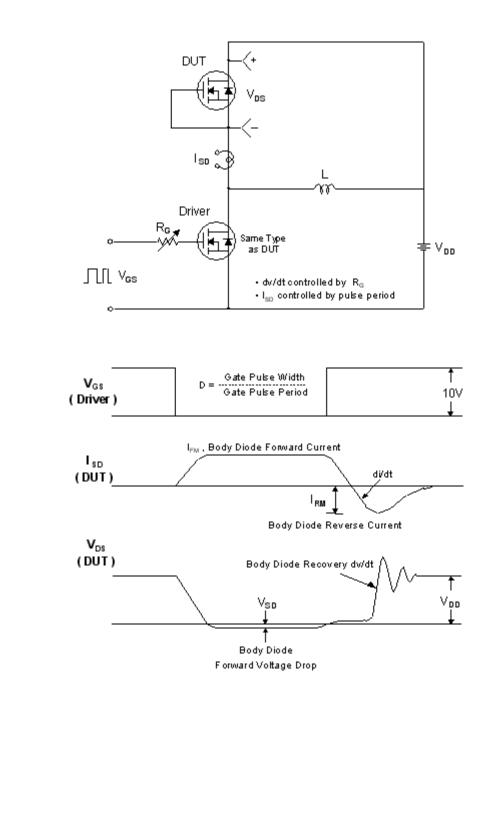


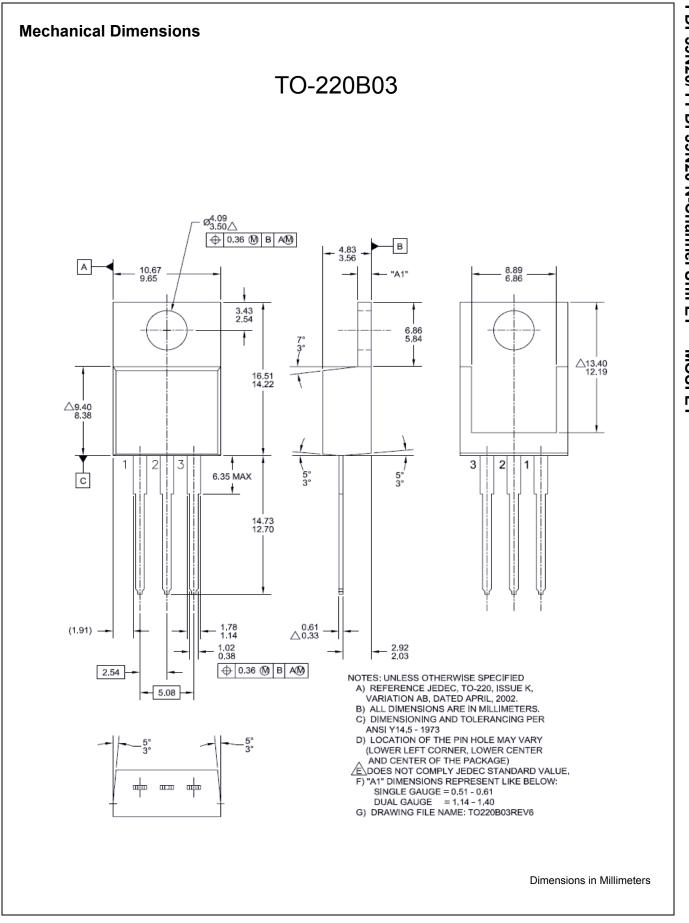


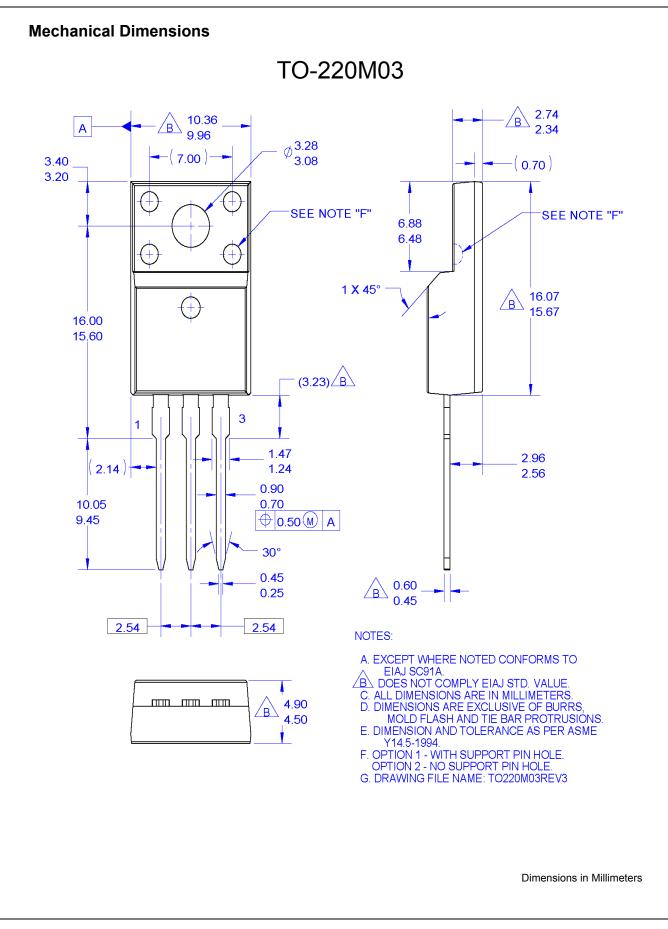


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Peak Diode Recovery dv/dt Test Circuit & Waveforms









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