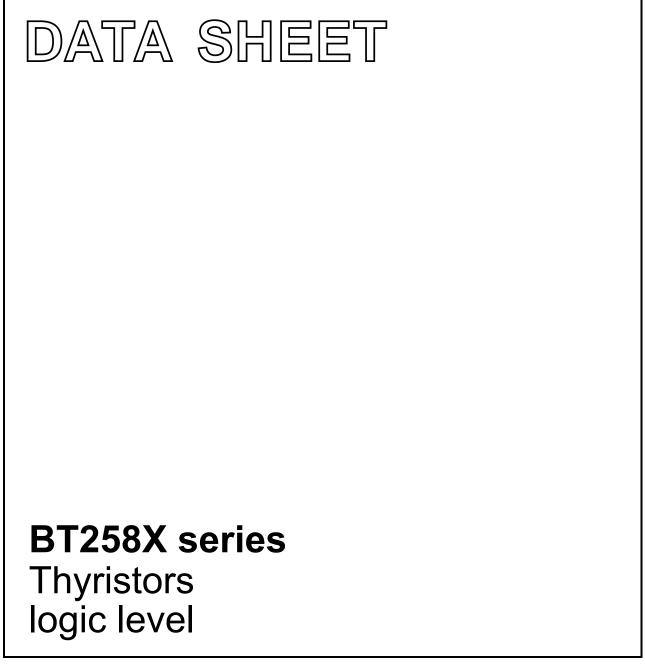
DISCRETE SEMICONDUCTORS



Product specification

October 2002



Thyristors logic level

GENERAL DESCRIPTION

Passivated, sensitive gate thyristors in a full pack, plastic envelope, intended for use in general purpose switching and phase control applications. These devices are intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

DESCRIPTION

PINNING - SOT186A

cathode

anode

gate

PIN

1

2

3

case

C cas

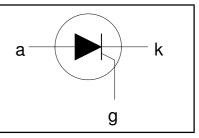
PIN CONFIGURATION

QUICK REFERENCE DATA

BT258X series

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
$\begin{array}{c} V_{\text{DRM}},\\ V_{\text{RRM}}\\ I_{\text{T(AV)}}\\ I_{\text{T(RMS)}}\\ I_{\text{TSM}} \end{array}$	BT258X- Repetitive peak off-state voltages Average on-state current RMS on-state current Non-repetitive peak on-state current	500R 500 5 8 75	600R 600 5 8 75	800R 800 5 8 75	V A A A

SYMBOL



LIMITING VALUES

isolated

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
$V_{\text{drm}}, V_{\text{rrm}}$	Repetitive peak off-state voltages		-	-500R 500 ¹	-600R 600 ¹	-800R 800	×
I _{T(AV)} I _{T(RMS)} I _{TSM}	Average on-state current RMS on-state current Non-repetitive peak on-state current	half sine wave; $T_{hs} \le 90$ °C all conduction angles half sine wave; $T_j = 25$ °C prior to surge	-		5 8		A A
l²t dl _⊤ /dt	I ² t for fusing Repetitive rate of rise of on-state current after	$\begin{array}{l} t = 10 \text{ ms} \\ t = 8.3 \text{ ms} \\ t = 10 \text{ ms} \\ I_{TM} = 10 \text{ A}; I_G = 50 \text{ mA}; \\ dI_G/dt = 50 \text{ mA}/\mu s \end{array}$	- - -		75 82 28 50		A A A²s A/µs
$\begin{matrix} I_{GM} \\ V_{RGM} \\ P_{GM} \\ P_{G(AV)} \\ T_{stg} \\ T_{j} \end{matrix}$	triggering Peak gate current Peak reverse gate voltage Peak gate power Average gate power Storage temperature Operating junction temperature	over any 20 ms period	- - -40 -		2 5 0.5 150 125 ²		o°o°≤≤<

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 A/ μ s.

² Note: Operation above 110°C may require the use of a gate to cathode resistor of $1k\Omega$ or less.

Product specification

Thyristors logic level

BT258X series

ISOLATION LIMITING VALUE & CHARACTERISTIC

 T_{hs} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{isol}	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65% ; clean and dustfree	-	-	2500	V
C _{isol}	Capacitance from T2 to external heatsink	f = 1 MHz	-	10	-	pF

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-hs} R _{th j-a}	Thermal resistance junction to heatsink Thermal resistance junction to ambient	with heatsink compound without heatsink compound in free air	-	- - 55	5.0 6.9 -	K/W K/W K/W

STATIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	-	50	200	μA
	Latching current	$V_{\rm D}^{\rm B} = 12 \text{ V}; \text{ I}_{\rm GT} = 0.1 \text{ A}$	-	0.4	10	mΑ
I I _H	Holding current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm GT} = 0.1 \text{ A}$	-	0.3	6	mA
İΫ _T	On-state voltage	$I_{T} = 16 A$	-	1.3	1.6	V
V _{GT}	Gate trigger voltage	$\dot{V}_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	-	0.4	1.5	V
		$V_{\rm D} = V_{\rm DBM(max)}; I_{\rm T} = 0.1 \text{ A}; T_{\rm i} = 110 \ ^{\circ}\text{C}$	0.1	0.2	-	V
I _D , I _R	Off-state leakage current	$V_D^{D} = V_{DRM(max)}^{DRM(max)}; V_R = V_{RRM(max)}; T_j = 125 \text{°C}$	-	0.1	0.5	mA

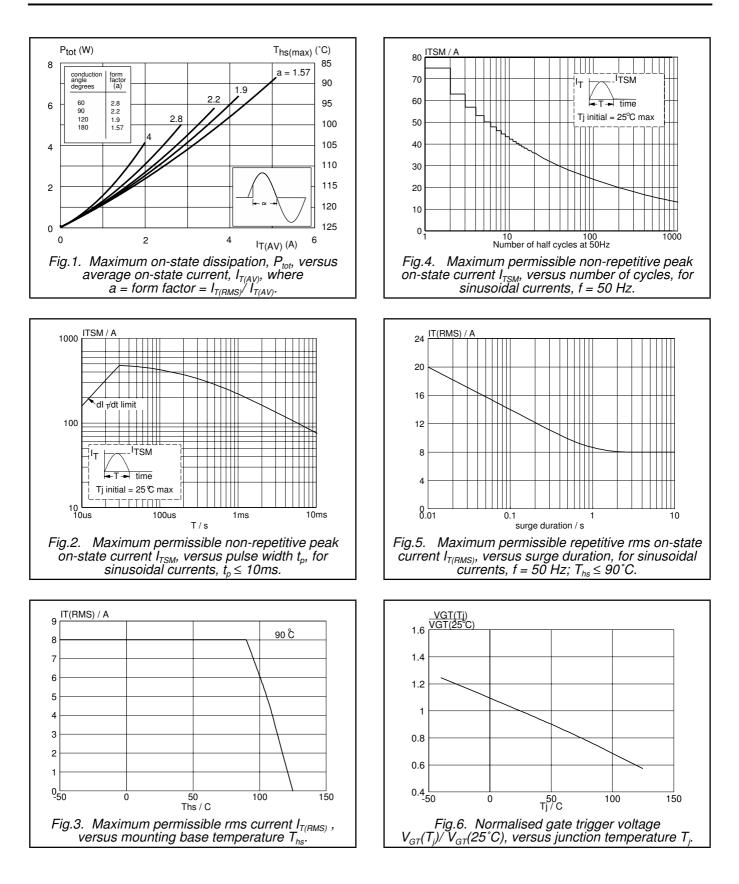
DYNAMIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$ exponential waveform; $R_{GK} = 100 \Omega$	50	100	-	V/µs
t _{gt}	Gate controlled turn-on	$I_{TM} = 10 \text{ A}; V_D = V_{DRM(max)}; I_G = 5 \text{ mA};$ $dI_G/dt = 0.2 \text{ A}/\mu\text{s}$	-	2	-	μs
t _q	Circuit commutated turn-off time		-	100	-	μs

BT258X series

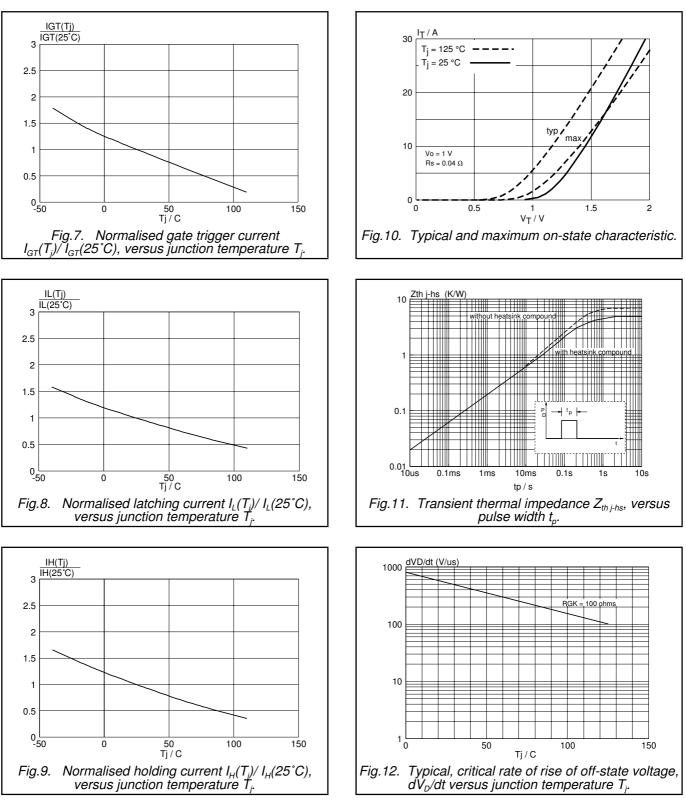
Thyristors logic level



Product specification

BT258X series

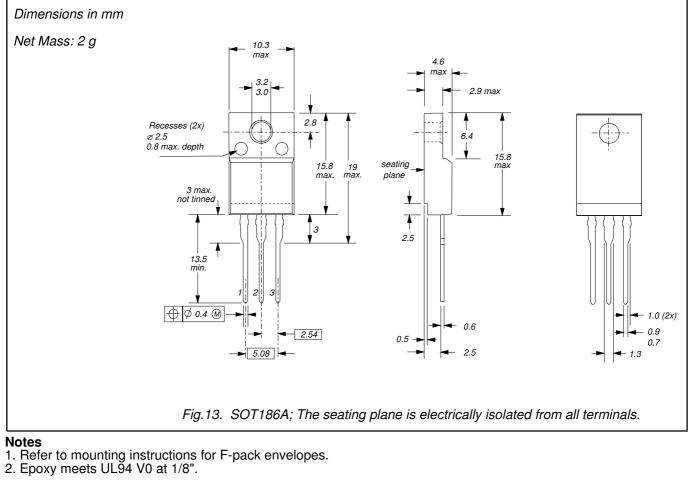
Thyristors logic level



Thyristors logic level

BT258X series

MECHANICAL DATA



Legal information

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Contact information

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