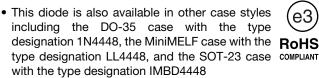


# **Small Signal Fast Switching Diode**



### **FEATURES**

- · Silicon epitaxial planar diode
- · Fast switching diode





- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **MECHANICAL DATA**

Case: SOD-123

Weight: approx. 10.3 mg Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS	
1N4448W-V	1N4448W-V-GS18 or 1N4448W-V-GS08	A3	Single diode	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	V	
Repetitive peak reverse voltage		$V_{RRM}$	100	V	
Average rectified current half wave rectification with resistive load (1)	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Surge current	t < 1 s and T <sub>j</sub> = 25 °C	I <sub>FSM</sub>	500	mA	
Power dissipation (1)		P <sub>tot</sub>	500	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	350	K/W	
Junction temperature		T <sub>j</sub>	150	°C	
Storage temperature		T <sub>stg</sub>	- 65 to + 150	°C	

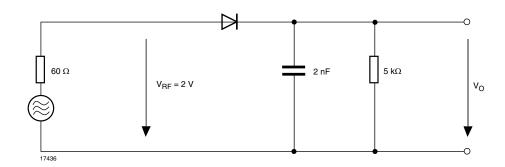
#### Note

<sup>(1)</sup> Valid provided that leads at a distance of 8 mm from case are kept at ambient temperature.

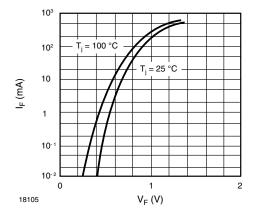


<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 5 mA	V <sub>F</sub>	0.62		0.72	V
Forward voltage	$I_F = 100 \text{ mA}$	V <sub>F</sub>			1	V
	V <sub>R</sub> = 20 V	I <sub>R</sub>			25	nA
Leakage current	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μΑ
	$V_R = 20 \text{ V}, T_J = 150 ^{\circ}\text{C}$	I <sub>R</sub>			50	μΑ
Capacitance	$V_F = V_R = 0 V$				4	pF
Reverse recovery time	$I_F$ = 10 mA, $I_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			4	ns
Rectification efficiency	f = 100 MHz, V <sub>RF</sub> = 2 V	ην	0.45			

#### RECTIFICATION EFFICIENCY MEASUREMENT CIRCUIT



### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)





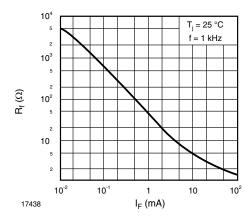


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

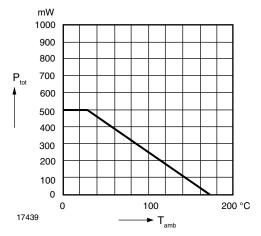


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

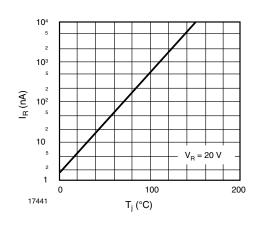


Fig. 5 - Leakage Current vs. Junction Temperature

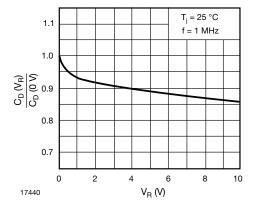


Fig. 4 - Relative Capacitance vs. Reverse Voltage

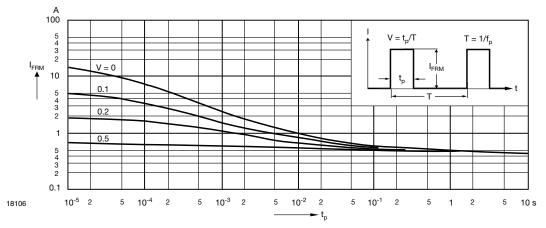
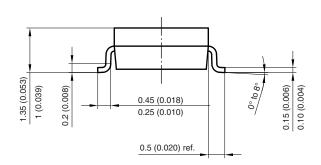
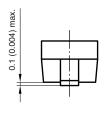


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

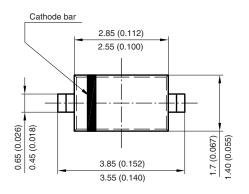


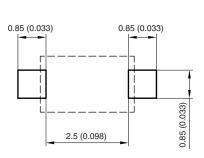
### PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4) 17432



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