



## PRODUCT BULLETIN

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**ISSUE DATE:** 12-Jun-2013  
**NOTIFICATION:** 15655  
**TITLE:** I.MX 6DUALLITE6SOLO DOCUMENTATION UPDATE  
**EFFECTIVE DATE:** 13-Jun-2013

### **DEVICE(S)**

MPN
MCIMX6S1AVM08AB
MCIMX6S1AVM08ABR
MCIMX6S4AVM08AB
MCIMX6S4AVM08ABR
MCIMX6S5DVM10AB
MCIMX6S5DVM10ABR
MCIMX6S5EVM10AB
MCIMX6S5EVM10ABR
MCIMX6S6AVM08AB
MCIMX6S6AVM08ABR
MCIMX6S7CVM08AB
MCIMX6S8DVM10AB
MCIMX6U1AVM08AB
MCIMX6U1AVM08ABR
MCIMX6U4AVM08AB
MCIMX6U4AVM08ABR
MCIMX6U5DVM10AB
MCIMX6U5DVM10ABR
MCIMX6U5EVM10AB
MCIMX6U5EVM10ABR
MCIMX6U6AVM08AB
MCIMX6U6AVM08ABR
MCIMX6U7CVM08AB
MCIMX6U8DVM10AB

### **AFFECTED CHANGE CATEGORIES**

## • DEVICE SPECIFICATION / ERRATA

### **DESCRIPTION OF CHANGE**

#### Datasheets:

The datasheets have been updated to revision 2.1 for the i.MX 6DualLite/6Solo product lines.

The updated automotive grade datasheet can be found at: [IMX6SDLAEC](#)

The updated commercial/extended commercial grade datasheet can be found at: [IMX6SDLCEC](#)

The updated industrial grade datasheet can be found at: [IMX6SDLIEC](#)

The following substantive changes apply to all temperature grade datasheets:

- Incorporated standardized signal names. This change is extensive throughout.
  - Added reference to EB792, i.MX Signal Name Mapping.
  - Figures updated to align to standardized signal names.
  - Updated references to eMMC standard to include 4.41.
  - Figure 1 "Part Number Nomenclature": Updates to Part differentiator section to align with Table 1.
  - Table 1 "Orderable Part Numbers": Options column Added ARM core information, 2x "ARM Cortex-A9" 64-bit for 6DualLite; Added 1x "ARM Cortex-A9" 32-bit to 6Solo. Removed "no EPD".
  - Table 3 "i.MX 6Solo/6DualLite Modules List": Changed reference to Global Power Controller to read General Power Controller.
  - Table 14 "Operating Ranges": Added reference for information on product lifetime : i.MX 6DualLite/6Solo Product Usage Lifetime Estimates Application Note, AN4725.
  - Table 17 "Maximum Supply Currents," updated footnote 2.
  - Table 18 "Stop Mode Current and Power Consumption": Added SNVS Only mode
  - Table 90 RGMII parameter TskewT minimum and maximum values corrected.
  - Table 90 RGMII parameter TskewR units corrected.
  - Table 135 Clarification of ENET\_REF\_CLK naming.
  - Added Table 136, "Signals with Differing Before Reset and After Reset States," on page 227.
  - Removed section, EIM Signal Cross Reference. Signal names are now aligned with reference manual.
  - Removed table from Section 3.2, "Recommended Connections for Unused Analog Interfaces" and referenced the Hardware Development Guide.
  - Section 1.2, "Features": Added bulleted item regarding the SOC-level memory system.
  - Section 1.2, "Features": Camera sensors: Corrected Camera ports to be up to 180 MHz peak.
  - Added Section 1.3, "Updated Signal Naming Convention"
  - Section 4.2.1, "Power-Up Sequence": updated wording.
  - Section 4.3.2, "Regulators for Analog Modules": section updates.
  - Added Section 4.6.1, "XTALI and RTC\_XTALI (Clock Inputs) DC Parameters".
  - Section 4.10, "General-Purpose Media Interface (GPMI) Timing": figures replaced, tables revised.
- Refer to the document revision history for detailed information.

The following substantive changes apply to the commercial temperature grade datasheet:

- Added MediaLB (MLB) feature and DTCP module to the Commercial temperature grade version.
- Table 1 "Orderable Part Numbers": Added MLB as a Commercial option.

#### Reference Manual:

The reference manual has been updated to revision 1 for the i.MX 6DualLite/6Solo product lines.

The updated reference manual can be found at: [IMX6SDLRM](#)

The following chapters have been updated:

- Architecture Overview
- Memory Maps
- External Signals
- Fusemap
- System Boot
- Multimedia
- System Security
- CCM
- CSI2/IPU
- IOMUXC
- IPU
- MMDC
- SRC
- UART
- USB
- SDMA Scripts

Refer to the document revision history for detailed information.

Silicon Errata:

The silicon errata document has been updated to revision 2 for the i.MX 6DualLite/6Solo product lines.

The updated errata can be found at: [IMX6SDLCE](#)

Substantive changes in the errata are as follows:

- Deleted the following errata:

- ERR004353 “CAAM: SMC deallocates partition when zeroizing CSP pages in Fail State”

- Added the following errata:

- ERR006308 “USB: Host non-double word –aligned buffer address can cause host to hang on OUT Retry”

- ERR006358 “ENET: Write to Transmit Descriptor Active Register (ENET\_TDAR) is ignored”

- ERR006687 “ENET: Only the ENET wake-up interrupt request can wake the system from Wait mode”

- Updated the following:

- ERR004446 “EIM: AUS mode is nonfunctional for devices larger than 32 MB”

- ERR005829 “FlexCAN: FlexCAN does not transmit a message that is enabled to be transmitted in a specific moment during the arbitration process”

Refer to the document revision history for detailed information.

## **REASON FOR CHANGE**

Updated documentation

## **ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)**

No changes to form, fit, function or reliability

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### **NOTE:**

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

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**QUAL DATA AVAILABILITY DATE:** 03-May-2013

**QUALIFICATION STATUS:** COMPLETED

**QUALIFICATION PLAN:**

N/A

**RELIABILITY DATA SUMMARY:**

N/A

**ELECTRICAL CHARACTERISTIC SUMMARY:**

N/A

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**CHANGED PART IDENTIFICATION:**

N/A

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**ATTACHMENT(S):**

External attachment(s) FOR this notification can be viewed AT:

[15655\\_IMX6SDLCE.pdf](#)

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