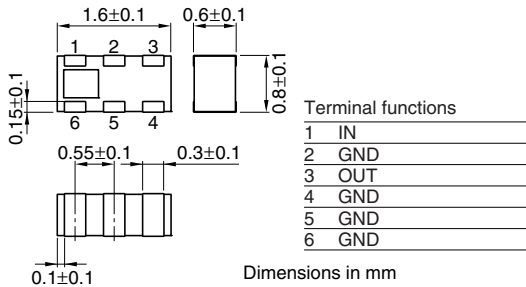


Multilayer Chip Low Pass Filters For DCS/PCS

Conformity to RoHS Directive

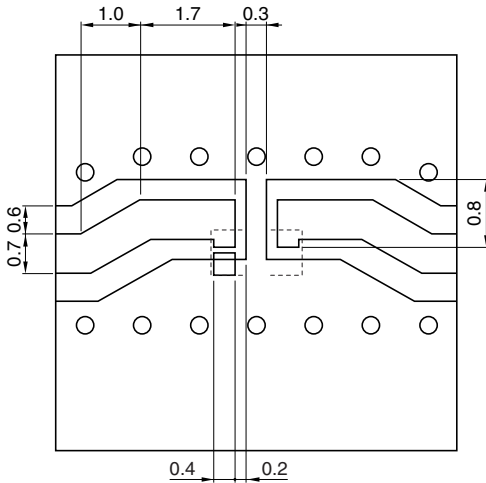
DEA Series DEA161910LT-5003C1

SHAPES AND DIMENSIONS



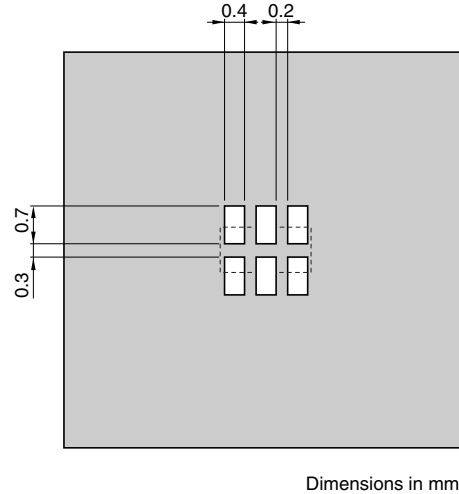
RECOMMENDED PC BOARD PATTERNS

LAND



Line width be designed to match 50Ω characteristic impedance depending on PCB material and thickness.

SOLDER RESIST



ELECTRICAL CHARACTERISTICS

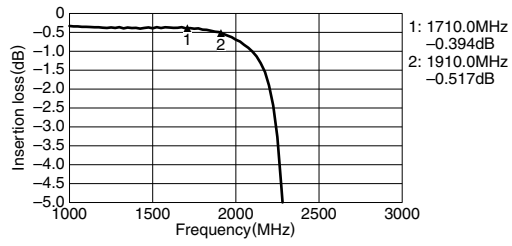
Item			Minimum value	Typical value	Maximum value
Insertion loss	[at RT]	[1710 to 1910MHz]	(dB)	0.52	0.8
	[-30 to +80°C]		(dB)	—	1.0
Attenuation	[at RT]	[2400 to 2480MHz]	(dB)	15	21
	[-30 to +80°C]		(dB)	15	—
	[at RT]	[3700 to 3820MHz]	(dB)	10	12
	[-30 to +80°C]		(dB)	9	—
Input VSWR	[at RT]	[1710 to 1910MHz]	(dB)	—	1.3
	[-30 to +80°C]		(dB)	—	1.9
Output VSWR	[at RT]	[1710 to 1910MHz]	(dB)	—	1.3
	[-30 to +80°C]		(dB)	—	1.9
Input impedance	[-30 to +80°C]	[1710 to 1910MHz]	(Ω)	50+j0	
Temperature range	Operating		(°C)	-40	+85
	Storage		(°C)	-40	+80

• Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.

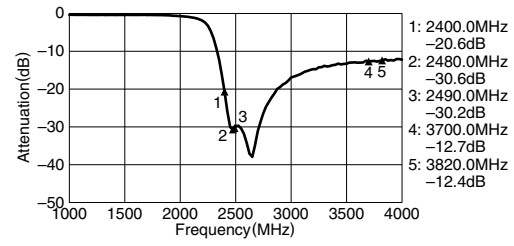
• All specifications are subject to change without notice.

FREQUENCY CHARACTERISTICS

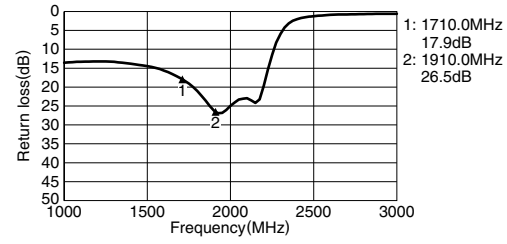
INSERTION LOSS



ATTENUATION



RETURN LOSS



VSWR

