

VS-25TTS..FPPbF Series, VS-25TTS..FP-M3 Series

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Thyristor High Voltage, Phase Control SCR, 25 A



PRODUCT SUMMARY				
Package	TO-220AB FP			
Diode variation	Single SCR			
I _{T(AV)}	16 A			
V_{DRM}/V_{RRM}	800 V, 1200 V			
V_{TM}	1.25 V			
I _{GT}	45 mA			
T_J	- 40 °C to 125 °C			

FEATURES

- · Designed and qualified for industrial level
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 pending
- 125 °C max. operating junction temperature
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





FREE

APPLICATIONS

 Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding, and battery charge

DESCRIPTION

The VS-25TTS...FP... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	18	22	А		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	16	۸		
I _{RMS}		25	Α		
V _{RRM} /V _{DRM}		800/1200	V		
I _{TSM}		350	Α		
V _T	16 A, T _J = 25 °C	1.25	V		
dV/dt		500	V/µs		
dI/dt		150	A/μs		
T _J		- 40 to 125	°C		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
VS-25TTS08FPPbF, VS-25TTS08FP-M3	800	800	10			
VS-25TTS12FPPbF, VS-25TTS12FP-M3	1200	1200	10			



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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL		TEST CONDITIONS		VALUES	
	STIVIBOL				MAX.	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 51 °C,	80° conduction half sine wave	1	6	
Maximum RMS on-state current	I _{RMS}			2	5	Α
Maximum peak, one-cycle,	L	10 ms sine p	ulse, rated V _{RRM} applied	30	00	A .
non-repetitive surge current	I _{TSM}	10 ms sine p	ulse, no voltage reapplied	3	50	
Maximum I ² t for fusing	l ² t	10 ms sine p	ulse, rated V _{RRM} applied	4	50	A ² s
Maximum i-t for fusing	1-1	10 ms sine p	10 ms sine pulse, no voltage reapplied		30	A-5
Maximum $I^2\sqrt{t}$ for fusing	I ² √t	t = 0.1ms to	10 ms, no voltage reapplied	63	00	A²√s
Maximum on-state voltage drop	V_{TM}	16 A, T _J = 25 °C		1.	25	V
On-state slope resistance	r _t	T _{.l} = 125 °C		12	2.0	mΩ
Threshold voltage	V _{T(TO)}	1]=125 0		1	.0	V
Maximum reverse and direct leakage current	1 /1	T _J = 25 °C	V - Potod V AV	0	.5	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	T _J = 125 °C	$V_R = Rated V_{RRM}/V_{DRM}$	1	0	
Holding current	I _H	Anode supply = 6 V, resistive load, initial I_T = 1 A, T_J = 25 °C		150	mA	
Maximum latching current	IL	Anode supply = 6 V, resistive load, T _J = 25 °C 200		00		
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J \text{ max., linear to } 80 \text{ %, } V_{DRM} = R_g - k = Open$ 500		00	V/µs	
Maximum rate of rise of turned-on current	dl/dt	150		50	A/µs	

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P_{GM}		8.0	W	
Maximum average gate power	P _{G(AV)}		2.0] vv	
Maximum peak positive gate current	+ I _{GM}		1.5	Α	
Maximum peak negative gate voltage	- V _{GM}		10	V	
		Anode supply = 6 V, resistive load, T _J = -10 °C	60		
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T _J = 25 °C	45	mA	
		Anode supply = 6 V, resistive load, T _J = 125 °C	20		
		Anode supply = 6 V, resistive load, T _J = - 10 °C	2.5		
Maximum required DC gate voltage to trigger	V_{GT} Anode supply = 6 V, resistive load, $T_J = 25$ °C Anode supply = 6 V, resistive load, $T_J = 125$ °C	Anode supply = 6 V, resistive load, T _J = 25 °C	2.0	- V	
voltage to ingger		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0]	
Maximum DC gate voltage not to trigger	V_{GD}	T 405 00 V Bullet at a	0.25		
Maximum DC gate current not to trigger	I _{GD}	T _J = 125 °C, V _{DRM} = Rated value		mA	

SWITCHING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9		
Typical reverse recovery time	t _{rr}	T _J = 125 °C	4	μs	
Typical turn-off time	t _q	1J = 120 0	110		



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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J , T_{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	2.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf ⋅ in)
Marking device			Case style TO-220AB FULL-PAK (94/V0)	25TTS0	BFP
				25TTS12	2FP

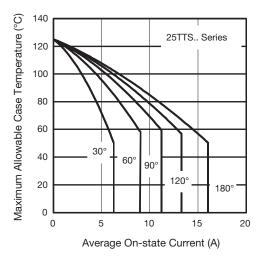


Fig. 1 - Current Rating Characteristics

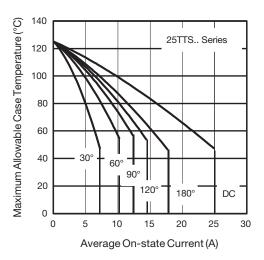


Fig. 2 - Current Rating Characteristics

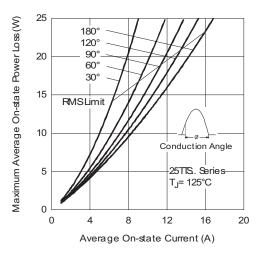


Fig. 3 - On-State Power Loss Characteristics

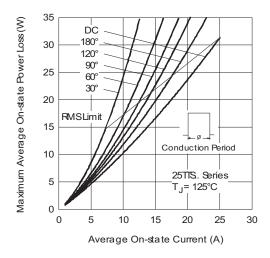


Fig. 4 - On-State Power Loss Characteristics

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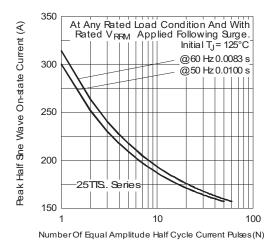


Fig. 5 - Maximum Non-Repetitive Surge Current

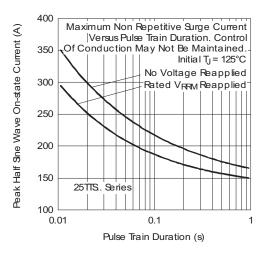


Fig. 6 - Maximum Non-Repetitive Surge Current

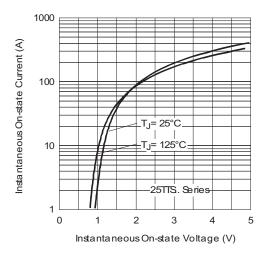


Fig. 7 - On-State Voltage Drop Characteristics

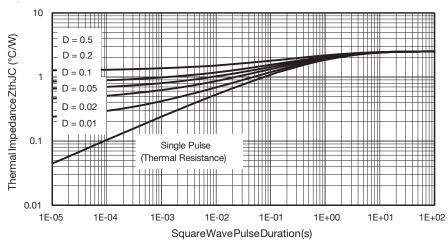


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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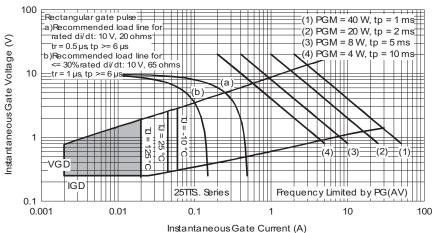
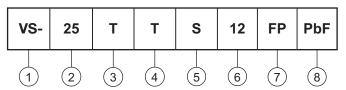


Fig. 9 - Gate Characteristics

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (25 = 25 A)
- 3 Circuit configuration:

T = Single thyristor

4 - Package:

T = TO-220AB

5 - Type of silicon:

Standard recovery rectifier

7 - FULL-PAK

8 - Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

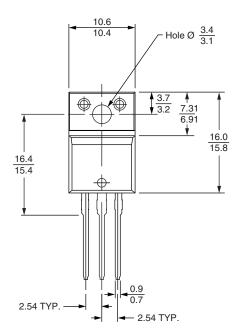
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-25TTS08FPPbF	50	1000	Antistatic plastic tubes		
VS-25TTS08FP-M3	50	1000	Antistatic plastic tubes		
VS-25TTS12FPPbF	50	1000	Antistatic plastic tubes		
VS-25TTS12FP-M3	50	1000	Antistatic plastic tubes		

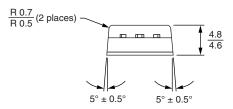
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95072</u>				
Dort marking information	TO-220FP PbF	www.vishay.com/doc?95069		
Part marking information	TO-220FP -M3	www.vishay.com/doc?95456		

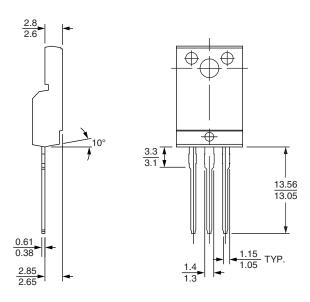


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DIMENSIONS in millimeters







Lead assignments

Diodes

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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